

COMMUNICATIONS SYSTEM

ABSTRACT OF THE DISCLOSURE

To simplify the manufacture of terminals for the use of chip cards, the system is designed so as to place a minimum set of instructions in these terminals and to transfer the storage of complementary instructions pertaining to uses of greater complexity and/or pertaining to the chip card into the chip card. To bring about the performance of these complementary instructions, an address designating an instruction of a complementary set is stored in a reserved memory of the chip card. The microprocessor of the chip card is furthermore provided with a microprogram enforcing the performance of an instruction thus designated as soon as it is detected that the reserved memory has been designated by an instruction of an elementary set of instructions of this microprocessor. It is shown that it is possible, in this way, to simplify and rationalize the manufacture of very powerful terminals.

(Figure 2)

This Sheet Has Been Replaced



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