

WHAT IS CLAIMED IS:

1. An apparatus comprising:
 - a substrate;
 - a first inductor integrated into the substrate, the first inductor including a plurality of first inductor windings in a first metal layer and a second metal layer;
 - a second inductor integrated into the substrate, the second inductor including a first spiral in a third metal layer, the first spiral located at least partially inside the plurality of first inductor windings, wherein the second inductor is perpendicular to the first inductor.
2. The apparatus of claim 1, wherein the third metal layer is located between the first metal layer and the second metal layer.
3. The apparatus of claim 1, wherein the first inductor and second inductor are not electrically coupled together.
4. The apparatus of claim 1, further comprising:
 - wherein the first inductor is configured to generate a first magnetic field in a first direction;
 - wherein the second inductor is configured to generate a second magnetic field in a second direction, wherein the second direction is different from the first direction.
5. The apparatus of claim 4, wherein the second magnetic field is generated from the second inductor.
6. The apparatus of claim 5, wherein the first magnetic field is generated from the plurality of first inductor windings.
7. The apparatus of claim 6, wherein the first direction of the first magnetic field and the second direction of the second magnetic field are perpendicular to each other.
8. The apparatus of claim 1, further comprising:

wherein the first inductor includes a plurality of first interconnects and a plurality of second interconnects for coupling the first inductor windings in the first metal layer to the first inductor windings in the second metal layer;

wherein the second inductor includes a third side and a fourth side; and

wherein the third side and fourth side of the second inductor are bound by the plurality of first interconnects and the plurality of second interconnects of the first inductor.

9. The apparatus of claim 8, wherein a first side and a second side of the second inductor are not bounded by the first inductor.

10. The apparatus of claim 8, wherein the first inductor windings include a plurality of first segments and a plurality of second segments, the first spiral located between the plurality of first segments and the plurality of second segments.

11. The apparatus of claim 8, wherein a first side of the second inductor is perpendicular to the plurality of first interconnects and the plurality of second interconnects.

12. The apparatus of claim 10, further comprising:

wherein the second inductor includes a second spiral on a fourth metal layer and a third spiral on a fifth metal layer, the second spiral and the third spiral located between the plurality of first segments and the plurality of second segments of the first inductor.

13. The apparatus of claim 12, wherein the fourth metal layer and the fifth metal layer are located between the first metal layer and the second metal layer.

14. The apparatus of claim 12, wherein a plurality of third interconnects is coupled to the first spiral, the first spiral is coupled to a plurality of fifth interconnects, the plurality of fifth interconnects couples the first spiral to the second spiral, the second spiral is coupled to the plurality of fourth interconnects, the plurality of fourth interconnects couples the second spiral to the third spiral, the third spiral is coupled to the plurality of fifth interconnects.

15. The apparatus of claim 12, wherein each one of the plurality of first interconnects and each one of the plurality of second interconnects includes stacked vias and pads, the stacked vias and pads configured to provide space for the first spiral, second spiral, and third spiral to be located between the plurality of first segments and the plurality of second segments.

16. The apparatus of claim 1, wherein the substrate including the first inductor and second inductor is part of a duplexer circuit.

17. The apparatus of claim 1, wherein the apparatus is incorporated into a device selected from a group consisting of a music player, a video player, an entertainment unit, a navigation device, a communications device, a mobile device, a mobile phone, a smart phone, a personal digital assistant, a fixed location terminal or server, a tablet computer, a wearable computing device, and a laptop computer.

18. A method of manufacturing a first inductor and second inductor, comprising:
providing a substrate;
plating a third metal layer to form a first spiral of the second inductor;
plating a first metal layer to form a plurality of first segments of the first inductor; and
plating a second metal layer to form a plurality of second segments of the first inductor, the first spiral of the second inductor located between the plurality of first segments and the plurality of second segments of the first inductor; the second inductor perpendicular to the first inductor.

19. The method of claim 18, comprising:
forming a plurality of first interconnects and forming a plurality of second interconnects;
coupling the plurality of first segments to the plurality of second segments with the plurality of first interconnects and the plurality of second interconnects.

20. The method of claim 19, further comprising:

wherein the second inductor includes a third side and a fourth side bound by the plurality of first interconnects and the plurality of second interconnects of the first inductor.

21. The method of claim 20, further comprising:

plating a fourth metal layer to form a second spiral of the second inductor;

plating a fifth metal layer to form a third spiral of the second inductor, the first spiral, the second spiral, and the third spiral formed between the plurality of first segments and the plurality of second segments.

22. The method of claim 21, further comprising:

wherein forming the plurality of first interconnects and the plurality of second interconnects includes forming a plurality of vias and a plurality of pads, the plurality of vias and the plurality of pads forming a stack.