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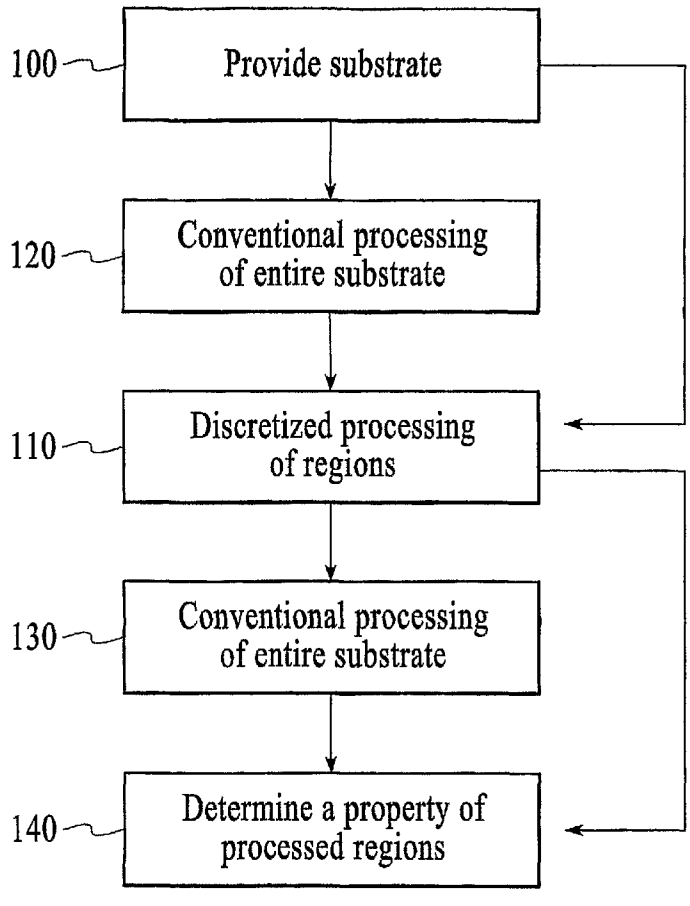
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(54) Title: DISCRETIZED PROCESSING AND PROCESS SEQUENCE INTEGRATION OF SUBSTRATE REGIONS



(57) Abstract: The present invention provides methods and systems for discretized, combinatorial processing of regions of a substrate such as for the discovery, implementation, optimization, and qualification of new materials, processes, and process sequence integration schemes used in integrated circuit fabrication. A substrate having an array of differentially processed regions thereon is processed by delivering materials to or modifying regions of the substrate.

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