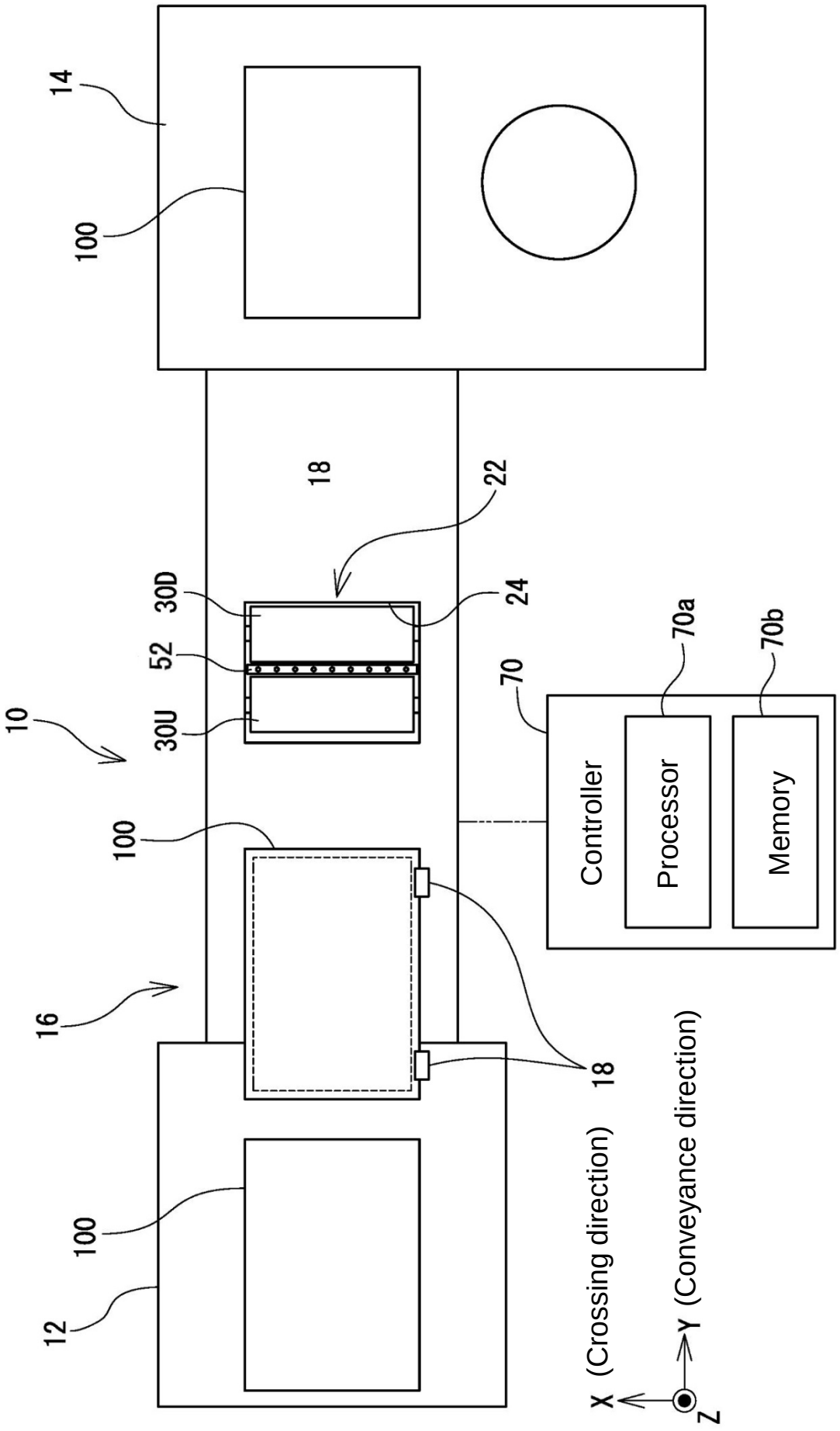

14
100
18
22
30D
24
52
30U
10
70
70a
70b
Controller
Processor
Memory
100
16
18
100
12
X (Crossing direction)
Y (Conveyance direction)
Z

FIG. 1