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(54) Title: METHOD AND APPARATUS FOR VARIABLE BIT RATE CLOCK RECOVERY

(57) Abstract: Methods and apparatuses consistent with the present invention recover a clock signal (160) from a variable bit rate data signal by estimating, in the time domain, the bit rate of the data signal, and based on the estimated variable bit rate, determining a center frequency of a narrow-band filter (330) for extracting the clock signal from the data signal (155). A clock recovery circuit (300) consistent with the present invention extracts a clock signal from a variable bit rate data signal by estimating a minimum time interval between transition (320) in the data signal, generating a plurality of pulses that correspond respectively to transitions in the data signal, adjusting the duration of each of the pulses based on the estimated minimum time interval (310) and inputting into a narrow-band filter (320) the adjusted pulses (RS2), determining the center frequency of the narrow-band filter (320) based on the estimated minimum time interval, and extracting in the narrow-band filter (320) the clock signal (160) from the adjusted pulses.