

ABSTRACT

A vehicle speed signal falsification detection apparatus has a control unit incorporating a computer. The control unit calculates a vehicle speed according to a pulse signal proportional to the rotational speed of a drive wheel and also calculates a simulated vehicle speed according to a pulse signal proportional to the rotational speed of a driven wheel. Then, the control unit determines, on the basis of the comparison between the vehicle speed and the simulated vehicle speed, whether a vehicle speed signal has been falsified. When the control unit determines that the vehicle speed signal has been falsified, then the control unit outputs a falsification detection signal to another control unit so as to suppress the output of an engine.