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(71) Applicant *(for all designated States except US)*: **MICRON TECHNOLOGY, INC.** [US/US]; 8000 Federal Way South, Boise, Idaho 83716-9632 (US).

(71) Applicant and

(72) Inventor: **JEDDELOH, Joseph, M.** [US/US]; 4302 Reiland Lane, Shoreview, MN 55126 (US).

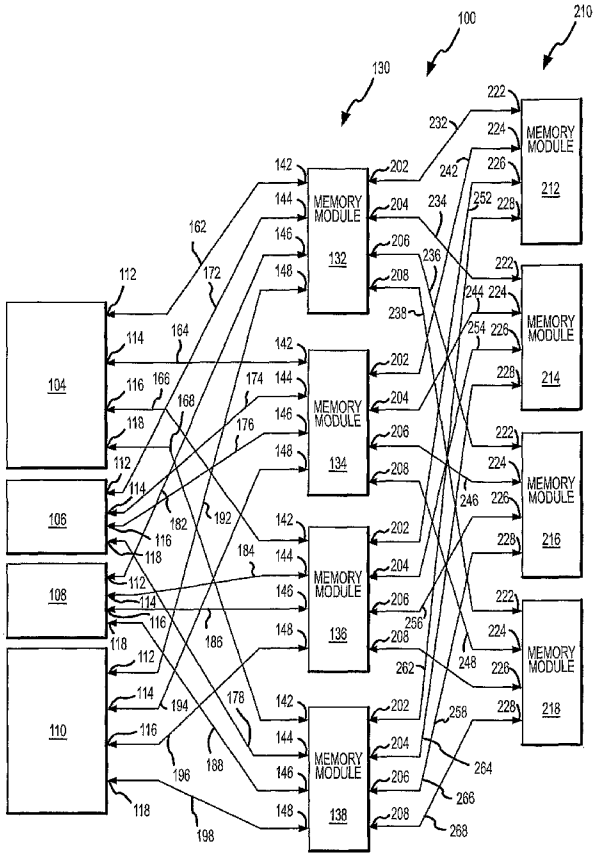
(74) Agents: **BULCHIS, Edward, W.** et al.; DORSEY & WHITNEY LLP, 1420 Fifth Avenue, Suite 3400, Seattle, Washington 98101 (US).

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(54) Title: MULTIPLE PROCESSOR SYSTEM AND METHOD INCLUDING MULTIPLE MEMORY HUB MODULES



(57) Abstract: A processor-based electronic system includes several memory modules arranged in first and second ranks. The memory modules in the first rank are directly accessed by any of several processors, and the memory modules in the second rank are accessed by the processors through the memory modules in the first rank. The data bandwidth between the processors and the memory modules in the second rank is varied by varying the number of memory modules in the first rank that are used to access the memory module in the second set. Each of the memory modules includes several memory devices coupled to a memory hub. The memory hub includes a memory controller coupled to each memory device, a link interface coupled to a respective processor or memory module, and a cross bar switch coupling any of the memory controllers to any of the link interfaces.

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