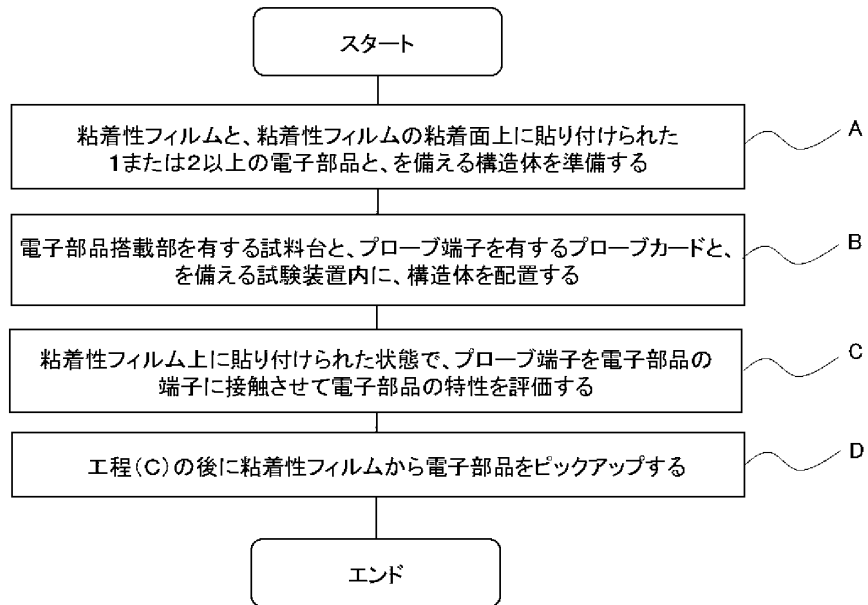
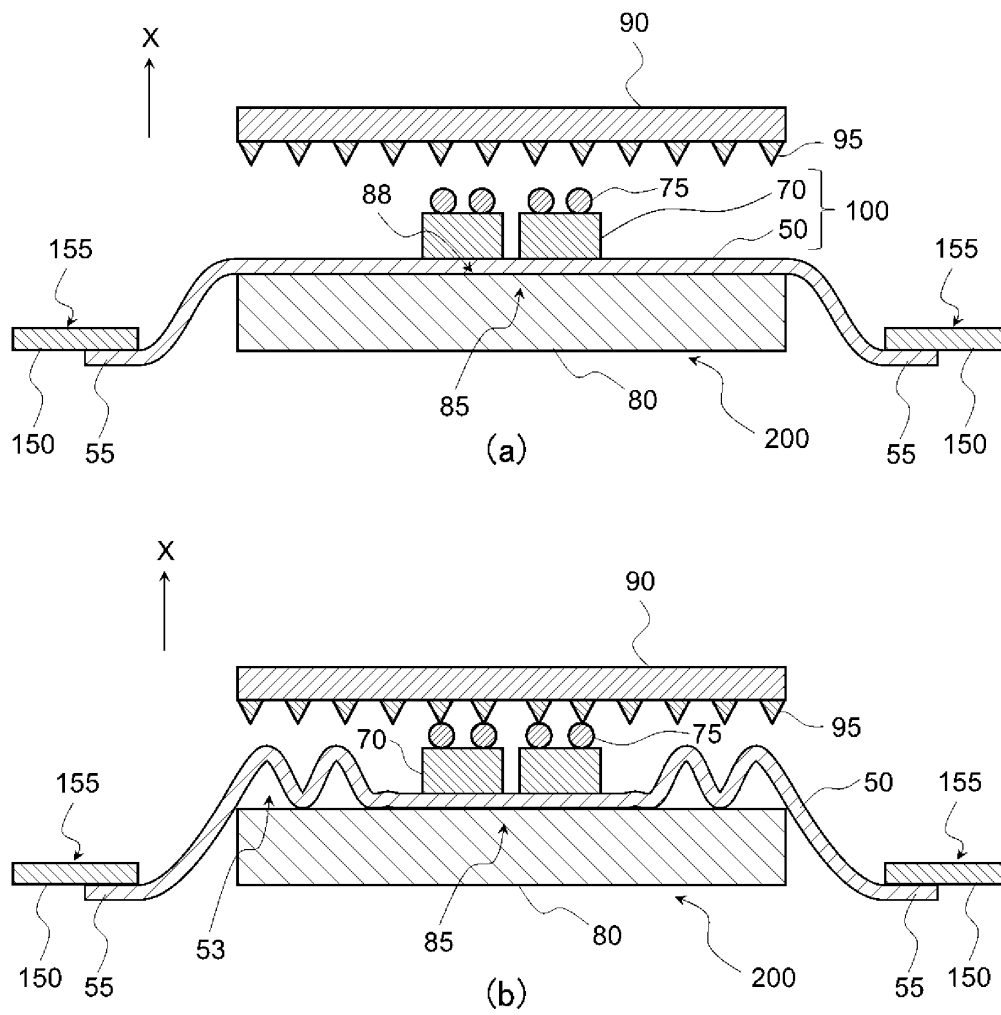


[図1]





[図3]

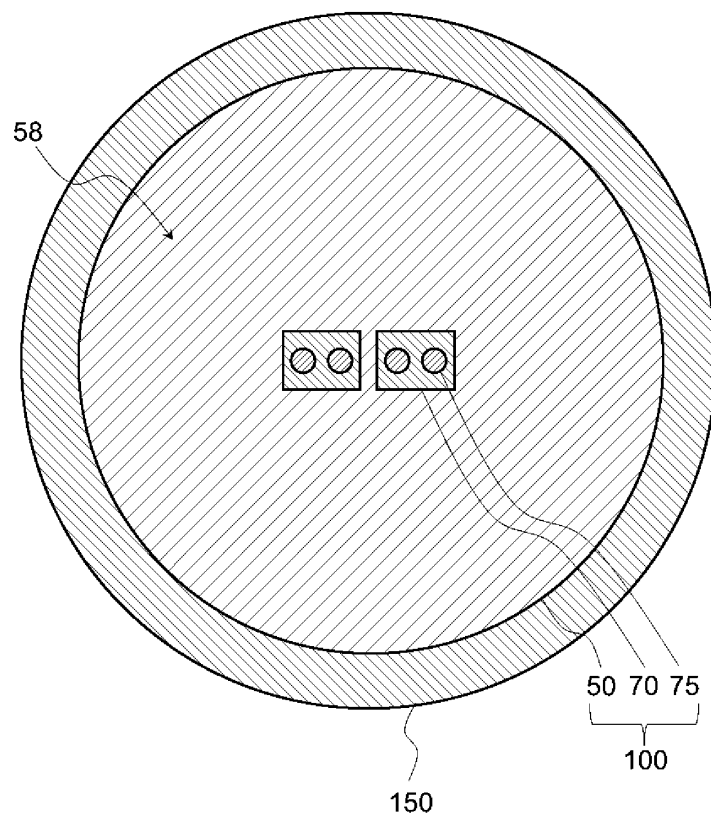
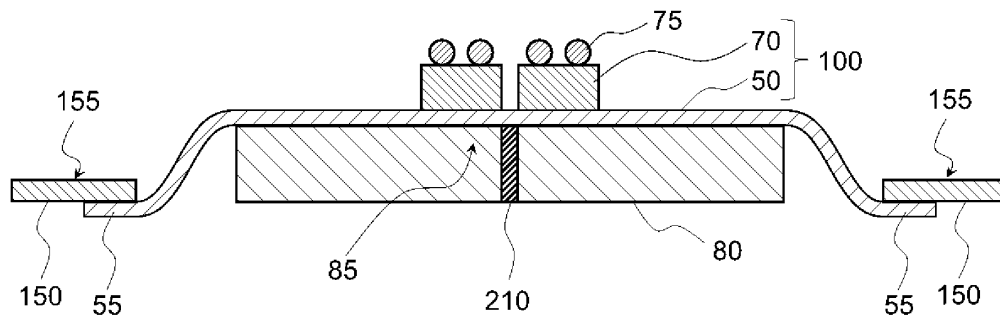
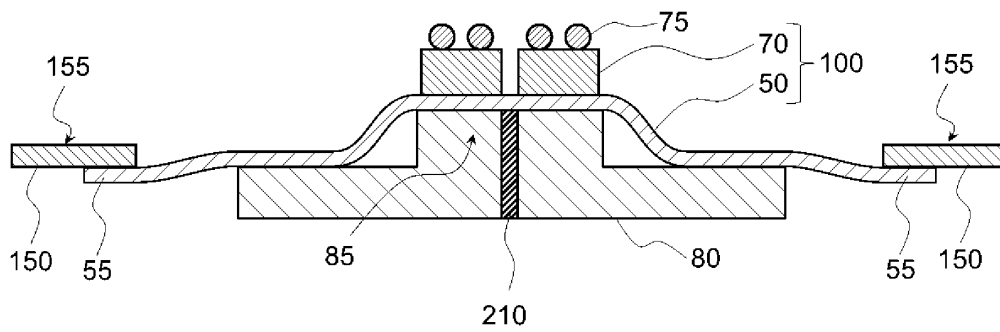


Figure 1 shows two cross-sectional views of a semiconductor device, labeled (a) and (b). Both views show a substrate 200 with a central region 80 and side regions 85. A layer 87 is on top of the central region 80. In view (a), a layer 70 is on top of the central region 80, and a layer 50 is on top of the side regions 85. A layer 155 is on top of the side regions 85. A layer 150 is on top of the side regions 85. A layer 55 is on top of the side regions 85. A layer 90 is on top of the central region 80. A layer 95 is on top of the side regions 85. A layer 75 is on top of the central region 80. A layer 88 is on top of the side regions 85. In view (b), the layer 70 is on top of the central region 80, and the layer 50 is on top of the side regions 85. The layer 155 is on top of the side regions 85. The layer 150 is on top of the side regions 85. The layer 55 is on top of the side regions 85. The layer 90 is on top of the central region 80. The layer 95 is on top of the side regions 85. The layer 75 is on top of the central region 80. The layer 88 is on top of the side regions 85. The layer 53 is on top of the side regions 85. The layer 80 is on top of the central region 80. The layer 85 is on top of the side regions 85. The layer 200 is the substrate. The layer 155 is on top of the side regions 85. The layer 150 is on top of the side regions 85. The layer 55 is on top of the side regions 85. The layer 90 is on top of the central region 80. The layer 95 is on top of the side regions 85. The layer 75 is on top of the central region 80. The layer 88 is on top of the side regions 85. The layer 53 is on top of the side regions 85. The layer 80 is on top of the central region 80. The layer 85 is on top of the side regions 85. The layer 200 is the substrate.

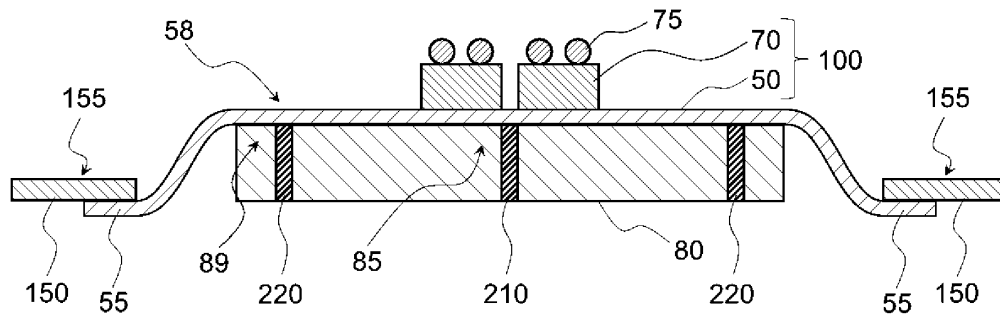
[図5]



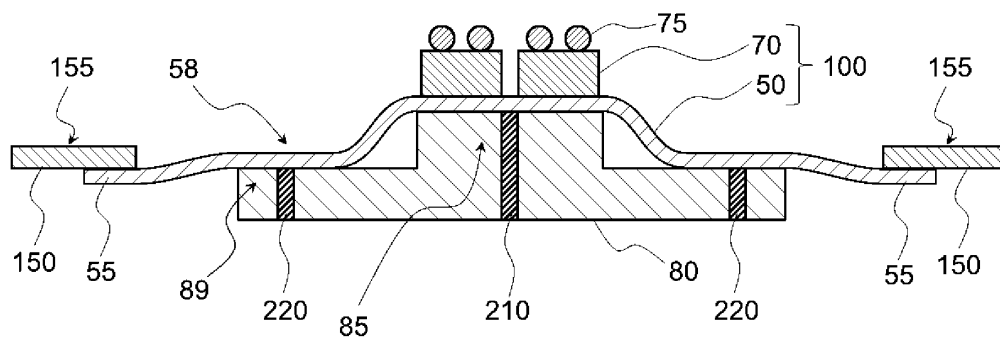
[図6]



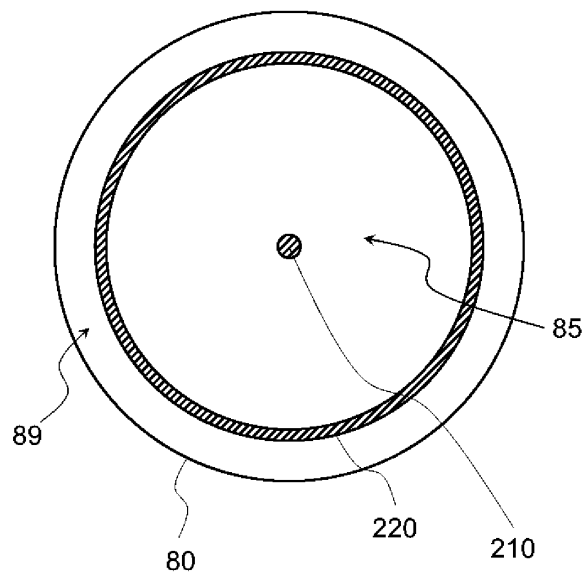
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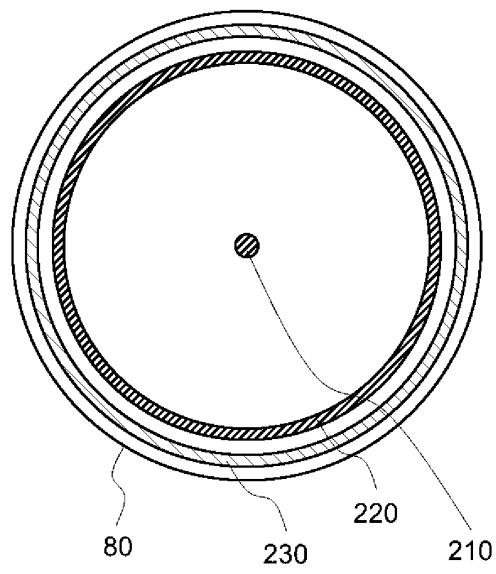
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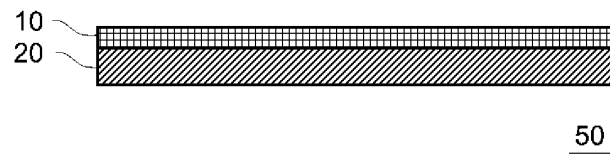
[図9]



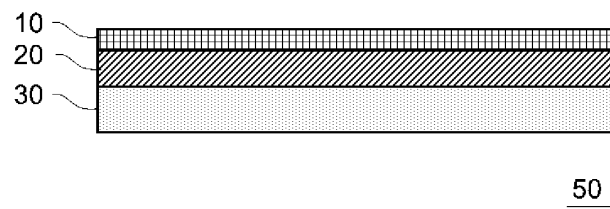
[図10]



[図11]



[図12]



[図13]

