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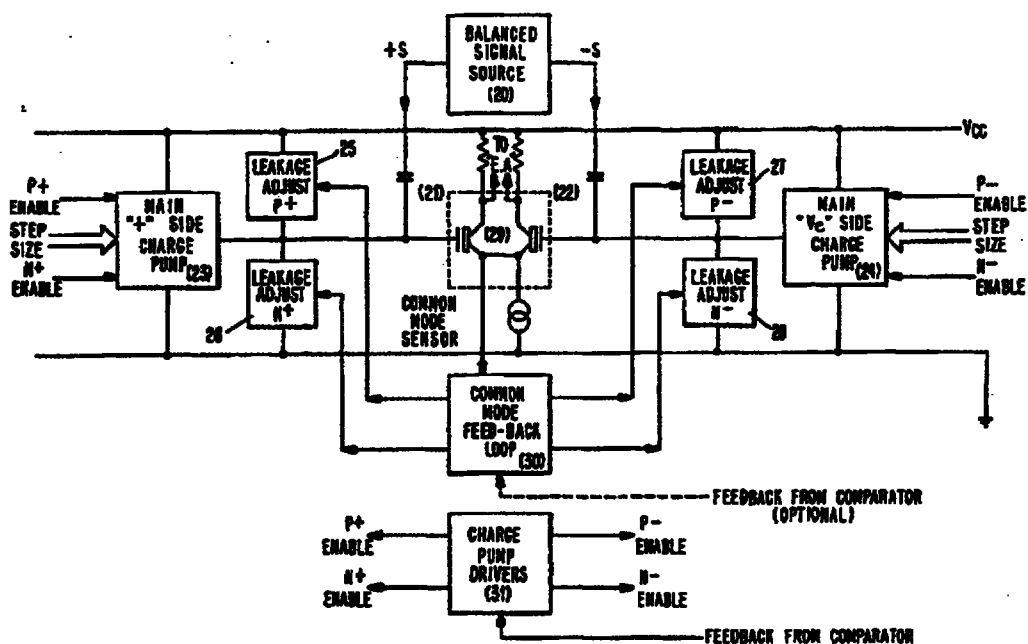
IP05



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : H03M 3/02	A1	(11) International Publication Number: WO 96/28893 (43) International Publication Date: 19 September 1996 (19.09.96)
(21) International Application Number: PCT/US96/02995 (22) International Filing Date: 1 March 1996 (01.03.96) (30) Priority Data: 08/401,124 9 March 1995 (09.03.95) US (71) Applicant: ERICSSON INC. [US/US]; One Triangle Drive, P.O. Box 13969, Research Triangle Park, NC 27709 (US). (72) Inventor: DENT, Paul, W.; Stehags Prastgard, S-240 36 Stehag (SE). (74) Agents: GRUDZIECKI, Ronald, L. et al.; Burns, Doane, Swecker & Mathis, L.L.P., P.O. Box 1404, Alexandria, VA 22313-1404 (US).		(81) Designated States: AL, AM, AT, AU, AZ, BB, BG, BR, BY, CA, CH, CN, CZ, DE, DK, EE, ES, FI, GB, GE, HU, IS, JP, KE, KG, KP, KR, KZ, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, TJ, TM, TR, TT, UA, UG, UZ, VN, ARIPO patent (KE, LS, MW, SD, SZ, UG), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG). Published <i>With international search report.</i> <i>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i>

(54) Title: BALANCE COMPANDED DELTA CONVERSION FOR HOMODYNE RECEIVER



(57) Abstract

A balanced delta-modulation analog-to-digital conversion circuit is disclosed. A first principal integrator produces a first output signal which rises when a first control signal is generated and falls when a second control signal is generated. A second principal integrator produces a second output signal which falls when the first control signal is generated and rises when the second control signal is generated. The first and second control signals are then generated based upon the differences between the first and second output signals.