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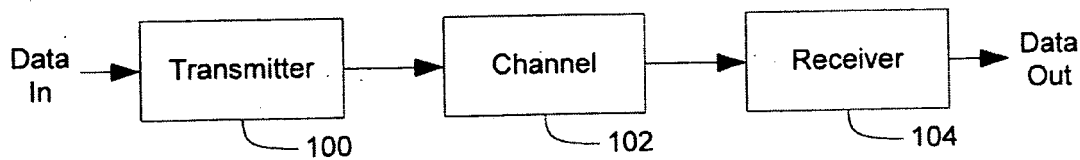


Fig 1

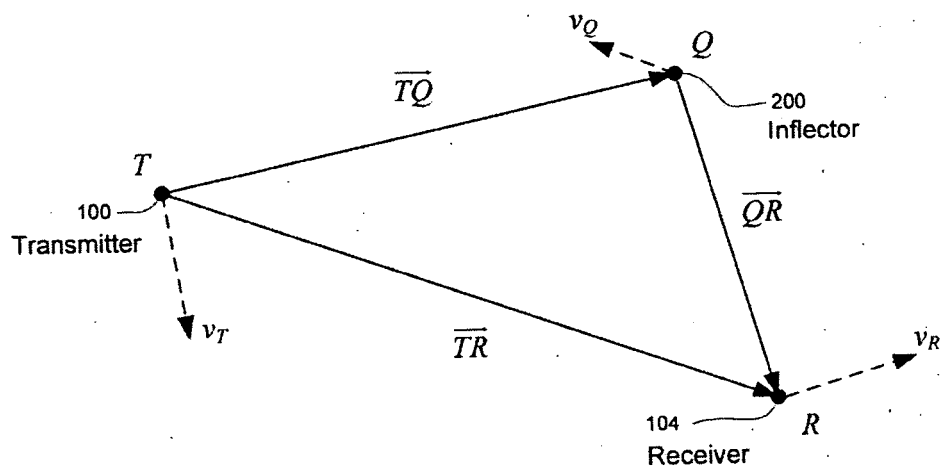


Fig 2

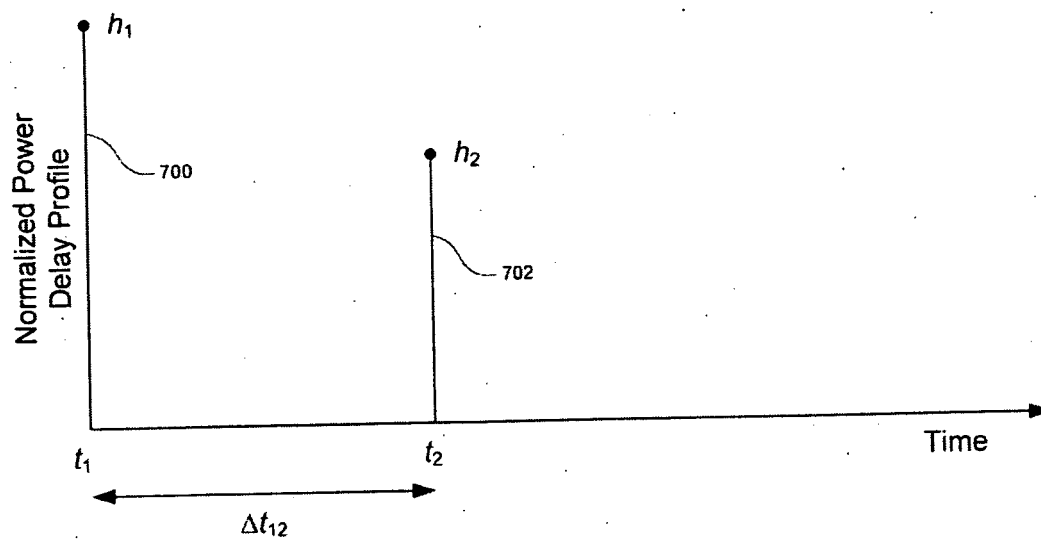


Fig 3

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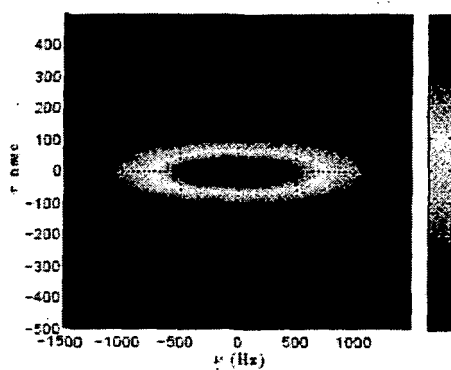
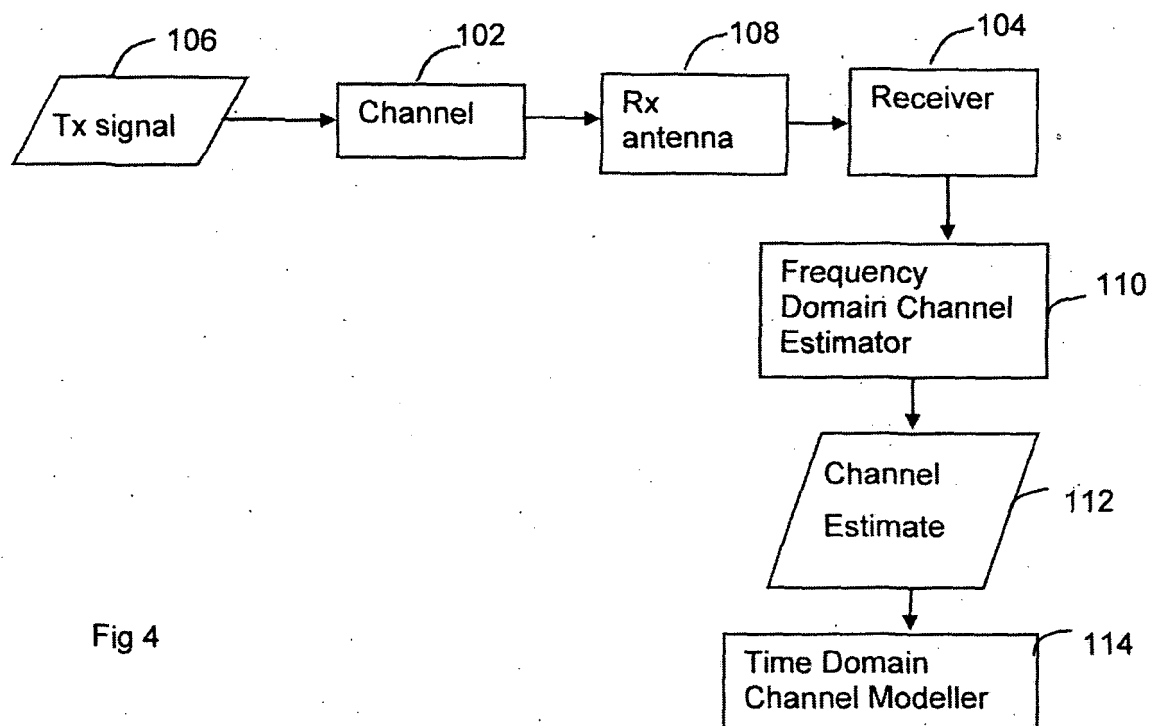
(a) $K = 52, L = 90$

Fig 5A

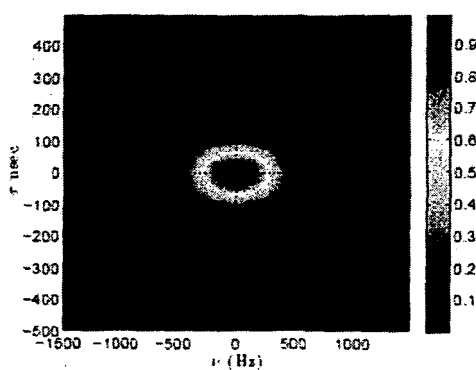
(b) $K = 52, L = 242$

Fig 5B

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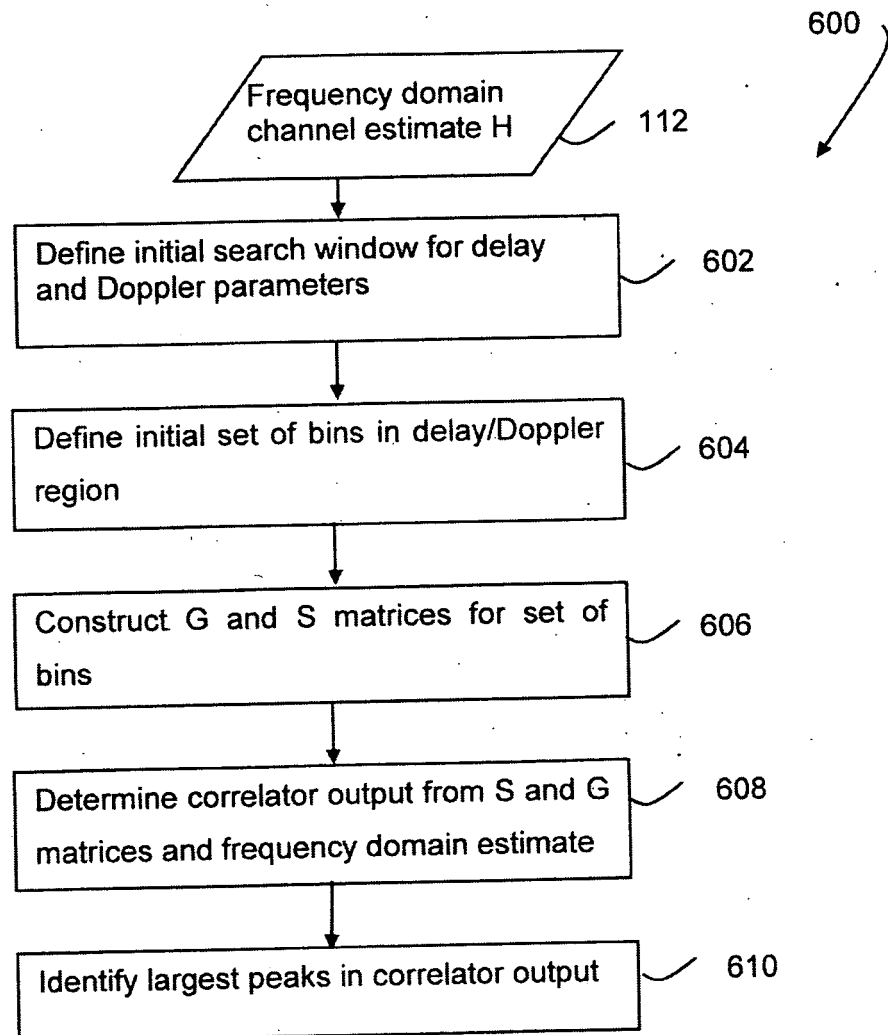


Fig 6A

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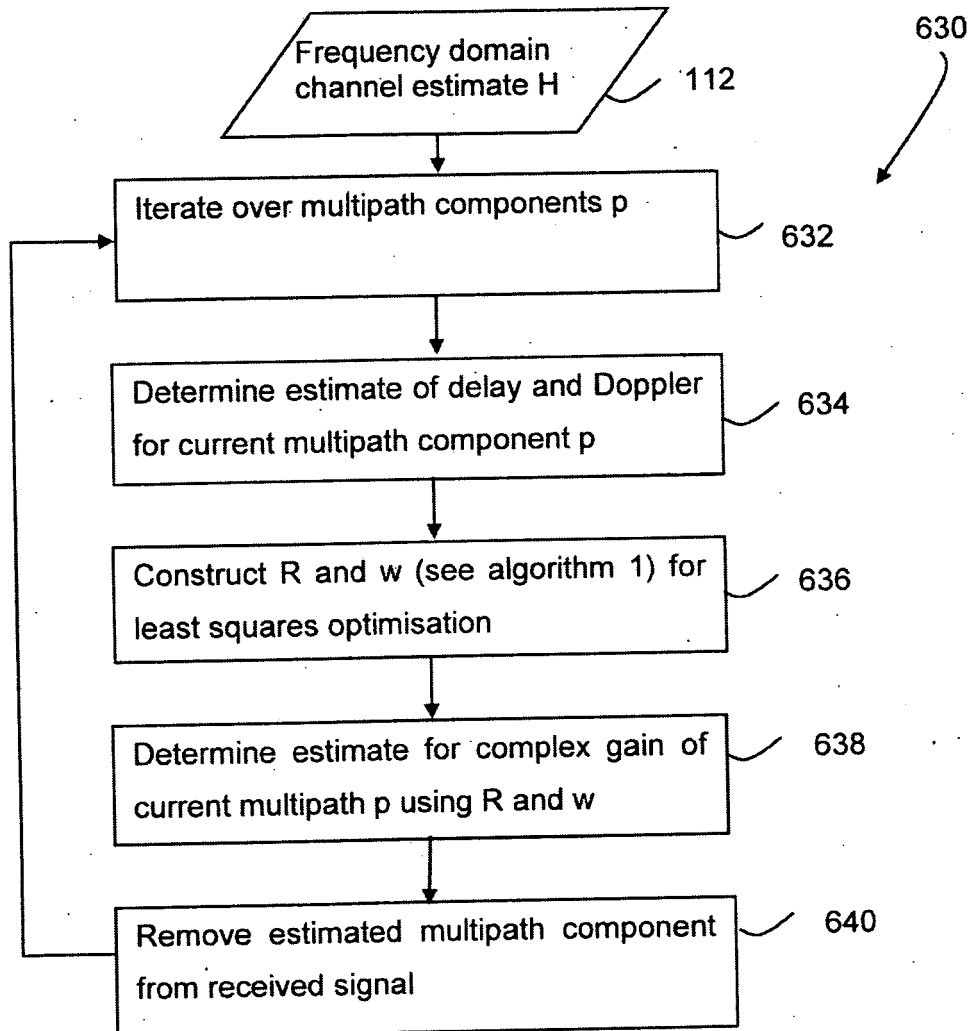


Fig 6B

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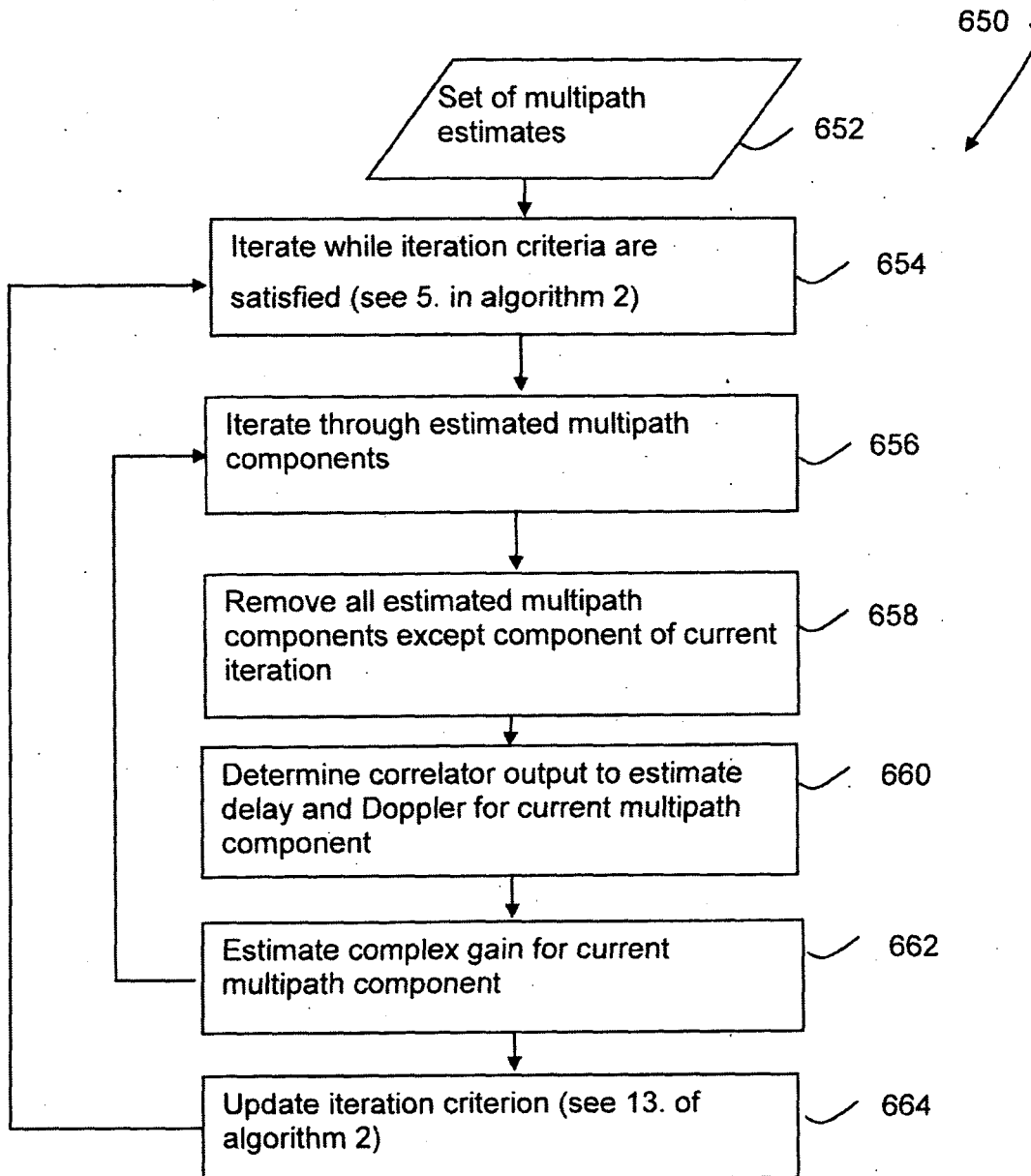
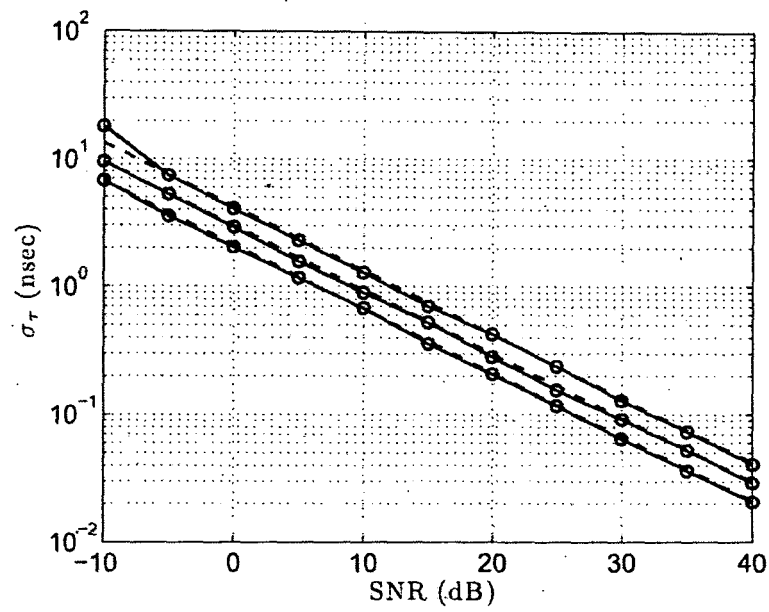


Fig 6C

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(a) RMS delay error

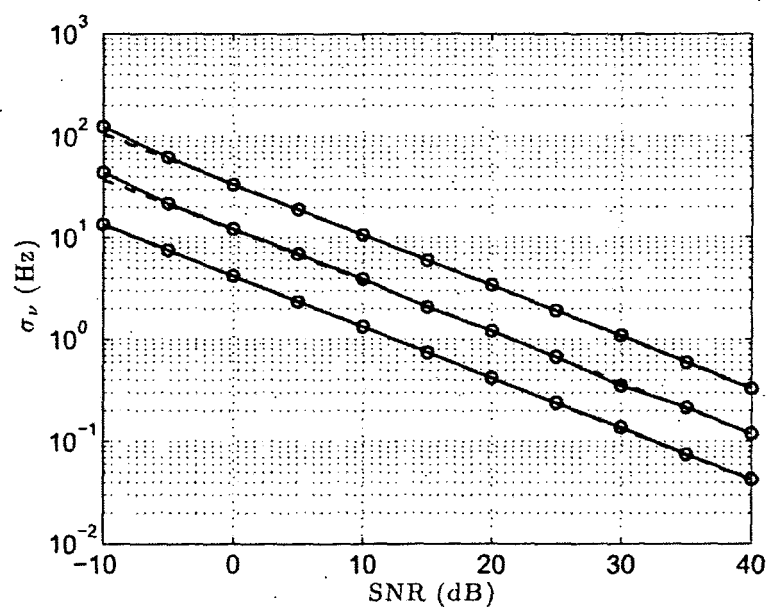


Fig 7

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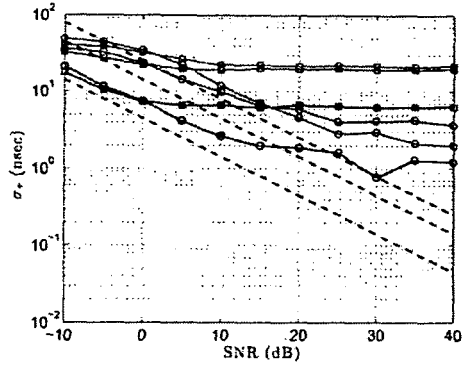
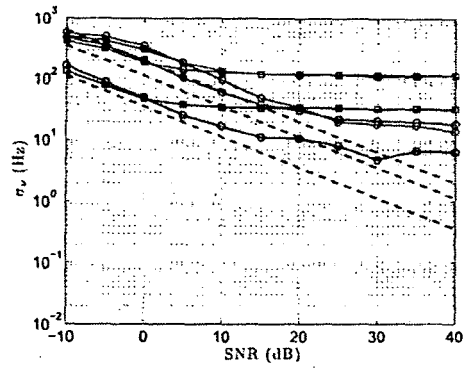
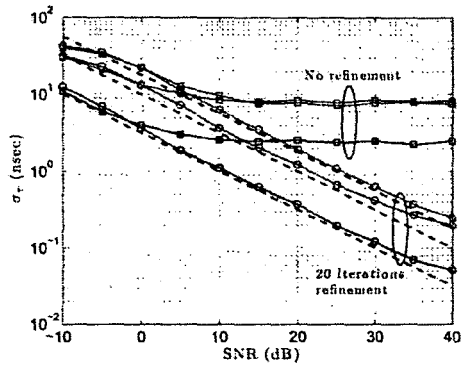
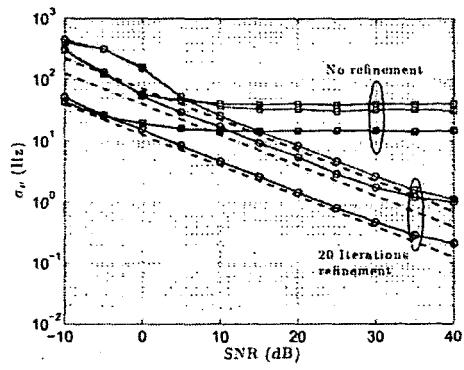
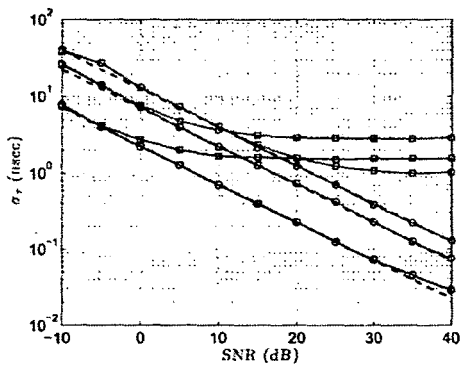
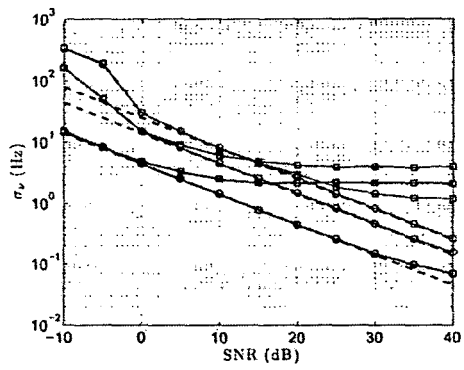
(a) $L = 128$ (b) $L = 128$ (c) $L = 256$ (d) $L = 256$ (e) $L = 512$ 

Fig 8