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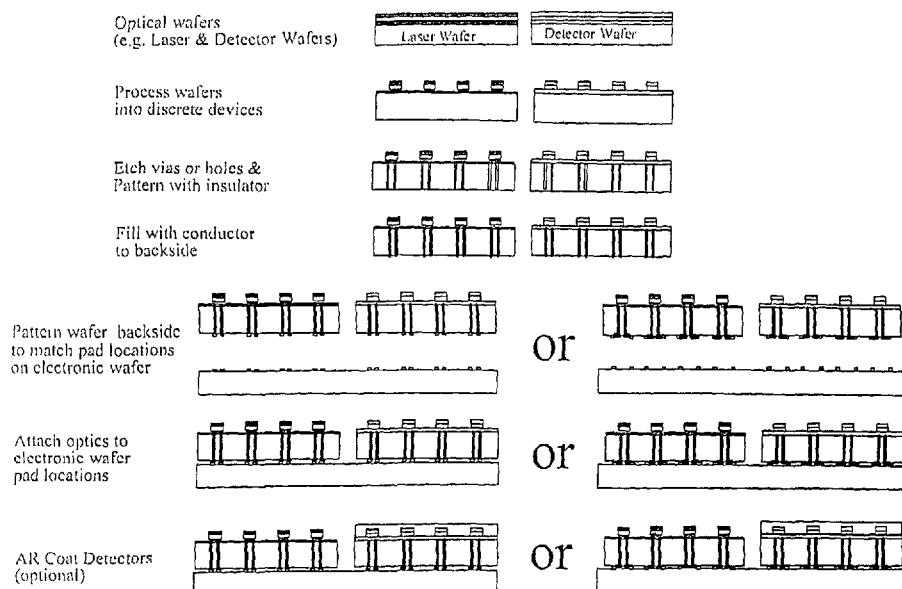
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(54) Title: OPTO-ELECTRONIC DEVICE INTEGRATION



(57) Abstract: A method of creating a hybridized chip having at least one top active device coupled to an electronic chip where a top active device is combined with an electronic chip having electronic chip contacts, when at least some of the top active device contacts are not aligned with at least some of the electronic chip contacts, and each of the at least some top active device contacts has an electrically corresponding electronic chip contact. The method involves creating sidewalls defining openings in the substrate, extending from the first side at the active device contacts to the bottom of the substrate opposite the first side, at points substantially coincident with the active device contacts; making the sidewalls electrically conductive; and connecting the points and the electronic chip contacts with an electrically conductive material.



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