

ABSTRACT

PARALLEL BIT INTERLEAVER

This bit interleave method performs bit permutation on QC LDPC code words consisting of N cyclic blocks of Q bits, and partitions said processed code words into multiple constellation words consisting of M bits. The code words are partitioned into $F \times N/M$ folding sections, and each constellation word is associated with one of the $F \times N/M$ folding sections. The bit permutation processing is performed such that the constellation words consist of F bits from each of M/F different cyclic blocks in the associated folding section.

10 FIG. 32