

ABSTRACT OF THE DISCLOSURE

MEDIA ERROR CODE GENERATION AS FOR
A INVERSE TRANSPORT PROCESSOR

In an inverse transport processor of the type which directs video payloads of a packet signal to buffer memory space (18), apparatus is included for writing media error codes at sequential first memory address locations in the memory (18) ahead of each packet payload. Concurrently a processor (101) examines the current packet to determine if it occurs in proper sequence. If a packet is lost, the payload is written to memory (80-98) in subsequent sequential address locations. If there is no packet loss, the sequential first memory address locations are simply overwritten by the packet payload to excise the undesired media error code. Media error codes can thus be inserted in the packet payload stream without creating timing obstacles, for a system designer.

Fig 4