

TITLE OF THE INVENTION

SEMICONDUCTOR DEVICE

REPLACED SHEETS SG

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Field of the Invention

5 The present invention relates to a semiconductor
 device; more specifically, the present invention relates to
 a semiconductor device particularly preferable for use in
 extremely thin, inexpensive IC cards with high bending
 toughness, wireless multi-chip modules and mobile
 10 communication terminals.

Background Art

As to IC cards, a card with the cross structure shown
 in Fig.20 is described in "Data Carrier, II", page 137 to 194,
 15 Japan Industrial Press Corporation, issued March 15, 1991.

In the card, as apparently shown in Fig.20, thick
 condenser chip 411 mounted on board 410 is connected through
 bonding wire 416 to printed wiring board 412 and is then molded
 with resin 415, and the resulting whole structure is
 20 incorporated into center core 413, of which the top and bottom
 are covered with over-sheet 409, 414.

Furthermore, Japanese Patent Laid-open No. Hei 3-
 87299 proposes an IC card comprising a thin-thickness chip.

In the card of such conventional type having the
 25 structure shown in Fig.20, elements such as condenser chip

411 are so thick that these elements are weak to stress of bending and are therefore readily broken, disadvantageously.

As to the card proposed by the Japanese Patent Laid-open No. Hei 3-87299, as shown in Fig.8, the surface and back face of the condenser chip 41 bonded to the thick board 42 are stressed by a stretch or press operation if the board is bent, so that larger stress is applied to the condenser chip 41 (of a thickness of 200 μ m). Therefore, the connection between metallized pattern 43 and the condenser chip 41 connected to the pattern 43 fails; or the condenser chip 41 weak to mechanical stress because of the thin-thickness is readily broken through the stress. Hence, the reliability thereof is particularly low.

A card of such conventional structure using condenser chip 41 is fabricated by attaching condenser chip 41 onto thin card 42 readily bendable, followed by wire bonding, and therefore, the card is at low reliability because condenser chip 41 is readily broken. Additionally, the number of the process steps for mounting is so large. Accordingly, it has been difficult to reduce the production cost.

Disclosure of the Invention.

It is an object of the present invention to overcome the problems of the prior art and provide a highly reliable, inexpensive semiconductor device strong to bending,

particularly a semiconductor device of a thin-thickness type, having functions as IC card, multi-chip module or mobile communication terminal.

So as to attain the object, in accordance with the present invention, a thin-thickness device or integrated circuit comprising a condenser is mounted on a flexible card board of the same size as that of a card, and the thickness of the condenser, an integrated circuit or a coil and the thickness of the card provided with the condenser, the integrated circuit or the coil are individually fixed at given dimensions.

More specifically, the thickness of the integrated circuit, the condenser or the coil is defined as $110\ \mu\text{m}$ or less, provided that the lower limits of the thickness of the card and the condenser are $50\ \mu\text{m}$ and $0.1\ \mu\text{m}$, respectively.

By fixing the integrated circuit, the condenser or the coil at such thin thickness, the integrated circuit, the condenser or the coil gets strong to stress of bending. When these are connected to a thin board such as IC card with a flexible adhesive, a highly reliable IC card strong to stress of bending can be produced.

Preferably, the thickness of a semiconductor device, namely the card on completion, is $760\ \mu\text{m}$ or less when the thickness of the integrated circuit, the condenser or the coil is $110\ \mu\text{m}$ or less.

Preferably, the thickness of a semiconductor device, namely the card on completion, is 500 μm or less, when the thickness of the integrated circuit, the condenser or the coil is 19 μm or less. Additionally, the thickness of a
5 semiconductor device, namely the card on completion, is 250 μm or less, when the thickness of the integrated circuit, the condenser or the coil is 4 μm or less.

Because the thin-thickness condenser attached on the card is thin, the board and the condenser are now possibly
10 wired by means of a conductive paste. Accordingly, compared with conventional wire bonding by means of gold wire, a flat IC card of thin thickness can be produced at a low material cost and at a large scale.

The structure comprising such a thin-thickness
15 condenser can be applied to the fabrication of not only IC card but also other devices of similar shapes and multi-chip mounting.

On the cross section of the card bent, stretch forces are induced on the surface of the curved board, while shrink
20 forces are induced on the back face. Because no shrinkage occurs then at the central part of the cross section of the card under less stress, the stress to be loaded onto a thin-thickness condenser chip can be released, if the condenser chip is placed on the part.

25 It is needless to say that the condenser chip of a

thin thickness is better. Extremely preferable outcome can be gained if the thickness is 110 μ m or less. If the card is thick, however, the stiffness of the card enlarges the critical curvature ratio. Hence, the card is scarcely bent.

5 Thus, the condenser chip may satisfactorily be thick at some extent.

If the card is of a thin thickness, on contrast, the card is readily bent. So as to release the stress of the condenser chip, the condenser chip should be thin. For

10 preparing a condenser of a thin thickness, a thinner thickness of the condenser requires more precise apparatuses for fabricating such condenser. Thus, as to how thin the condenser should be fabricated, it should be assessed from both the standpoints of economical efficiency and procurement

15 of reliability.

A given correlation in thickness between the card and the condenser chip is present; by fixing both the card and the condenser chip at the aforementioned thickness, various cards strong to bending and highly reliable can be produced

20 at a low cost. It is needless to say that this is the case with the thickness of the coil and the integrated circuit placed internally in the card, other than the thickness of the condenser.

The lowest thickness limits of the semiconductor

25 device, namely the card on completion, and the condenser, are

50 μm and 0.1 μm , respectively. If the card thickness is smaller than 50 μm , the flexibility of the card is distinctively reduced which causes difficulty in putting the card for practical use; and it is also difficult to fabricate a condenser of a thickness of 0.1 μm .

Brief Description of the Drawings

Fig.1 is a cross sectional view explaining Example 1 of the present invention;

10 Fig.2 is a cross sectional view explaining the Example 1 of the present invention;

Fig.3 is a cross sectional view explaining the Example 1 of the present invention;

15 Fig.4 is a plane view explaining the Example 1 of the present invention;

Fig.5 is a cross sectional view explaining the Example 1 of the present invention;

Fig.6 is a cross sectional view explaining Example 2 of the present invention;

20 Fig.7 is a plane view explaining Example 3 of the present invention;

Fig.8 is a cross sectional view explaining the problems of conventional cards;

25 Fig.9 is a cross sectional view explaining the Example 3 of the present invention;

Fig.10 is a cross sectional view explaining the Example 3 of the present invention;

Fig.11 is a cross sectional view explaining Example 4 of the present invention;

5 Fig.12 is a cross sectional view explaining the Example 4 of the present invention;

Fig.13 is an explanatory view of the Example 4 of the present invention;

10 Fig.14 is an explanatory view of the Example 4 of the present invention;

Fig.15 is an explanatory view of the Example 4 of the present invention;

Fig.16 is a plane view explaining Example 5 of the present invention;

15 Fig.17 is a plane view explaining Example 6 of the present invention;

Fig.18 is a cross sectional view explaining Example 7 of the present invention;

20 Fig.19 is a plane view explaining the Example 7 of the present invention;

Fig.20 is a cross sectional view depicting one example of conventional cards; and

Fig.21 is a cross sectional view depicting one example of conventional cards.

Best Mode for Carrying out the Invention

Example 1

Fig.1 is a cross sectional view explaining Example 1 of the present invention;

5 As shown in Fig.1, thin-thickness condenser 303 and coil 305 are bonded onto the surface of card board 301 with conductive material film 302 (Trade name; Anisolm, manufactured by Hitachi Chemicals, Co.).

Because the thickness of the thin-thickness condenser 10 303 is as thin as about 1 to 10 μm , the condenser 303 can readily be bonded to the surface of the board 301 by using the conductive material film 301 in paste or ink-like liquid because the difference in level between the surface of the board 301 and the condenser 303 bonded to the board 301 is 15 so small.

Therefore, the optimum card shape can be formed owing to such flat connection at an extremely low height. The conductive material film 302 in paste is as thin as about 10 μm , with higher flexibility, so the film is 20 characteristically strong to bending and resistant to difference in thermal expansion coefficient.

The thin-thickness condenser chip 303 was formed as follows.

As shown in Fig.2, firstly, lamination film 310 25 comprising an oxide film and a single crystal silicone film

was formed on silicone board 311, to fabricate SOI (silicone on insulator) wafer.

As shown in Fig.3, then, condenser 303 comprising lower electrode 307, isolation film 308 and upper electrode 309 was fabricated on the main surface side of the SOI wafer by a well-known semiconductive process. As the lower electrode 307, use was made of thermally resistant titanium and platinum; as the isolation film 308, use was made of a film comprising a material with a larger dielectric constant, such as PZT (solid solution of lead zirconia and lead titanate).

Subsequently, selective etching by means of an aqueous 40 % KOH (potassium hydroxide) solution was done to remove the silicone board 311, to subsequently fabricate the structure shown in Fig.3. Because the oxide film formed on the silicone board 311 then functioned as a stopper of etching, the lamination film 310 comprising the silicone film and the oxide film could be left while the silicone board 311 was selectively removed. Consequently, the condenser 303 comprising the electrode 307, the isolation film 308 and the electrode 309 was formed on the thin lamination film 310, structurally.

By fabricating thin-thickness integrated circuit 312 and printed coil 115 as a conductive pattern by a well-known process, card 113 of the plane structure shown in Fig.4 was

formed. As the conductive pattern, coil 115 formed by printing process was used in the present Example, but a coil formed by processes other than printing process may also be used satisfactorily.

5 The coil 115 generates a dielectric electromotive force on receiving electromagnetic wave from outside, to supply energy to thin-thickness condenser 114. The coil 115 and the thin-thickness condenser 114 adhered highly in packing density to the integrated circuit 310 with a conductive paste
10 or an anisotropic conductive adhesive, so that the coil 115 and the thin-thickness condenser 114 are electrically connected together. The coil 115 also functions to transmit information data supplied from the outside of card 113 to the thin-thickness condenser 114 and to transform the data from
15 the thin-thickness condenser 114 in electromagnetic wave and transfer the wave outside the card 112. A communication card, contactless and highly reliable, can be produced by forming the card 112 of such a structure.

 Because electrodes are placed on the surface of cards
20 called as contact type among conventional cards, contact-miss failure occurs or the cards are weak to electrostatic force, disadvantageously. The present invention may satisfactorily be applicable to conventional contact-type cards.

25 Then, flexible adhesive 119 for example silicone was

filled into the space formed by the thin-thickness condenser 114, the integrated circuit 312 and the coil 115 fabricated by printing process; additionally, the upper cover sheet 117 and the lower cover sheet 118 were fixed with the adhesive 119, to form a card of the cross structure shown in Fig.5.

The adhesive 119 had double operation of adhesion and filling up; and the thin-film condenser 114 and the like were enclosed and retained within the soft gummous material, so any stress was hardly loaded onto the surface of the condenser 114 and the like; and additionally, the resulting card was strong to bending.

Even if the card deforms when the card is attacked by shock pin-point force, the force from outside is released through the adhesive layer 119, which prevents stress attack on the surface of the condenser 114.

Example 2

In the present Example, an extremely thin-thickness condenser was placed on the neutral surface of cards. By putting the condenser between the two cards, a practically satisfactory bending toughness was procured in this Example.

As apparently shown in Fig.6, in the present Example, thin-thickness element 315 for example condenser chip and coil is fixed between the upper card board 317 and the lower card board 318 by means of adhesive 314, and by individually

arranging thin plates 313, 316 comprising a harder material than these card boards 317, 318 on these card boards, the element was reinforced.

The thin-thickness element 315 is at a thickness of
5 1 to 110 μ m, which is far thinner than the thickness of conventional elements, and therefore, by arranging the thin-thickness element 315 on the neutral surface, the element was reinforced by the thin plates 313, 316, whereby satisfactory bending toughness was procured and the card
10 surface could be prepared as flat.

Example 3

Using Fig.7 depicting plane placement, another example of the present invention to produce a card with better
15 bending toughness than conventional ones will be explained.

As apparently shown in Fig.7, in the present Example, thin-thickness element 315 for example condenser chip and coil is placed inside circle 321 with the diameter equal to the short side's length of card 319 and with the center thereof
20 placed at the center of the card. It was observed that the immunity to bending was thereby improved, and it was found that the resulting card could be used in a far more simple manner than conventional ones.

Fig.9 depicts an example wherein a thin-thickness
25 condenser was used as the thin-thickness element 315 and the

thin-thickness condenser 315 was embedded at the center position 37 of card board 36.

When the card board 36 was bent, the surface and back face thereof were both stressed by a stretch or press operation. Because the thin-thickness condenser 315 was placed at the center position 37 of the card board 36, the thin-thickness condenser 315 was never attacked by such stress. Thus, a highly reliable card strong to bending was produced.

So as to form a card of the structure shown in Fig.9, firstly, thin-thickness condenser 315 is attached on the surface of card board 39, as shown in Fig.10. Then, second card board 36 of the same thickness as that of the card board 39 is attached onto the thin-thickness condenser 315, whereby the structure shown in Fig.9 can be formed readily. It is needless to say that the thin-thickness condenser 315 may be placed at a desirable position inside the circle 321 shown in Fig.7, in addition to the center position of the board 39.

Example 4

Fig.11 is a view explaining another example of the present invention, depicting the state of a card with a curvature due to bending stress.

Because thin-thickness condenser chip 104 is put between lower card board 103 and upper card board 101 along the center line 102a of the cross sections of the two boards,

such structure is the last to be influenced by bending. Thus, the thin-thickness condenser chip 104 is not stressed. When the card is bent, the thin-thickness condenser chip 104 is also bent, but the stress then is extremely small because the
 5 thin-thickness condenser chip 104 is extremely thin.

Fig.12 shows the case that condenser chip 104 is bent. When the condenser chip 104 is bent, the surface stress ρ of the condenser chip 104 is represented, as follows, according to Navier's theorem; $\rho = E \times t / R$. As shown in Fig.12, herein, E
 10 represents Young's modulus of the condenser; R represents radius of curvature; and t represents 1/2 of the thickness of the condenser chip 104.

Because the surface of the condenser chip 104 comprises silicone oxide, E is equivalently equal to the
 15 Young's modulus of the silicone oxide. The above formula indicates that the surface stress of the condenser chip 104 is in proportion to the thickness of the condenser chip 104 but in reverse proportion to the radius of curvature R. When the surface stress of the condenser chip 104 is larger than
 20 the mechanical toughness of the condenser chip 104, the chip is broken through bending. Because the radius of curvature R is infinite in the absence of any bending, the surface stress ρ is zero; when R gets smaller following the progress of bending, the stress ρ gets larger, until the condenser chip
 25 104 is broken finally.

However, if the condenser chip 104 is thin, the surface stress ρ is reduced, even through bending with the same radius of curvature R , and therefore, the condenser chip 104 can get sufficiently strong to bending if the condenser chip 104 is made thinner within a range not exceeding the limit against mechanical break.

If the condenser chip 104 is made so thin, however, the chip is difficult to be handled. As shown in Fig.11, thus, the thin-thickness condenser 104 is put between two card boards 102, 103 comprising plastics, metal and the like, whereby the condenser is readily handled, involving the increase of the toughness. Then, most preferably, the thin-thickness condenser chip 104 is placed on neutral surface 102a of card 101. By such placement, the neutral surface of the thin-thickness condenser 104 agrees with the neutral surface 102a of the card 101 with zero stress even when the card is bent, so that the thin-thickness condenser chip 104 is possibly never broken even if the card 101 is bent, as in the case that only the thin-thickness condenser chip 104 is singly bent.

Fig.13 shows the results of the determination of the dependency of the surface stress of LSI on the ratio of the LSI thickness to the card thickness, using as the parameter the card thickness. After placing the thin-thickness condenser on the neutral surface of the card board, the surface stress of the thin-thickness condenser was determined,

correspondingly to the ratio of the thickness of the thin-thickness condenser to the card thickness.

The LSI surface stress has a significant relation with the degree of the curvature of the card; the degree of the curvature of the card varies largely, depending on the thickness and materials of the card, and the force loaded onto the card, and the position of the card. In the present Example, an LSI chip was placed at the center position of the plane face of the card; as the card material, vinyl chloride commonly used for general magnetic cards and credit cards was used. Because PET material is characteristically harder and is more scarcely bent than vinyl chloride, the results recovered by using vinyl chloride are applicable to any card comprising other materials including PET.

The radius of curvature defining the degree of bending varies depending on the bending moment loaded on the card. The bending moment was loaded onto the card, up to a limit above which the card is bent and folded over. The radius of curvature at the center of a vinyl chloride card of a thickness of 0.76 mm was 50 mm. Provided that the thickness of the LSI chip is the same as the thickness of the card, herein, the surface stress of the LSI chip is calculated by the formula $8E12 \times 0.38 / 50$ (Pa), according to the aforementioned formula of stress, which is 600 MPa. The Young's modulus of glass cited from the Japanese Scientific Table was used. Because

the surface of the LSI chip is principally composed of silicone oxide film layer, it is assumed that the surface has the same physical properties as those of glass.

The moment of inertia of the card is involved in the
5 relation between the radius of curvature and the thickness of the card. The radius of curvature R is represented by $E \times I / M$, wherein E represents the Young's modulus of the card; I represents the moment of inertia; and M represents bending moment. Because the moment of inertia of the card is in
10 proportion to the cube of the thickness of the card, the profile curve of the radius of curvature, as shown in Fig.15, is prepared. Fig.15 shows that the surface stress of the LSI chip is 2.5 GPa and 5.4 GPa at card thickness of 0.5 mm and 0.25 mm, respectively, provided that the ratio of the thickness
15 of the LSI chip to the card thickness is 1.0. At that state, the LSI chip is readily broken, but in accordance with the present invention, the LSI chip fabricated thin is put between the neutral surfaces of the cards. Therefore, such break can be prevented.

20 Using the ratio of the thickness of the LSI chip to the card thickness as a parameter, the surface stress of the resulting condenser of thin thickness was measured. The results are shown in Fig.13. An enlarged view of a part of Fig.13 is shown in Fig.14, wherein the ratio of the thickness
25 of the LSI chip to the card thickness is 0 to 0.16.

In Fig.14, the stress of the LSI chip durable of bending is 90 MPa, and the value is cited from the Japanese Scientific Table, provided that the break strength of the LSI chip is assumed to be equal to the break strength of glass.

5 Thus, the necessary thickness of the LSI chip and the lowest thickness limit of the LSI chip at various dimensions of card thickness can be determined in Fig.14. More specifically, any break of the LSI chip due to card bending absolutely never occurs, provided that the thickness of the LSI chip is 110
10 μm or less at the card thickness of 0.76 mm; that the thickness of the LSI chip is 19 μm or less at the card thickness of 0.5 mm; and that the thickness of the LSI chip is 4 μm or less at the card thickness of 0.25 mm.

It is needless to say that the reliability of the LSI
15 chip is so much improved when the thickness of the LSI chip is as thin as the lowest limit, but the limit of the thickness of the chip to be possibly fabricated is almost 0.1 μm . The fabrication of any LSI chip thinner than the limit is difficult.

20 The LSI chip and the thin-thickness condenser are most preferably placed in such a manner that the neutral surfaces of the LSI chip and the thin-thickness condenser might agree with the neutral surface of the card. However, the upper or lower faces of the LSI chip and the thin-thickness condenser
25 are satisfactorily placed within the upper or lower faces of

an LSI chip and a thin-thickness condenser being individually of the lowest thickness limits defined by the card thickness and being placed on the neutral surface of the card.

More specifically, the upper or lower face of the
5 thin-thickness integrated circuit, the thin-thickness condenser or the coil is satisfactorily positioned within 55 μm above or below the neutral surface of the card provided that the thickness of the card on completion is 760 μm or less, or within 9.5 μm above or below the neutral surface of
10 the card provided that the thickness of the card on completion is 500 μm or less, or within 2 μm above or below the neutral surface of the card provided that the thickness of the card on completion is 250 μm or less.

15 Example 5

Fig.16 shows another example of the present invention.

Thin-thickness condenser may possibly be provided with various control functions. More specifically, as described
20 above, SOI wafer and the well-known semiconductive process are used to fabricate circuit device part 323 and condenser part 324 in an adjacent fashion to each other, in thin-thickness condenser 322, whereby various controls can be included in one chip and high performance and low cost can
25 be established. For example, the circuit device part 323 can

be utilized for data storage in wireless cards.

Example 6

Fig.17 depicts another example of the present invention. In accordance with the present invention, thin-thickness elements such as condenser chip are put between two card boards to fabricate a card, and therefore, the card surface is very flat. A card comprising conventional thick elements is weak to bending, on the surface of which difference in level up to 150 μ m may be formed, so that it is difficult to make the card flat until the difference is reduced to 30 μ m, which is essential for pressure-sensitive printing process. So as to make the surface flat, the structure should be so highly precise that the cost therefor is eventually escalated.

In accordance with the present invention, however, various elements such as condenser chip are extremely thin and the surface is flat, as described above. As shown in Fig.17, therefore, thin-thickness element 326 can be placed below picture 327, in the present Example, whereby the degree of freedom is improved.

Example 7

Fig.18 shows the cross structure of a card, on the surface of which printing is effected; printing material 328

is placed so that the material might be hung or held over thin-thickness element 332. In the structure, the thin-thickness element 332 is so thin and embedded in adhesive 331, and the upper cover sheet 329 and lower cover sheet 330 are
5 bonded together with the adhesive 331. Thus, the surface turns flat.

Therefore, even when a printing roll reaches the top of the edge of the thin-thickness element 332, the pressure is dispersed, with no occurrence of break of the thin-thickness
10 element 332. Those to be printed on the surface of the upper cover sheet 329 or lower cover sheet 330 include for example a photograph of the face of a card holder. In this case, the part of the photograph is handled gently, so the thin-thickness element 332 can be placed at a desirable position, taking
15 account of the use.

Fig.19 is a view depicting the plane structure of the card shown in Fig.18, wherein printing material 332 effects predetermined printing over the thin-thickness element 332 placed on the card 333. Conventionally, the thin-thickness
20 element 332 has been broken frequently in such structure. In the present Example, however, the thin-thickness element 332 is so extremely thin as described above that predetermined printing could be effected with no concern of break. A card of not a complex structure but a simple structure at high
25 reliability could be produced.

As apparent in the description above, the following advantages can be brought about in accordance with the present invention.

1. High reliability because of no concern of break due to
5 bending.
2. Ready production at low cost because of the simple structure.
3. Because the condenser is extremely thin, the board and the condenser can be wired with a conductive paste, with the
10 resulting lower cost and additionally with the resulting flat surface.
4. An extremely thin-thickness condenser can be produced by the use of SOI wafer, with lesser concern against break due to bending.



What is claimed is;

1. A semiconductor device at least comprising a flexible first card board and a flexible second card board, the two boards being placed to face to each other at a given interval, an integrated circuit formed between the first card board and the second card board, a condenser or a coil, wherein the thickness of the integrated circuit, the condenser or the coil is 110 μ m or less.

2. A semiconductor device according to claim 1, wherein the thickness of the device is 760 μ m or less.

3. A semiconductor device according to claim 1, wherein the thickness of the integrated circuit, the condenser or the coil is 19 μ m or less and the thickness of the semiconductor device is 500 μ m or less.

4. A semiconductor device according to claim 1, wherein the thickness of the integrated circuit, the condenser or the coil is 4 μ m or less and the thickness of the semiconductor device is 150 μ m or less.

5. A semiconductor device according to claim 1, wherein the thickness of the semiconductor device is 50 μ m or more.

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6. A semiconductor device according to claim 1, wherein the thickness of the integrated circuit, the condenser or the coil is $0.1 \mu\text{m}$ or more.

5

7. A semiconductor device according to claim 1, wherein the integrated circuit, the condenser or the coil is placed at a position within $55 \mu\text{m}$ above and below the neutral surfaces of the first coil board and the second coil board.

10

8. A semiconductor device according to claim 7, wherein the integrated circuit, the condenser or the coil is placed on the neutral surfaces of the first card board and the second card board.

15

9. A semiconductor device according to claim 1, wherein the integrated circuit, the condenser or the coil is electrically connected together through a conductive substance.

20

10. A semiconductor device according to claim 9, wherein the conductive substance is a conductive paste or an anisotropic conductive adhesive.

25

11. A semiconductor device according to claim 10,





wherein the first card board and the second card board are bonded together through an adhesive layer with no conductivity and wherein the integrated circuit, the condenser or the coil is individually fixed on a predetermined position through the
5 anisotropic conductive adhesive layer.

12. A semiconductor device according to claim 1,
wherein a reinforcing film comprising a material harder than the first card board and the second card board is placed on
10 the first card board and the second card board.

13. A semiconductor device according to claim 12,
wherein no substantial difference in level is present between the surface of the reinforcing film and the surfaces of the
15 first card board and the second card board.

14. A semiconductor device according to claim 1,
wherein the coil has a function to generate a dielectric electromotive force on receiving electromagnetic wave from
20 outside to supply energy to the condenser.

15. A semiconductor device according to claim 1,
wherein the coil has a function to transmit information data supplied from outside to the condenser.





16. A semiconductor device according to claim 1, wherein the coil has a function to transfer the data from outside in the form of electromagnetic wave from the condenser outside.

5

17. A semiconductor device according to claim 1, wherein the coil is formed by printing process.

18. A semiconductor device according to claim 1,
10 wherein the integrated circuit, the condenser or the coil is placed in a circle of which the center lies at the center point of the plane faces of the first card board and the second card board, the circle being of a diameter of the short side's length of the first card board and the second card board.

15

19. A semiconductor device according to claim 1, wherein a desirable image is printed on the surface of the card board.

20

20. A semiconductor device according to claim 1, which is a card of contactless type.

21. A semiconductor device according to claim 1, which is a card of contact type.

25





22. A semiconductor device according to claim 1,
wherein the integrated circuit and the condenser are formed
in one chip.

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Abstract of the Disclosure

Condenser (114), coil (115) and thin-thickness integrated circuit (312) are placed between the upper cover sheet (117) and the lower cover sheet (118), and adhesive (119) is filled into the space among them, whereby a card is fabricated.

Because condenser (114), coil (115) and thin-thickness integrated circuit (312) are extremely thin, the resulting semiconductor device is strong to bending and highly reliable at a low cost.

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FIG. 1

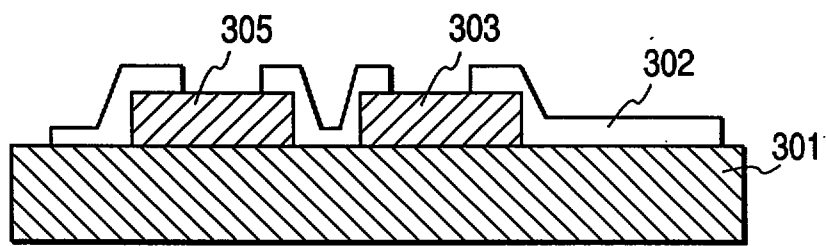


FIG. 2

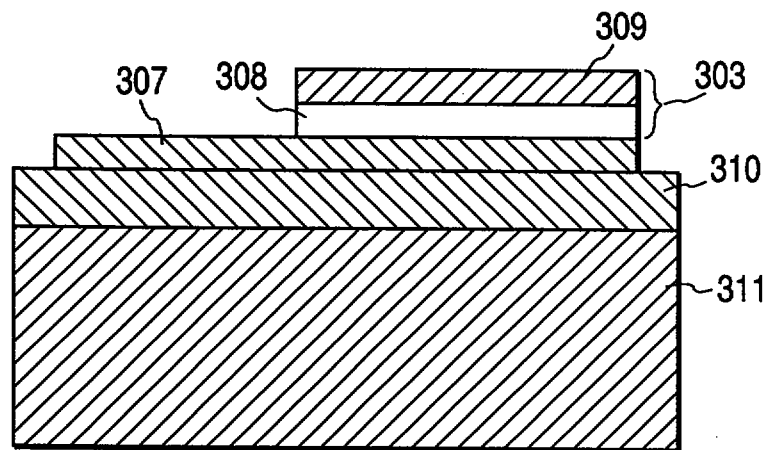


FIG. 3

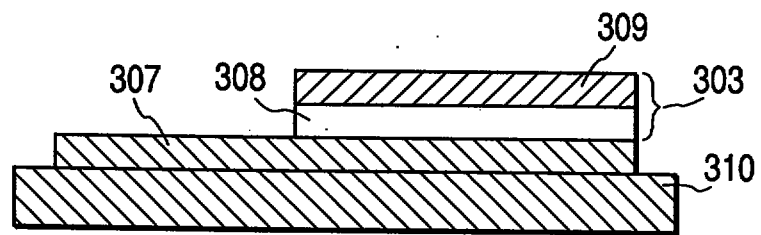


FIG. 4

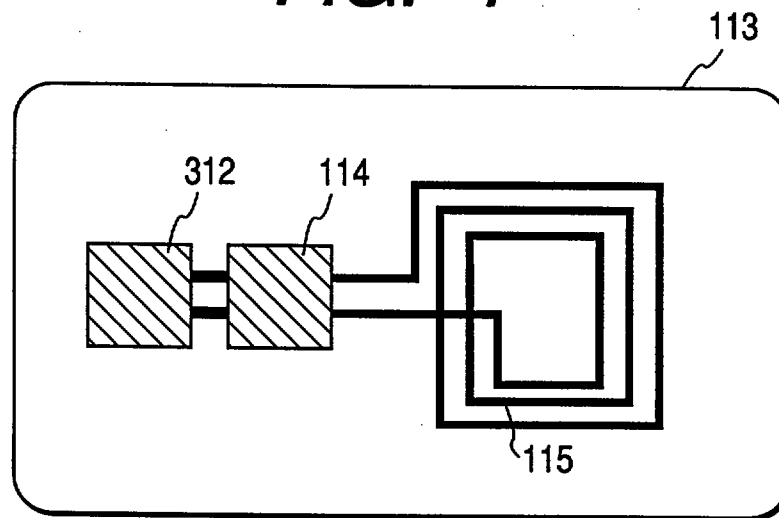


FIG. 5

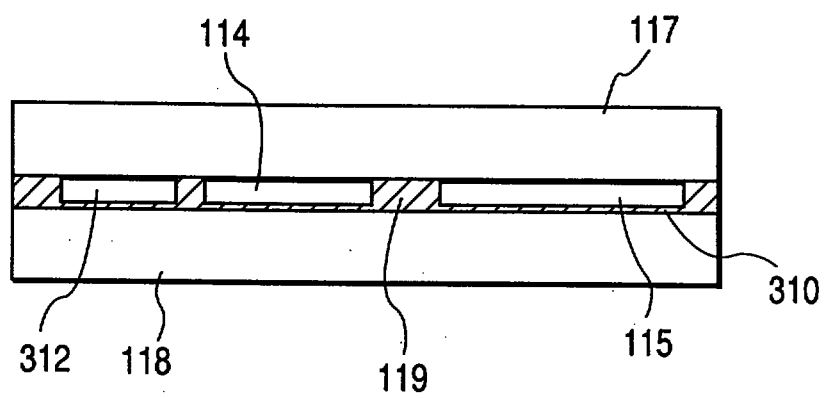


FIG. 6

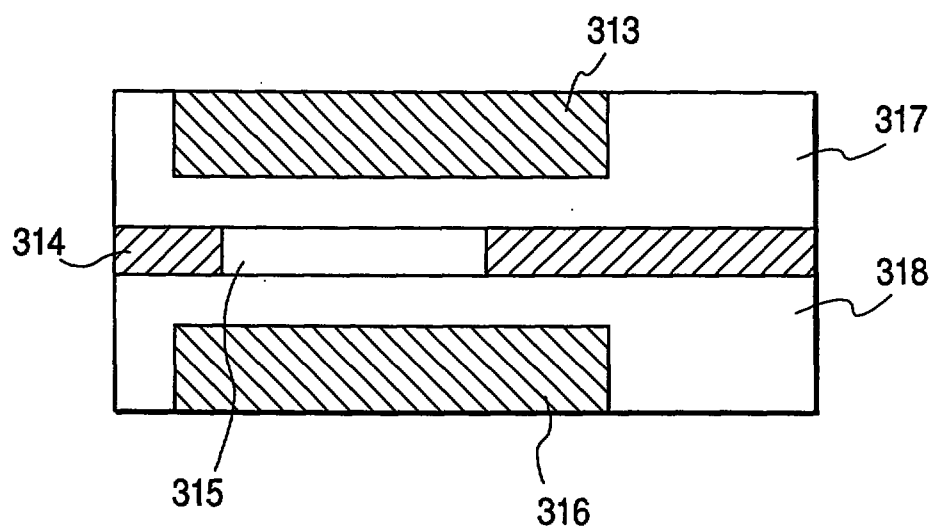


FIG. 7

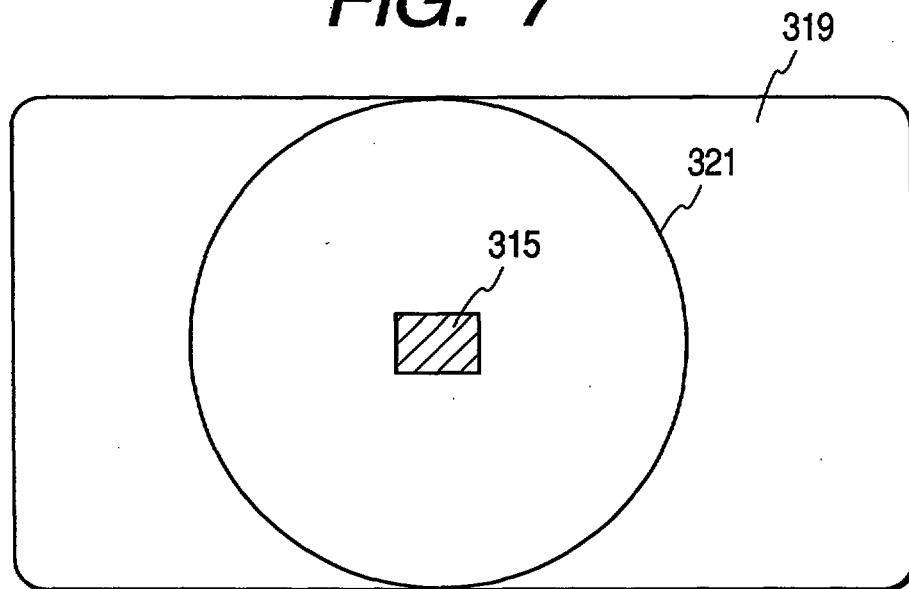


FIG. 8

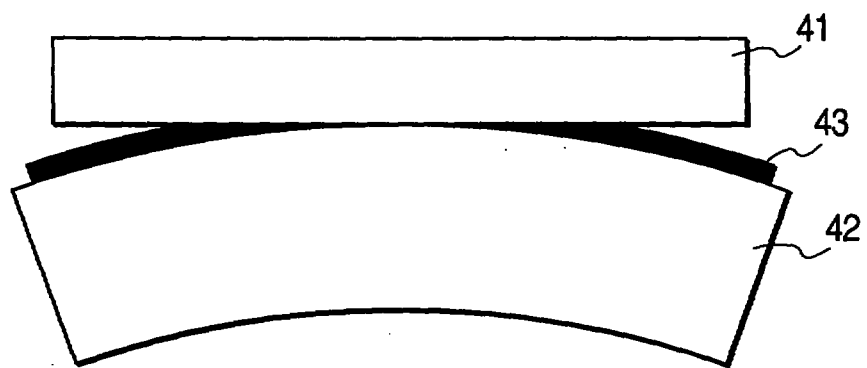


FIG. 9

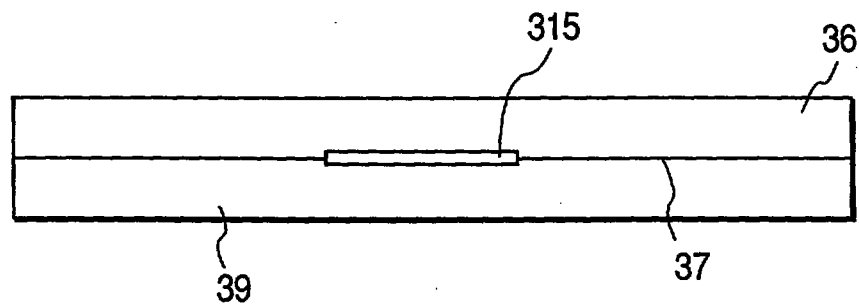


FIG. 10

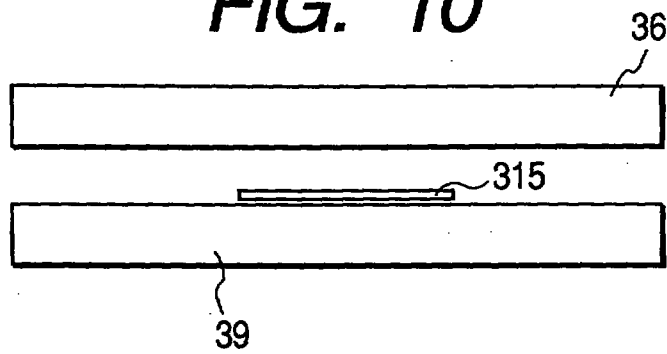


FIG. 11

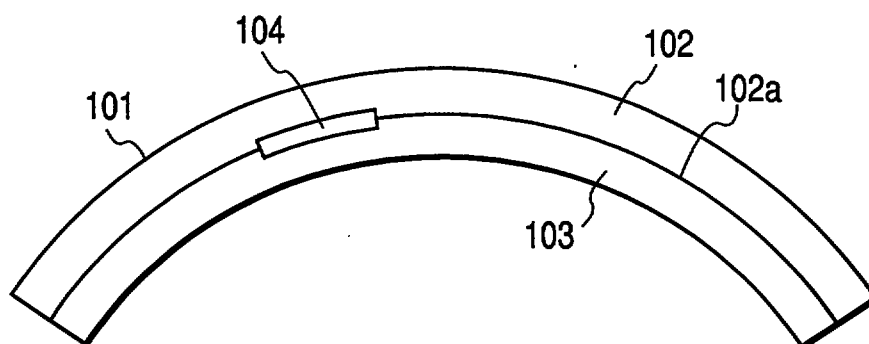


FIG. 12

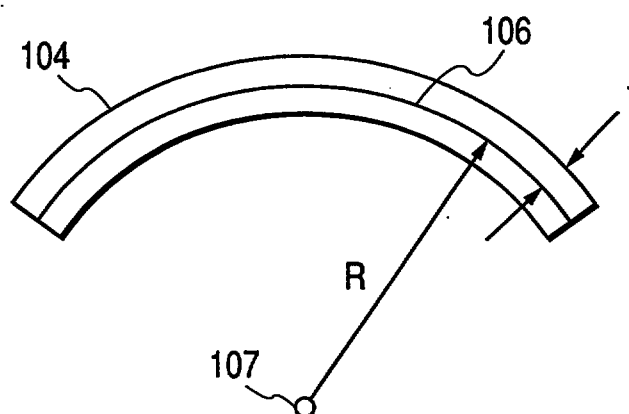


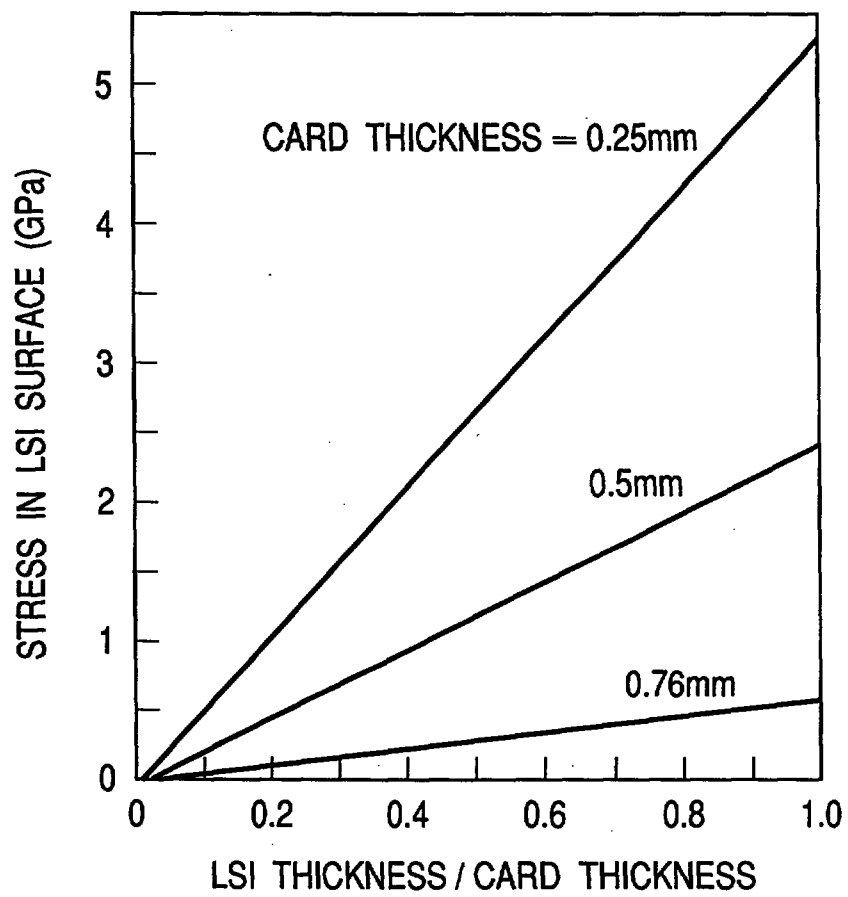
FIG. 13

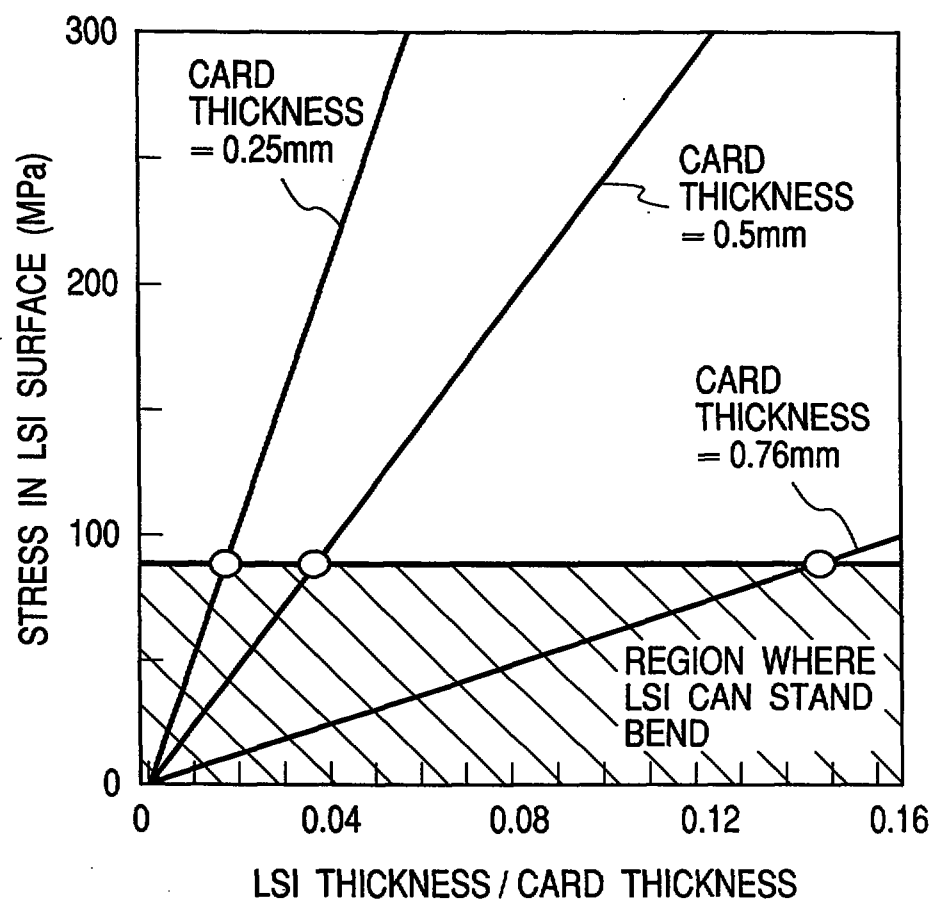
FIG. 14

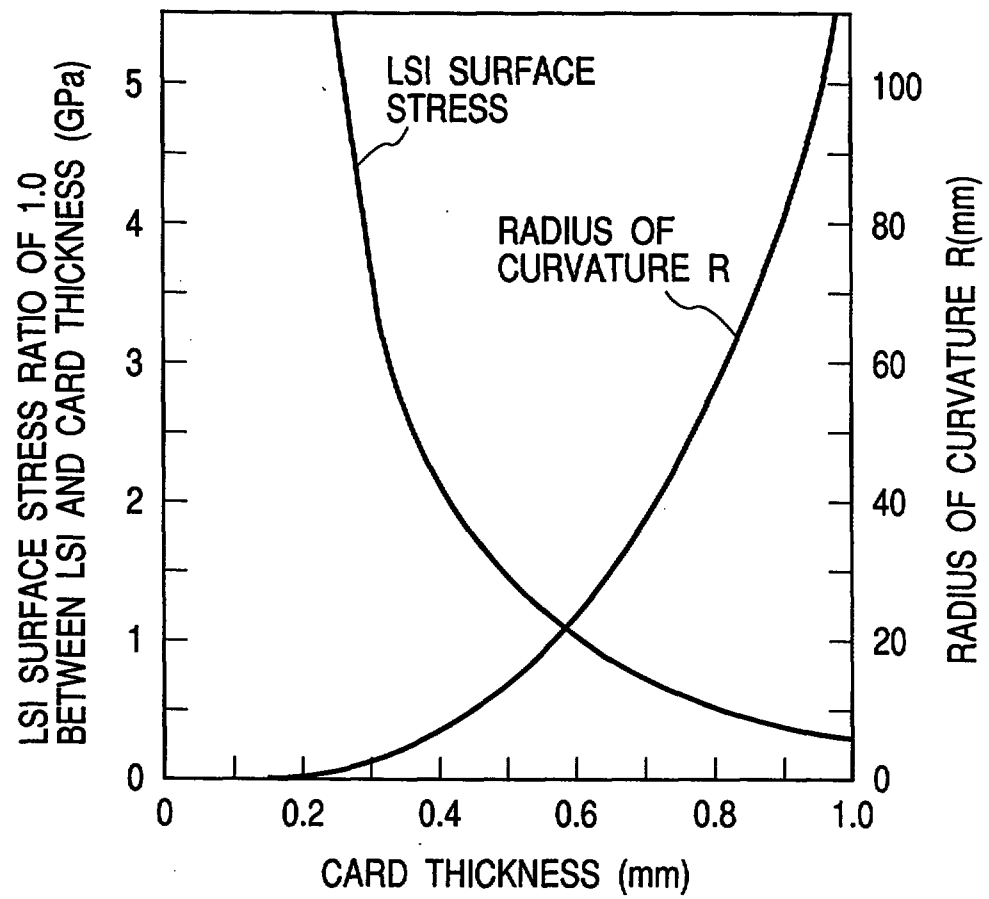
FIG. 15

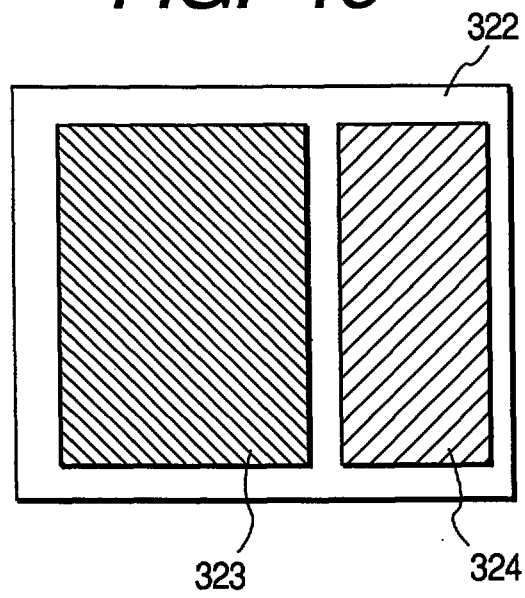
FIG. 16

FIG. 17

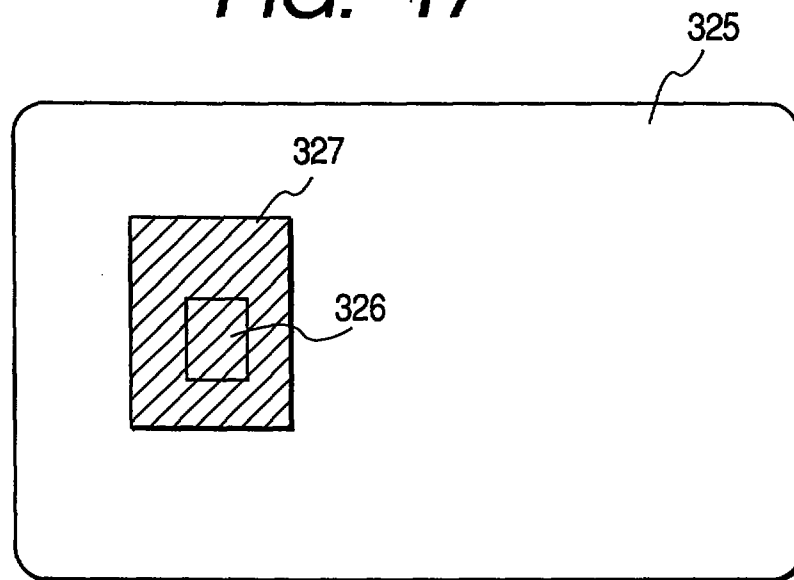


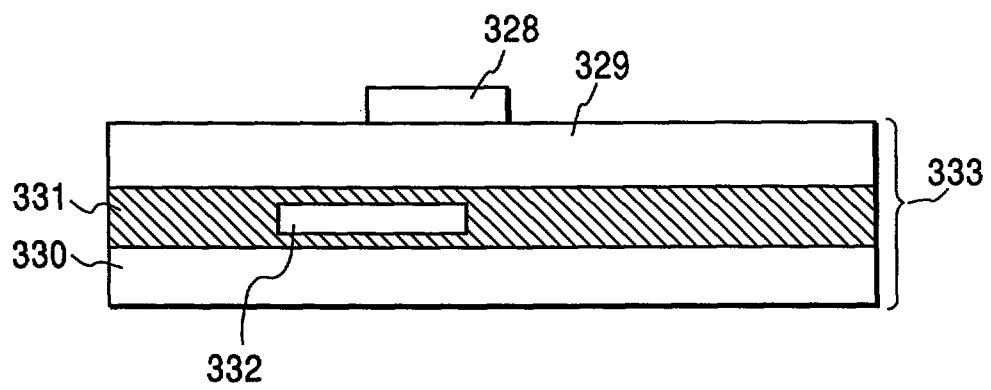
FIG. 18

FIG. 19

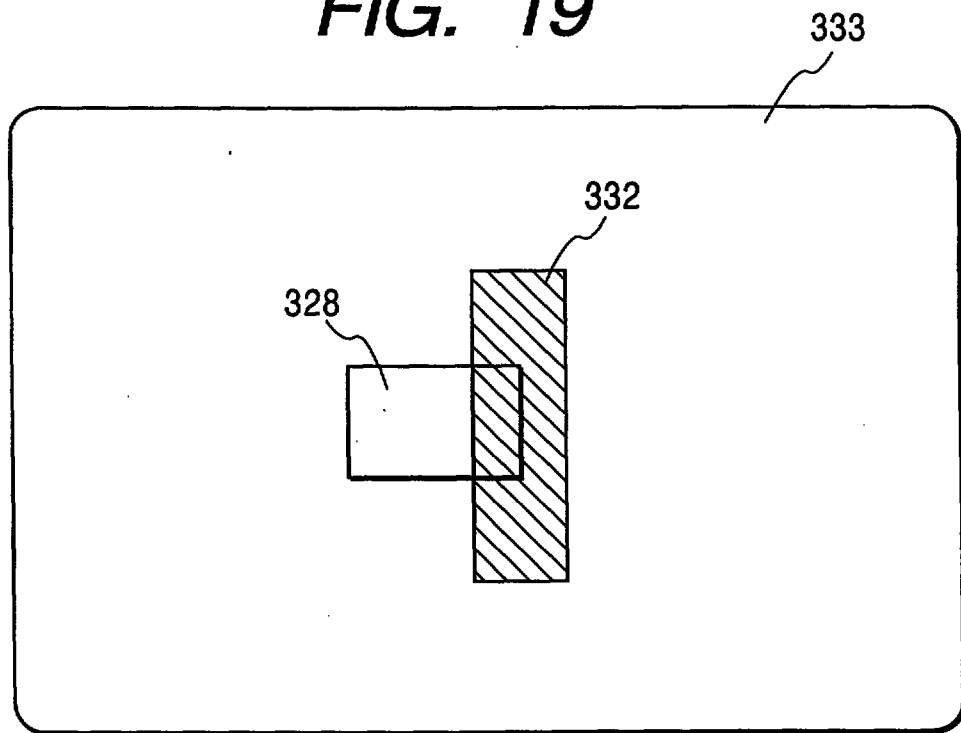


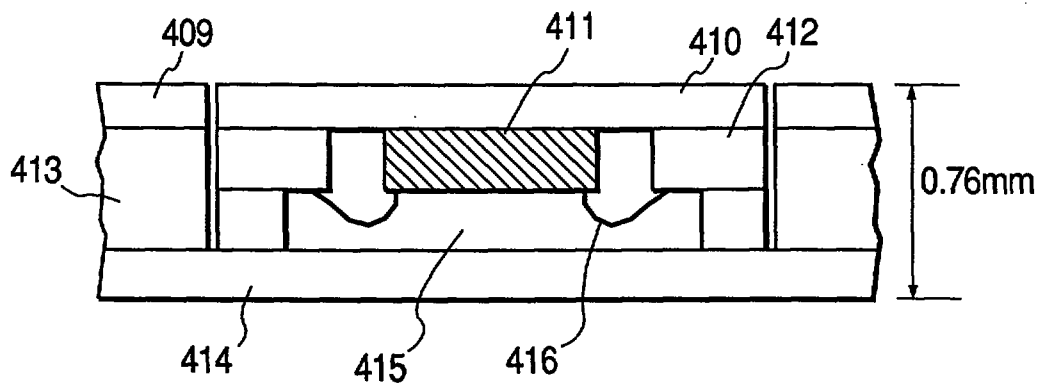
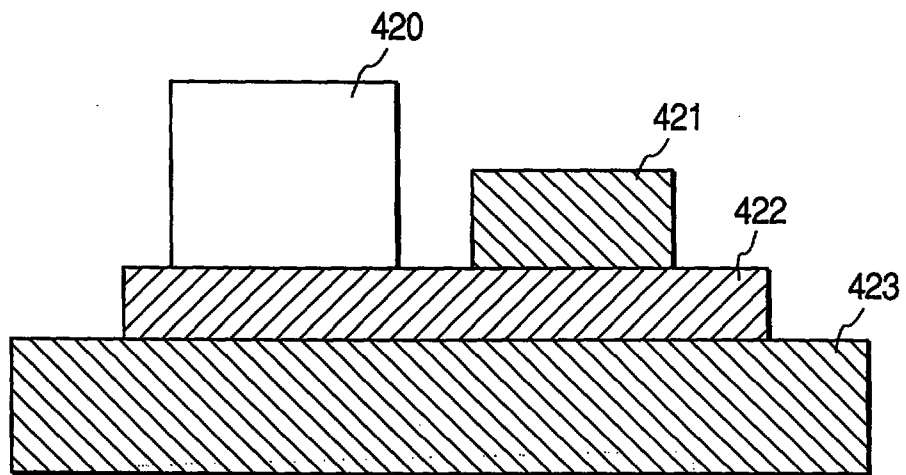
FIG. 20

FIG. 21





What is claimed is;

REPLACEMENT SHEETS		SG
☐	Description	: paged ... to paged ...
☒	Claim(s)	: paged 22 to paged 28
☐	Drawing(s)	: paged ... to paged ...
☒	Abstract	: paged 39 to paged ...
DATE OF RECEIPT		22 MAY 1998

1. A semiconductor device comprising,
a condenser chip having a thickness of $110\mu\text{m}$ or less;
an integrated circuit chip having a thickness of $110\mu\text{m}$ or less coupled with said condenser chip; and
a first and a second flexible substrates interposing said condenser chip and integrated circuit chip between them.
2. A semiconductor device according to claim 1, wherein said semiconductor device includes plural integrated circuit chips.
3. A semiconductor device according to claim 2, wherein said semiconductor device is a multi chip module.
4. A semiconductor device according to claim 1, wherein said condenser chip includes a titanium oxide film as a dielectric film of the condenser.
5. A semiconductor device according to claim 4, wherein said condenser chip includes a titanium or platinum film as an electrode of said condenser.
6. A semiconductor device according to claim 1, wherein said condenser chip is coupled with a coil having a thickness of $110\mu\text{m}$ or less.
7. A semiconductor device according to claim 6, wherein said coil is printed and disposed between said first and second substrates.
8. A semiconductor device according to claim 6, wherein said coil provides energy to said condenser chip.
9. A semiconductor device according to claim 1, wherein said semiconductor device is an IC card, and said first and second substrates are a first and a second IC substrates, respectively.
10. A semiconductor device according to claim 9, wherein said condenser chip is connected with said first substrate by using a conductive paste.
11. A semiconductor device according to claim 9, wherein said condenser chip is strengthened by using a plate harder than said first and second substrates.



12. A semiconductor device according to claim 9, wherein said condenser chip is disposed inside the circle having a diameter equal to the length of the short side of the card and having the center placed at the center of the card.
13. A semiconductor device according to claim 9, wherein said condenser chip is disposed in said IC card so that the neutral plane of said IC card is positioned between an upper surface and a rear surface of said condenser chip.
14. A semiconductor device according to claim 9, wherein material of said first and second substrates is PET.
15. A semiconductor device according to claim 9, wherein said condenser chip is disposed in a region within $55\mu\text{m}$ above and below the neutral plane of said IC card.
16. A semiconductor device according to claim 9, wherein a photograph of a holder of said IC card is disposed over said condenser chip.
17. A semiconductor device according to claim 9, wherein said IC card is a contact type card.
18. A semiconductor device comprising,
 - a flexible first substrate;
 - a condenser chip having a thickness of $110\mu\text{m}$ or less and disposed over said first substrate connected with by using conductive paste;
 - an integrated circuit chip coupled with said condenser chip; and
 - a flexible second substrate disposed opposite to said first substrate so that said condenser chip and integrated circuit chip are interposed between said first and second substrates.
19. A semiconductor device comprising, a condenser chip having a thickness of $110\mu\text{m}$ or less, an integrated circuit chip coupled with said condenser chip, and a flexible first and second substrates, and a thin plate harder than said first and second substrates so as to cover said condenser chip.
20. A semiconductor device comprising, a condenser chip having a thickness of $110\mu\text{m}$ or less, an integrated circuit chip coupled with said condenser chip, and a first and second flexible substrates interposing said condenser chip and integrated circuit chip between them, wherein said condenser chip is disposed in said semiconductor device so that the neutral plane of said semiconductor device is disposed between an upper surface and a lower surface of said condenser chip.



21. A semiconductor device according to claim 20, wherein said semiconductor device is an IC card, and said first and second substrates are a first and a second IC substrates, respectively.
22. A semiconductor device according to claim 21, wherein said condenser chip is strengthened by using a plate harder than said first and second substrates.
23. A semiconductor device according to claim 21, wherein material of said first and second substrates is PET.
24. A semiconductor device according to claim 21, wherein a photograph of a holder of said IC card is disposed over said condenser chip.
25. A semiconductor device according to claim 21, wherein said semiconductor device includes plural integrated circuit chips.
26. A semiconductor device according to claim 21, wherein said condenser chip includes a titanium oxide film as a dielectric film of the condenser.
27. A semiconductor device according to claim 21, wherein said condenser chip includes a titanium or platinum film as an electrode of the condenser.
28. A semiconductor device according to claim 21, wherein said condenser chip is coupled with a coil having a thickness not greater than $110\mu\text{m}$.
29. A semiconductor device according to claim 21, wherein said coil is printed and disposed between said first and second substrates.
30. A semiconductor device according to claim 21, wherein said coil provides energy to said condenser chip.
31. A semiconductor device comprising, a condenser chip having a thickness not greater than $110\mu\text{m}$ and including a circuit element portion and a condenser portion, a coil of a thickness not greater than $110\mu\text{m}$ which provides energy to said condenser portion, and a first and second substrates interposing said condenser chip and coil between them.
32. A semiconductor device according to claim 31, wherein said coil is printed.



33. A semiconductor device according to claim 31, wherein said semiconductor device is an IC card and said first and second substrates are a first IC substrate and a second IC substrate, respectively.
34. A semiconductor device according to claim 33, wherein material of said first and second substrates are PET.
35. A semiconductor device according to claim 33, wherein said condenser chip is disposed at a portion within $55\mu\text{m}$ above and below the neutral plane of said IC card.
36. A semiconductor device including a circuit element portion and a condenser portion, wherein a condenser chip having a thickness not greater than $110\mu\text{m}$ and a first and a second flexible substrates are disposed in said condenser portion so that said condenser chip is interposed between said first and second flexible substrates is covered with a thin plate harder than said first and second flexible substrates.
37. A semiconductor device according to claim 36, wherein said semiconductor device is an IC card and said first and second flexible substrates are a first and a second card substrates, respectively.
38. A semiconductor device according to claim 36, wherein material of said first and a second card substrates is PET.
39. A semiconductor device according to claim 36, wherein said condenser chip is disposed at a portion within $55\mu\text{m}$ above and below the neutral plane of said IC card.
40. A semiconductor device according to claim 36, wherein said condenser chip is coupled with a coil having a thickness not greater than $110\mu\text{m}$.
41. A semiconductor device according to claim 40, wherein said coil is printed.
42. A semiconductor device according to claim 40, wherein said coil provides energy to said condenser chip.
43. A semiconductor device according to claim 36, wherein said thin plate is selectively disposed in a predetermined portion of said IC card.



44. A semiconductor device comprising;
a condenser chip having a thickness not greater than $110\mu\text{m}$ and having a circuit element portion and a condenser portion;
a first and a second flexible substrates interposing said condenser chip between them; and
a photograph disposed over said first or second flexible substrate.
45. A semiconductor device according to claim 44, wherein said semiconductor device is an IC card.
46. A semiconductor device according to claim 45, wherein material of said first and a second card substrates is PET.
47. A semiconductor device according to claim 45, wherein said condenser chip is disposed at a portion within $55\mu\text{m}$ above and below the neutral plane of said IC card.
48. A semiconductor device according to claim 45, wherein said condenser chip is coupled with a coil having a thickness not greater than $110\mu\text{m}$.
49. A semiconductor device according to claim 48, wherein said coil is printed and disposed between said first and second card substrates.
50. A semiconductor device according to claim 48, wherein said coil provides energy to said condenser chip.
51. A semiconductor device according to claim 45, wherein said thin plate is partially formed in said IC card.
52. A semiconductor device comprising, a condenser chip having a thickness not greater than $110\mu\text{m}$ and including a circuit element portion and a condenser portion, a coil having a thickness of $110\mu\text{m}$ or less and providing energy to said condenser portion, and a first and a second flexible substrates interposing said condenser chip and coil between them, wherein the neutral plane of said semiconductor device is positioned between an upper and a lower surfaces of said condenser chip.
53. A semiconductor device comprising a condenser chip having a thickness not greater than $110\mu\text{m}$ and including a circuit element portion and a condenser portion, a first and a second flexible substrates interposing said condenser chip between them, and a thin plate harder than said first and second flexible substrates, wherein the neutral plane of said semiconductor device is positioned between an upper and a lower surfaces of said condenser chip.



54. A semiconductor device comprising, a condenser chip having a thickness not greater than $110\mu\text{m}$ and including a circuit element portion and a condenser portion, a first and a second flexible substrates interposing said condenser chip between them, and a photograph disposed over a position of a surface of said first or second flexible substrate wherein said position corresponds to a position of said condenser chip, and wherein said condenser chip is disposed in said semiconductor device such that a neutral plane of said semiconductor device is positioned between an upper and a lower surfaces of said condenser chip.

Abstract of the Disclosure

Condenser (114), coil (115) and thin-thickness integrated circuit (312) are placed between the upper cover sheet (117) and the lower cover sheet (118), and adhesive (119) is filled into the space among them, whereby a card is fabricated.

Because condenser (114), coil (115) and thin-thickness integrated circuit (312) are extremely thin, the resulting semiconductor device is strong to bending and highly reliable at a low cost.