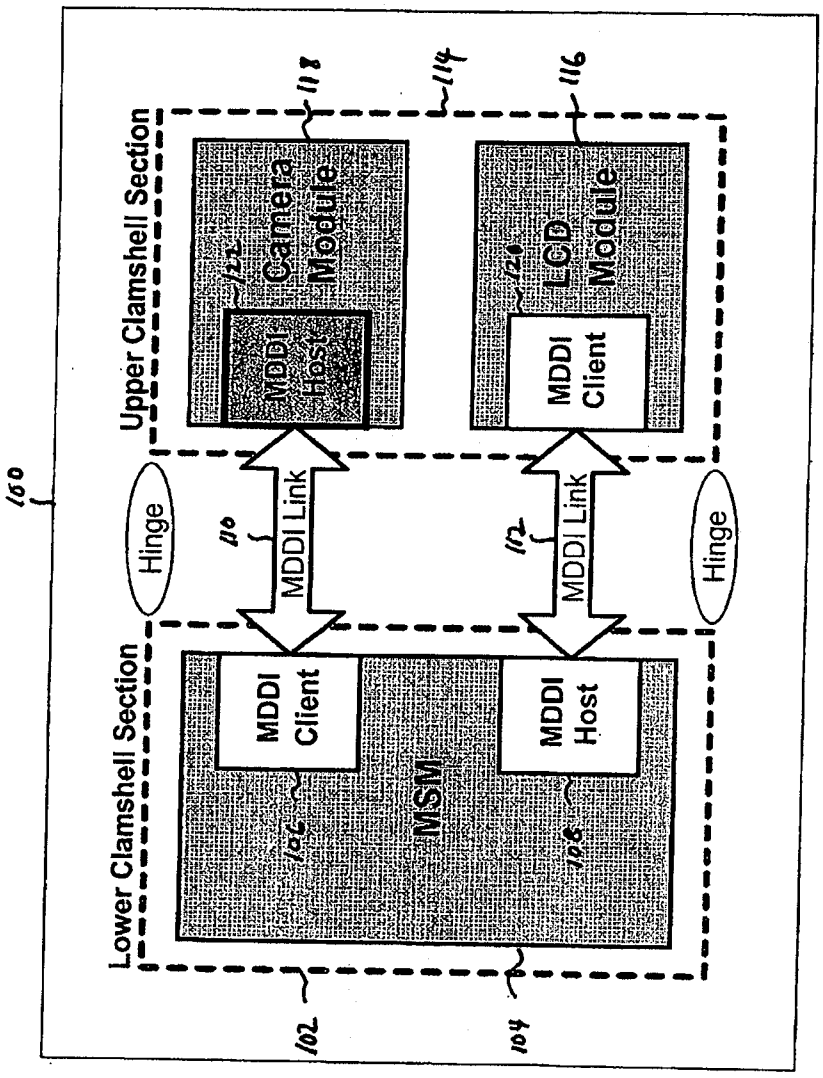

FIG. 1

BEST AVAILABLE COPY

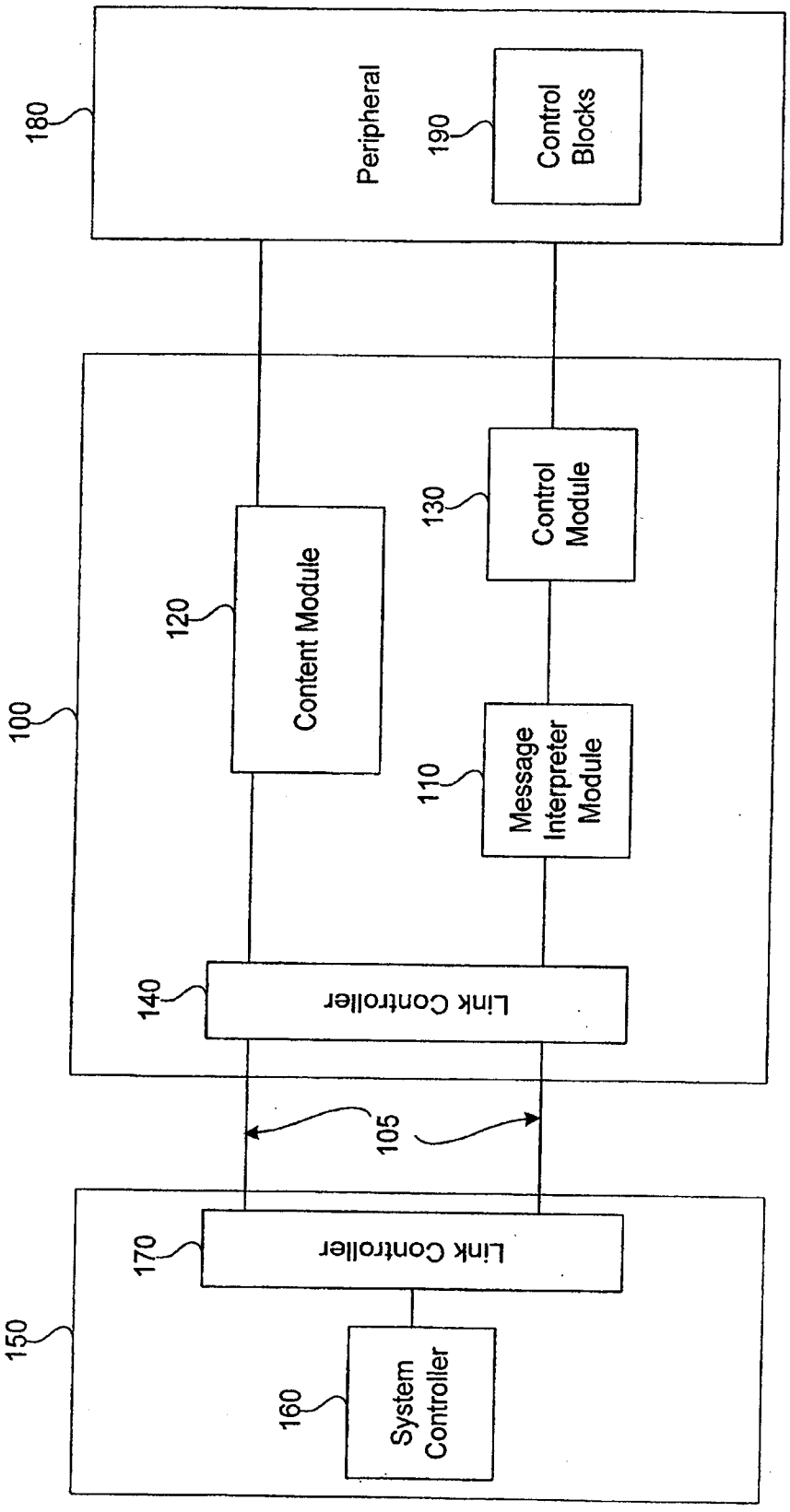
180
Peripheral
190
Control Blocks
100
120
Content Module
130
Control Module
110
Message Interpreter Module
140
Link Controller
105
150
170
Link Controller
160
System Controller

FIG. 1A

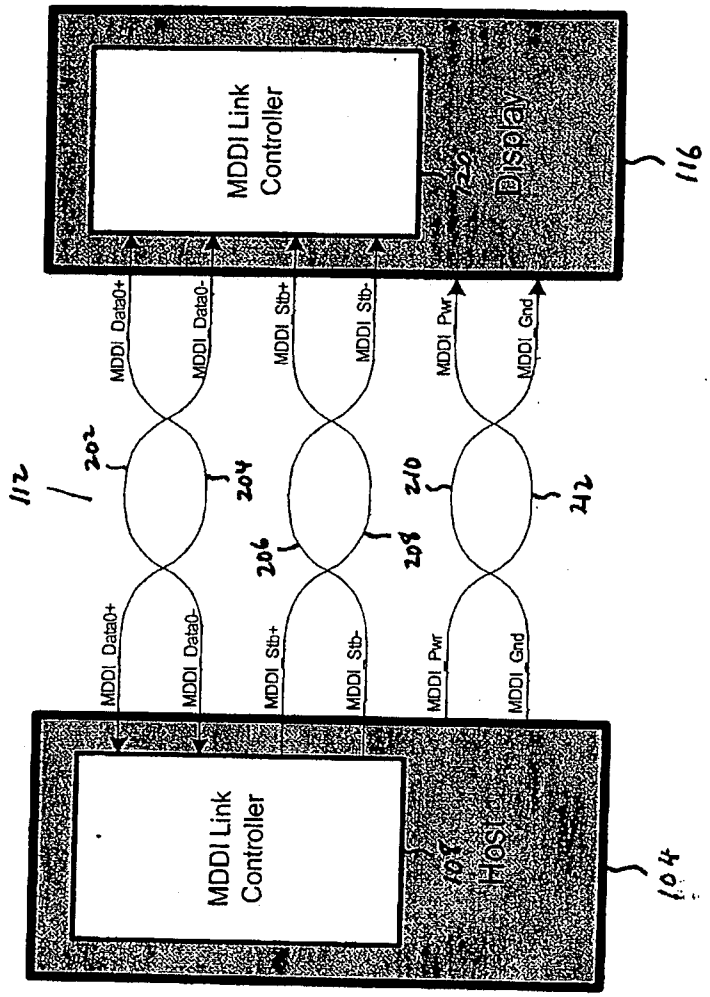

FIG. 2

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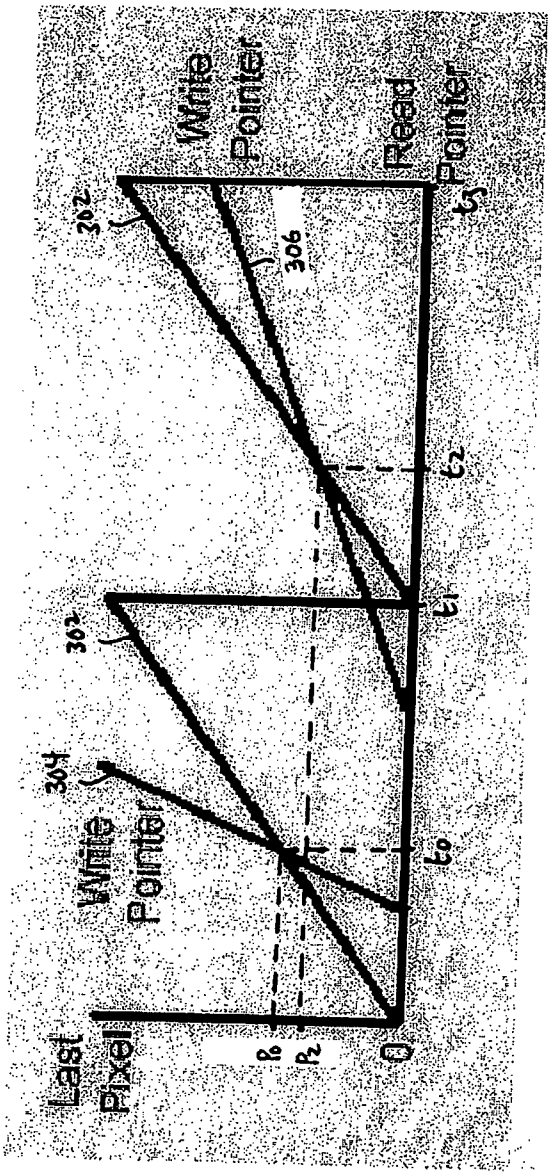

FIG. 3

BEST AVAILABLE COPY

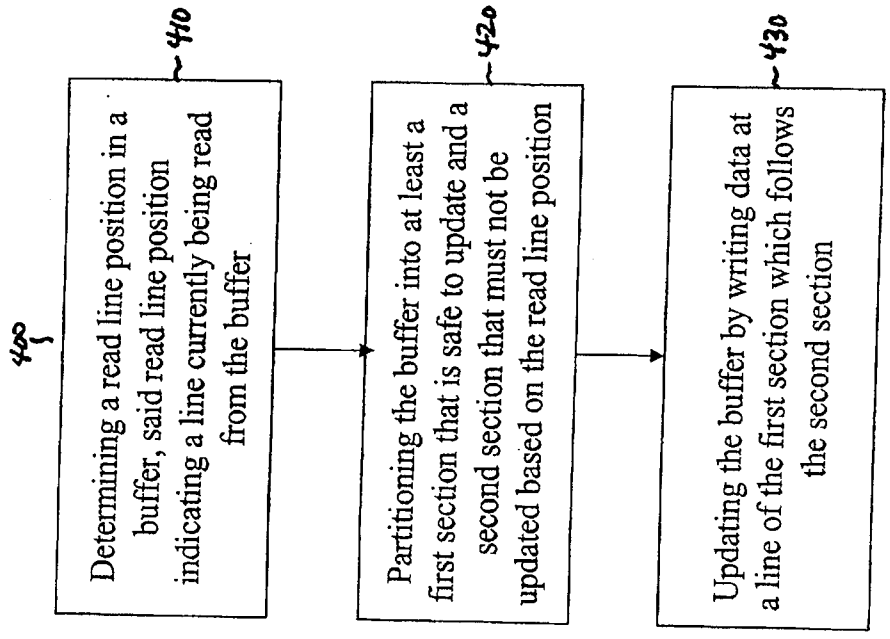
400
410
Determining a read line position in a buffer, said read line position indicating a line currently being read from the buffer
420
Partitioning the buffer into at least a first section that is safe to update and a second section that must not be updated based on the read line position
430
Updating the buffer by writing data at a line of the first section which follows the second section

FIG. 4

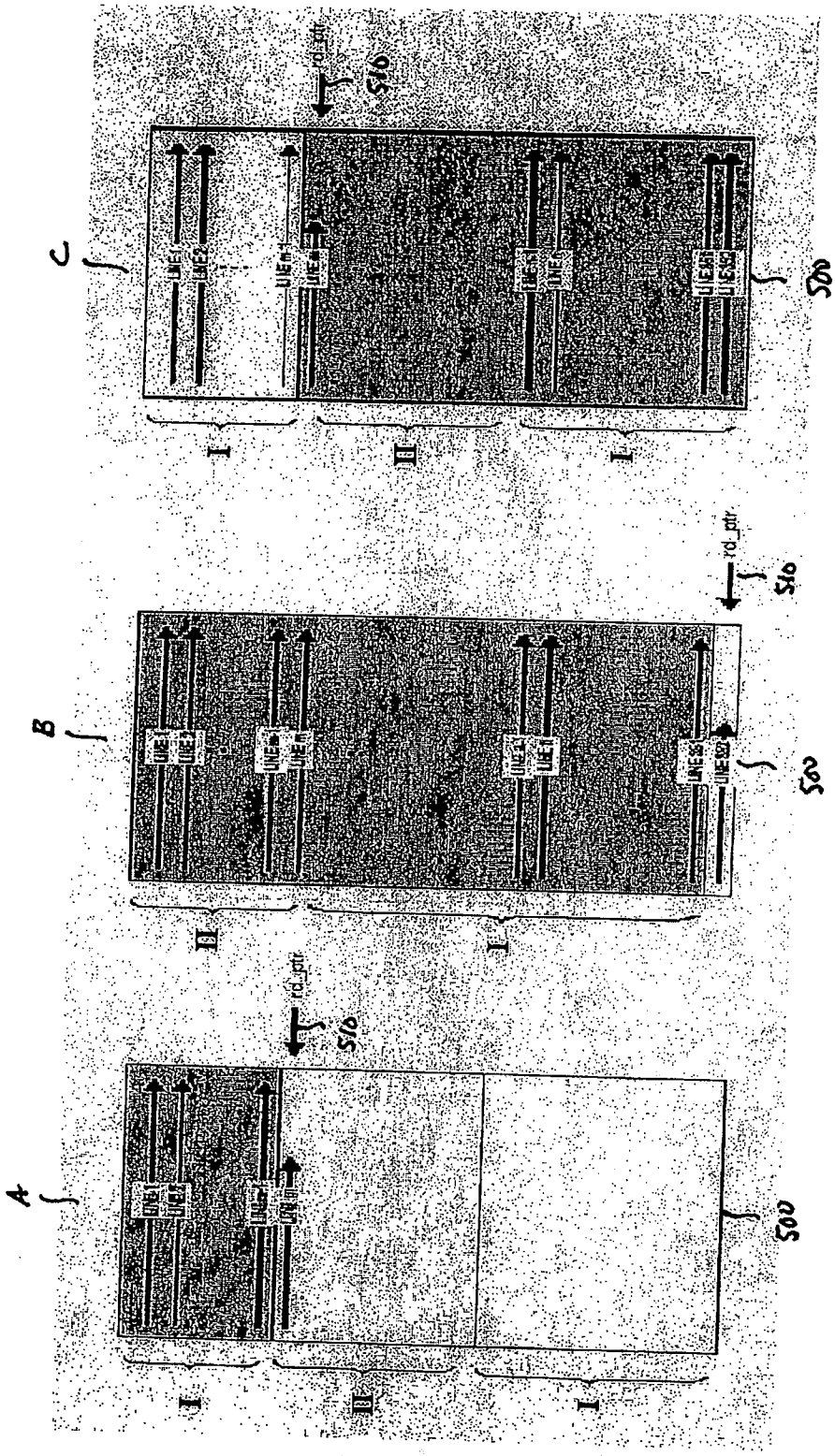

FIG. 5

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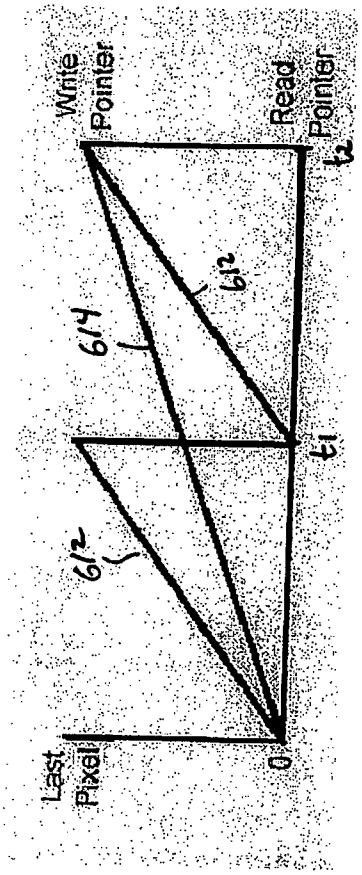

FIG. 6A

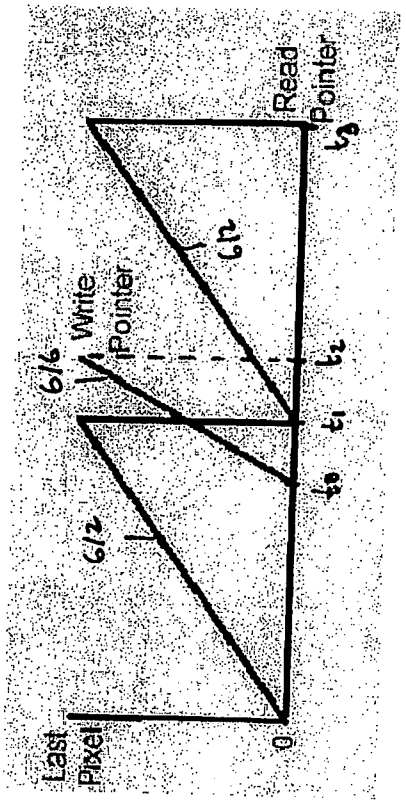

FIG. 6B

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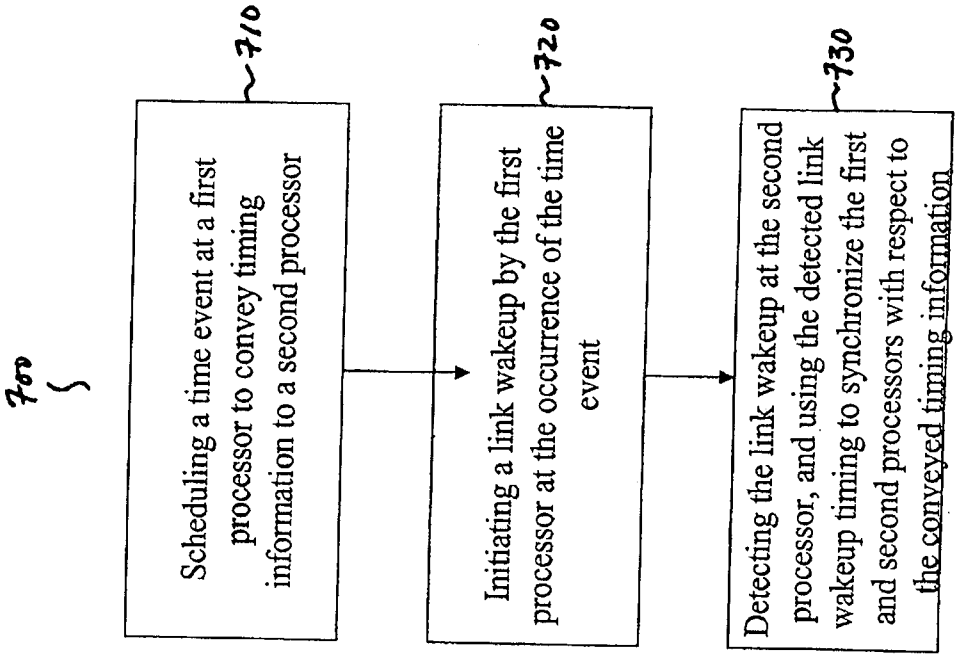
700
710
Scheduling a time event at a first processor to convey timing information to a second processor
720
Initiating a link wakeup by the first processor at the occurrence of the time event
730
Detecting the link wakeup at the second processor, and using the detected link wakeup timing to synchronize the first and second processors with respect to the conveyed timing information

FIG. 7

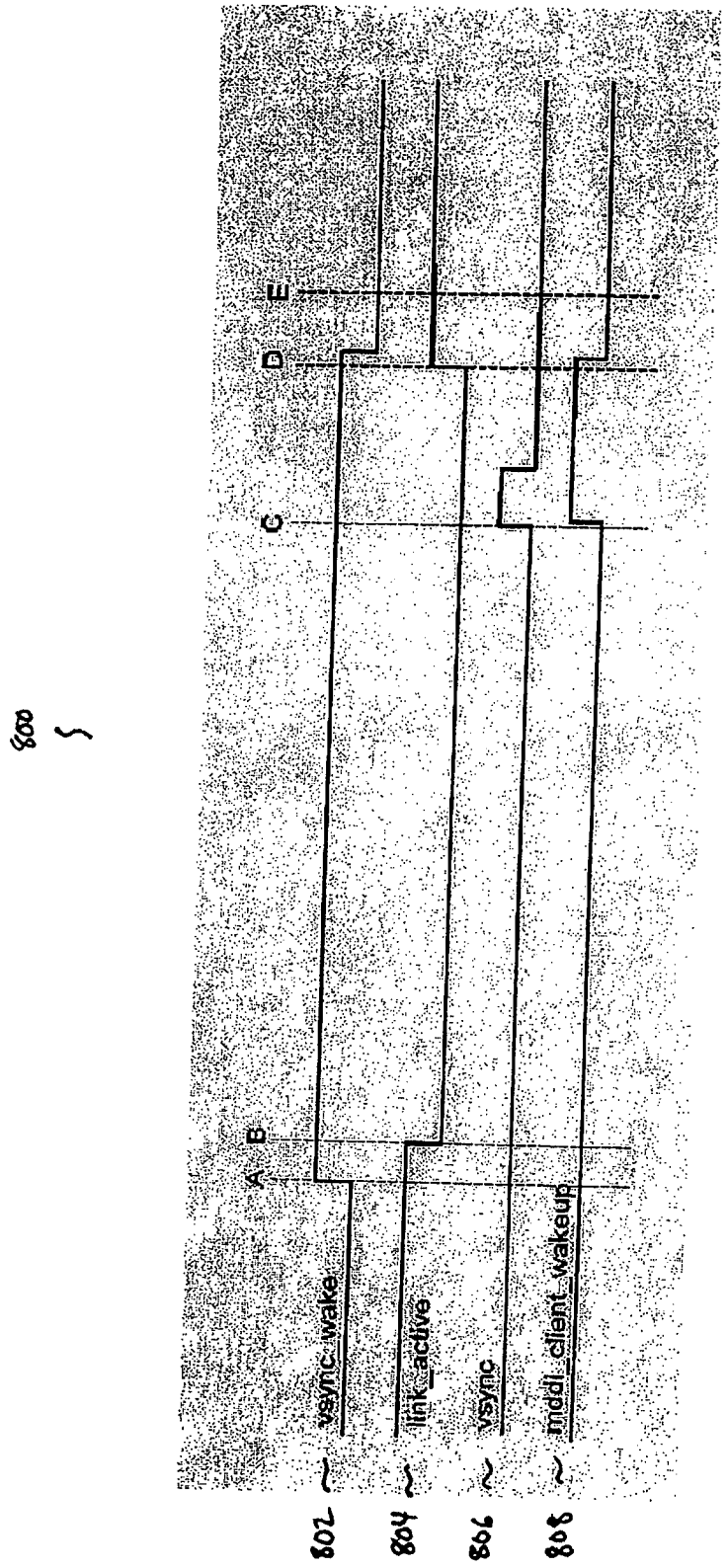

FIG. 8

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