

EMBEDDED MRAMS INCLUDING DUAL READ PORTS

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Abstract of the Disclosure

Processing equipment with embedded MRAMs, and a method of fabricating, including a data processing device fabricated on a semiconductor chip with MRAM cells
10 fabricated on the chip to form one to all of the memories on the chip. Also included is a dual bank memory in communication with the data processing device and circuitry coupled to the data processing device and the dual bank memory for providing simultaneous read access
15 to the dual bank memory.

Fig 1.