

THIS SHEET HAS BEEN
REPLACED

ZERO INSERTION FORCE PIN GRID ARRAY SOCKET

ABSTRACT OF THE INVENTION

5 A zero insertion force pin grid array socket includes a cover slidably engaged to a base and having an array of holes formed therethrough. The base has an array of corresponding passages, the passages receiving pins of an IC package inserted through corresponding holes of the cover. Contacts are mounted within the passages and are mounted at an angle askew to the sides of the base in order to provide a high density
10 interstitial array. The contacts include torsional beams which deflect upon the engagement of the pin of the IC package which have a surface area of at least thirty percent of the entire surface area of contacts. The contacts include a wiping area which is plated which has
15 a surface area of less than a fortieth of the entire surface area of the contact.

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