FLAT DISPLAY DEVICE AND DISPLAY BODY DRIVING DEVICE

Abstract

Signal management control units 47_1 - 47n of respective scan drivers LSI in an LCD module are cascade-connected and each have the same construction. A detected signal of the signal management control unit 47_1 is a data signal latch clock LP applied to a terminal CKB_1. A detected signal of the signal management control unit 47_2 is a frame start signal SP applied to a terminal CKB_2. A detected signal of the signal management control unit 47n is an AC-transforming clock FR applied to a terminal CKBn. The signal management control unit 47_1 includes a signal stop detection circuit 48 serving as a signal detection means for detecting a stop of the detected signal and a sequence processing circuit 51 consisting of a signal delay circuit 49 and a logic circuit 50. When stopping oscillations of, e.g., the frame start signal SP, outputs T_1 - Tn of the circuit 51 change to an L level. Hence, a display-off signal DF of the LCD module assumes the L level. A liquid crystal panel is forcibly set in a display-off mode. As a result, even if the frame start signal SP is stopped due to some cause, a liquid crystal application voltage is set down to zero. It is therefore possible to avoid a liquid crystal DC drive and prevent a deterioration of the liquid crystal.

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