

METHOD AND CIRCUIT FOR PROTECTING POWER CIRCUITS
AGAINST SHORT CIRCUIT AND OVER CURRENT FAULTS

ABSTRACT OF THE DISCLOSURE

A fault protection circuit for protecting IGBT's and other non-latching semiconductor devices in power circuits, for example, power converting/inverting circuits, against phase to phase, phase to earth and shoot through short circuit faults as well as against over current faults. The circuit provides local protection for devices on the high side of such power circuits, and transfers the fault to the low side where it is detected and appropriate control circuitry is activated to latch the fault, thereby avoiding the need for isolated sensing or feedback to protect the high side devices as well as the complete power circuit.

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