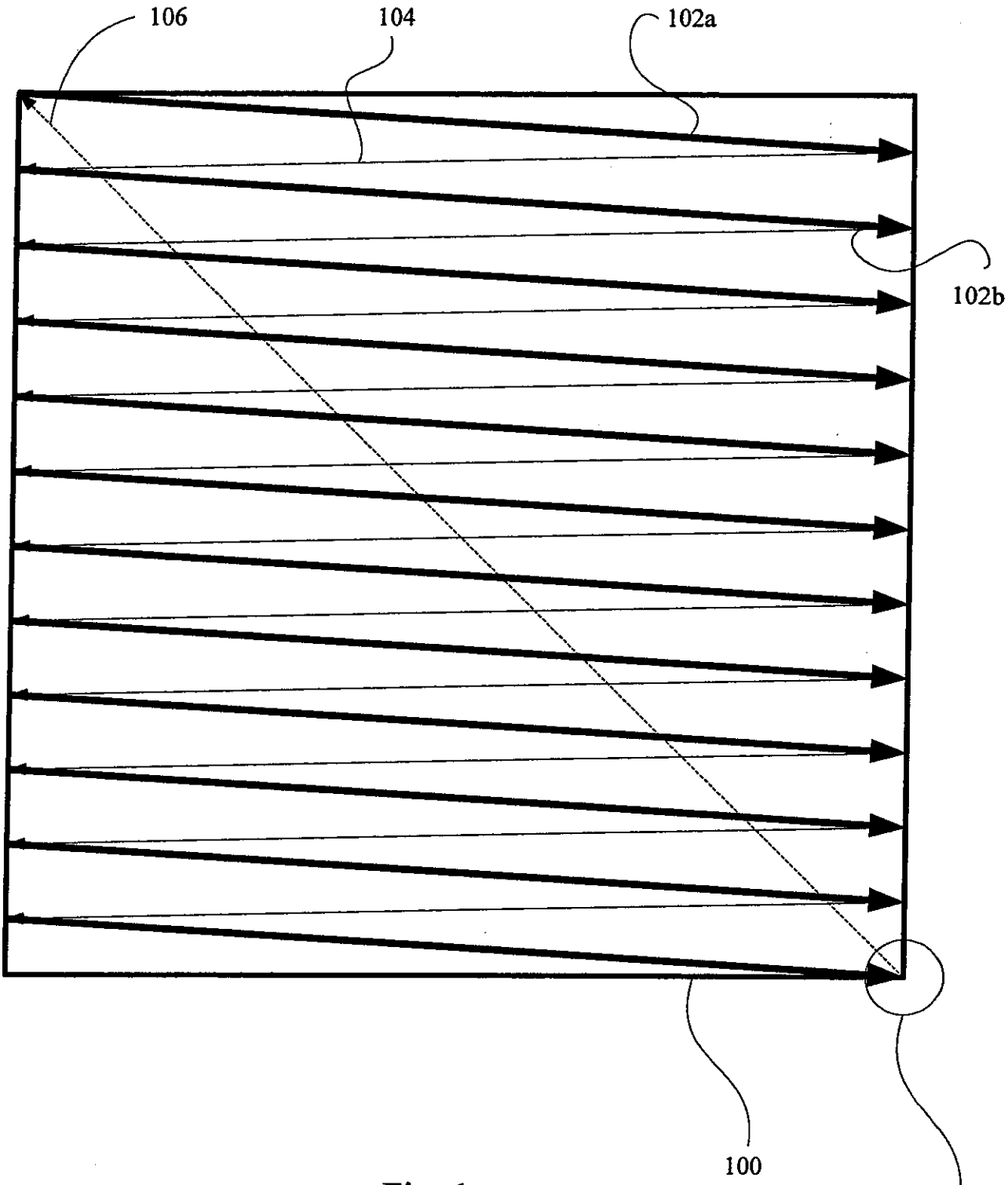


Fig. 1
Prior Art

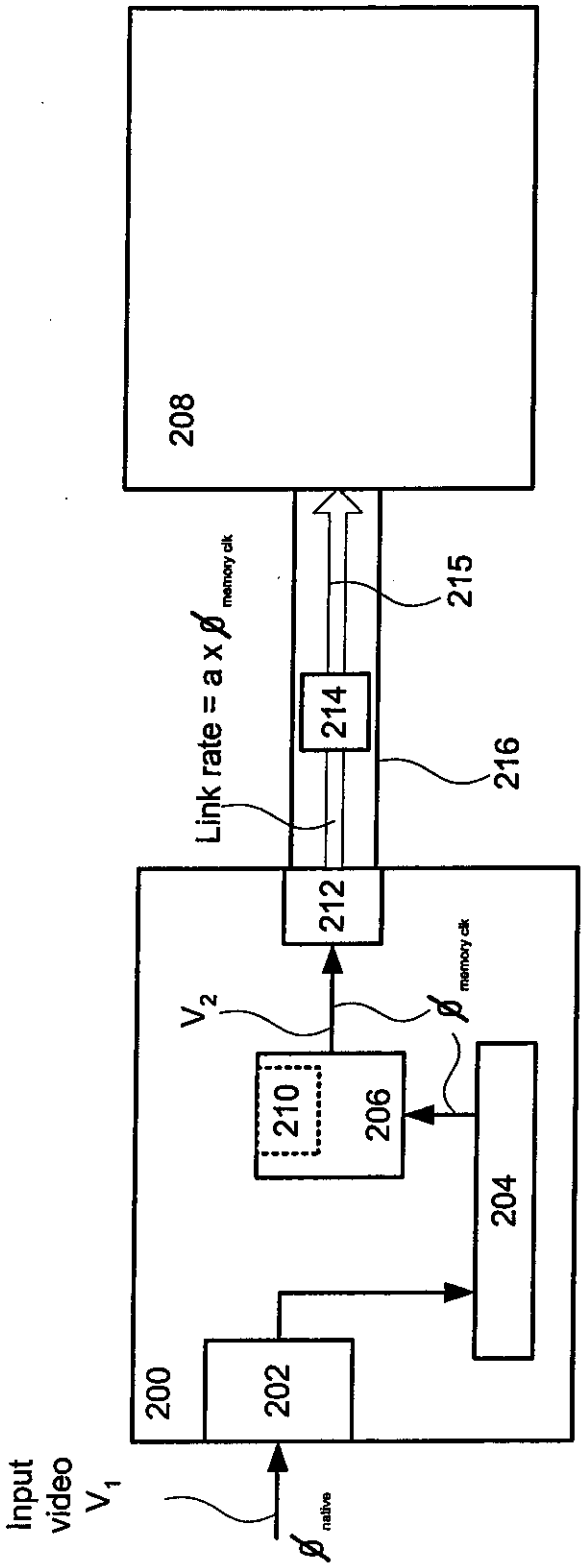

208
215
Link rate = a x $\phi_{memory\ clk}$
214
216
212
V_2
$\phi_{memory\ clk}$
210
206
204
200
202
Input video V_1
ϕ_{native}

Fig. 2

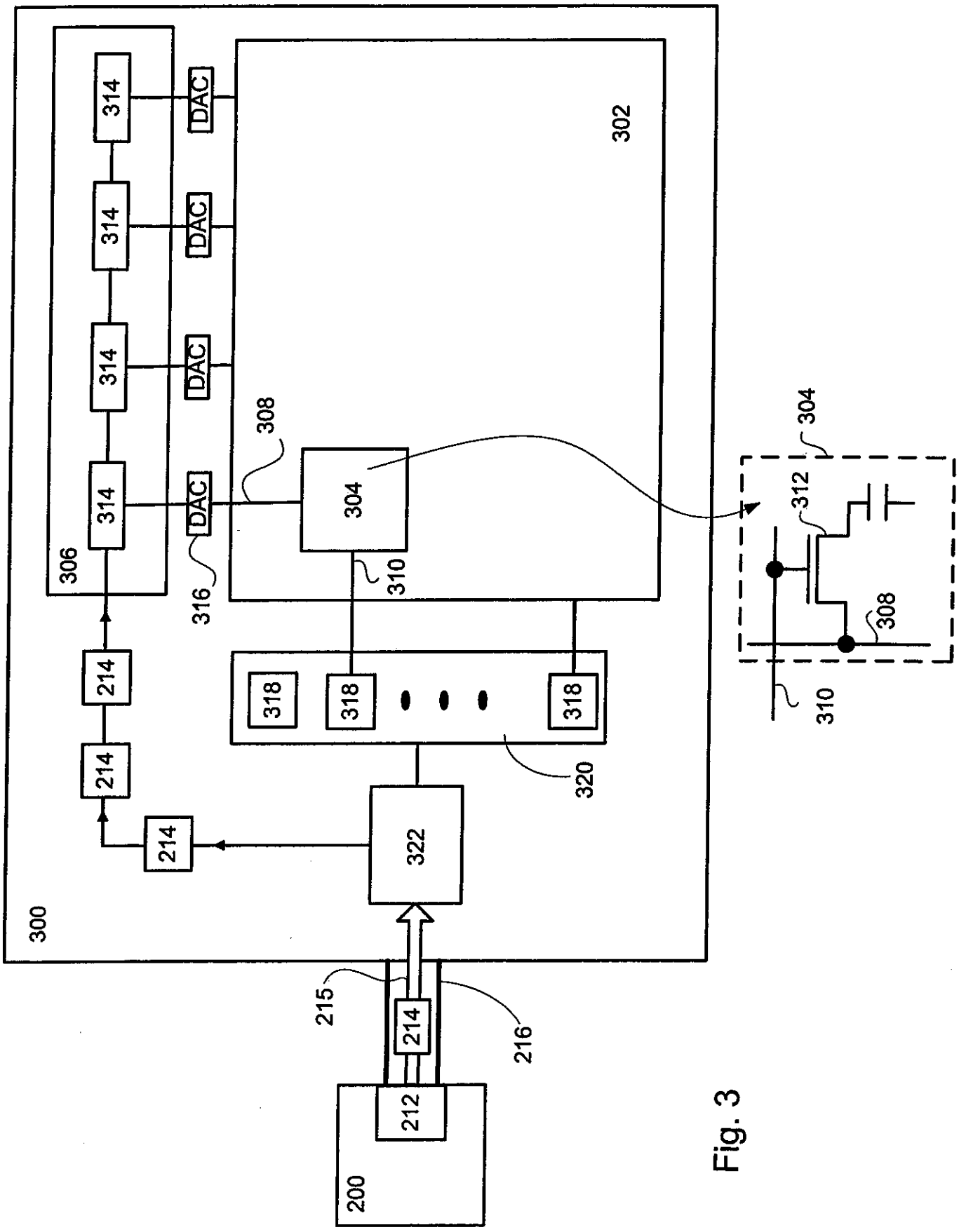

300
302
304
306
308
310
312
314
316
318
320
322
200
212
214
215
216
DAC

Fig. 3

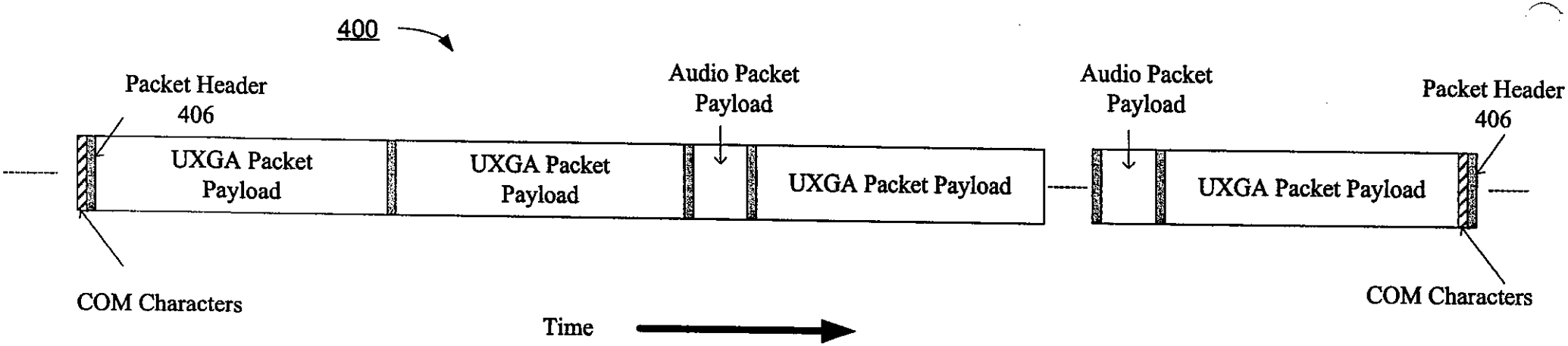


Fig. 4

High-level diagram of link traffic example

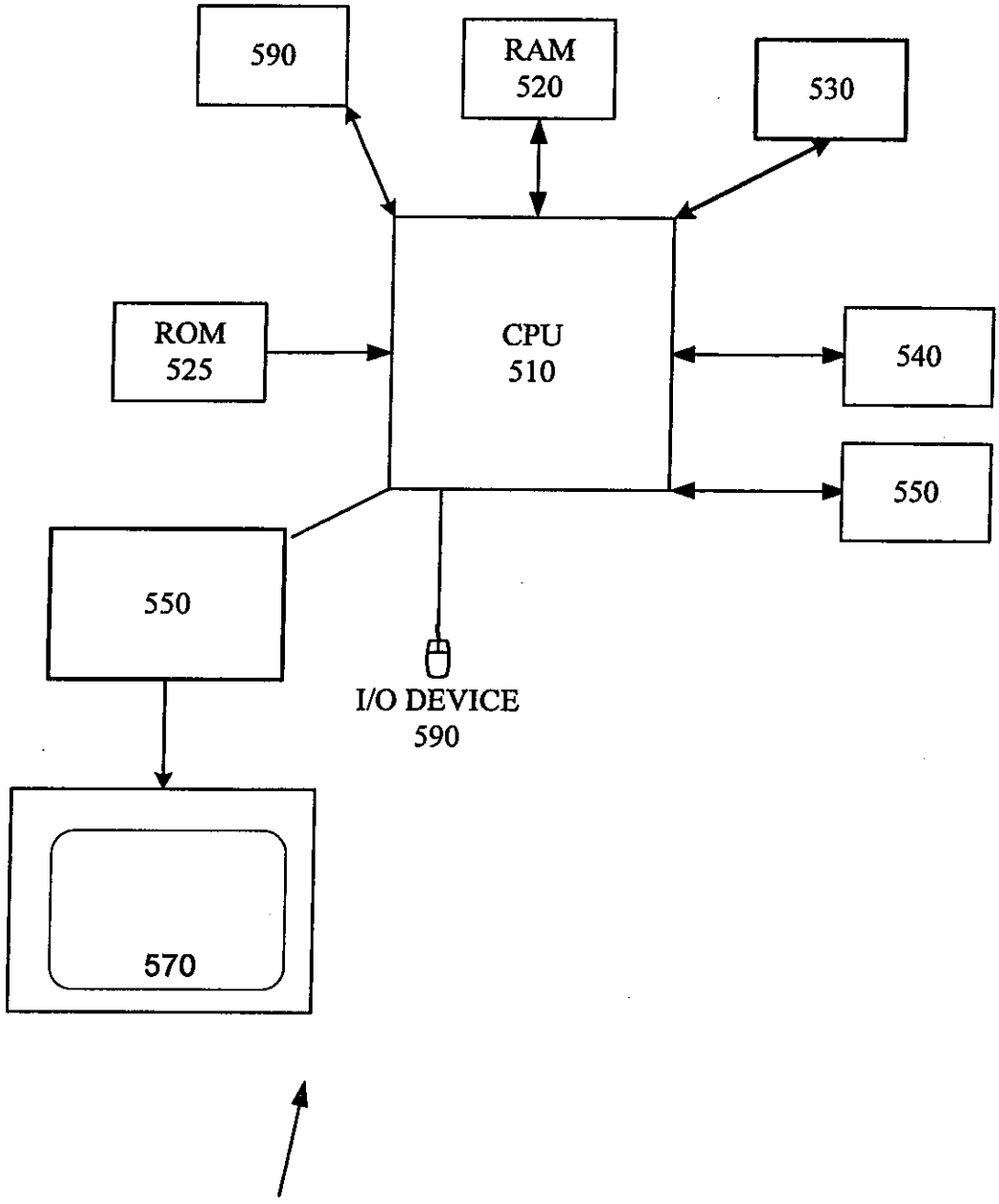

590
RAM
520
530
ROM
525
CPU
510
540
550
550
I/O DEVICE
590
570
500

Fig. 5