

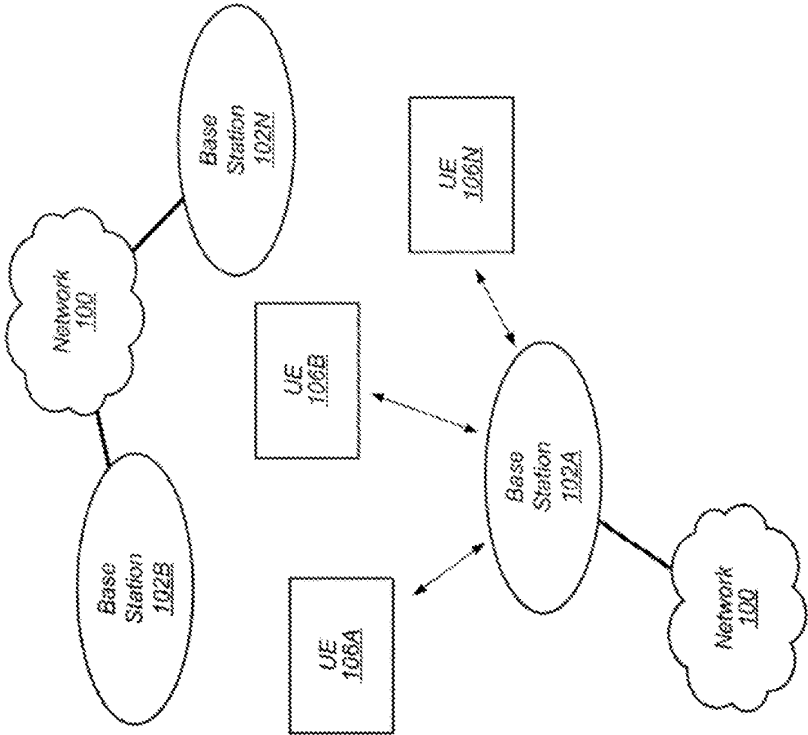


FIG. 1

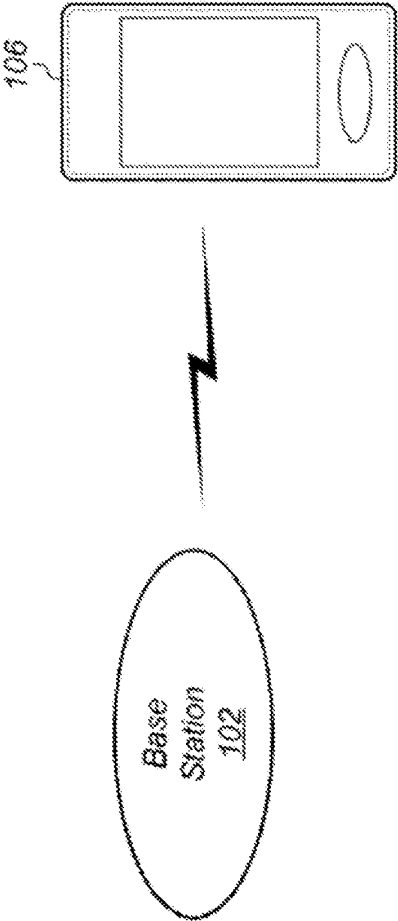


FIG.2

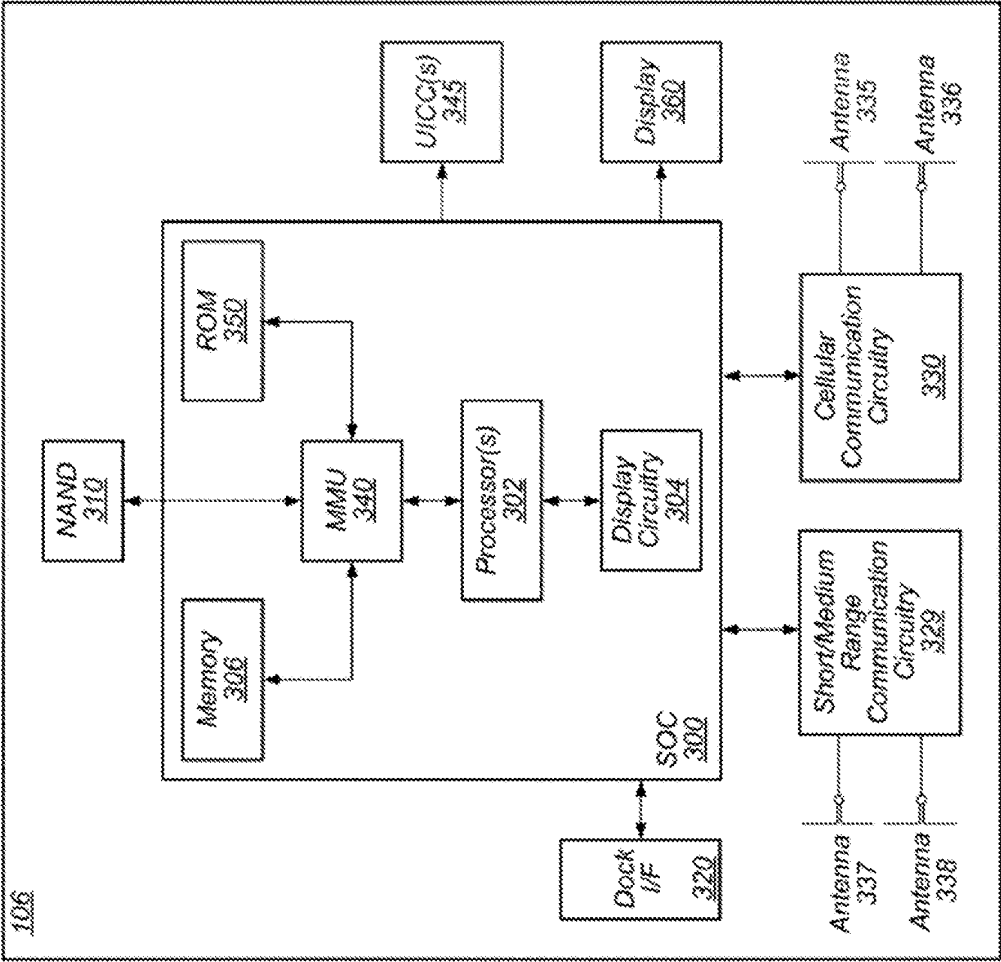


FIG.3

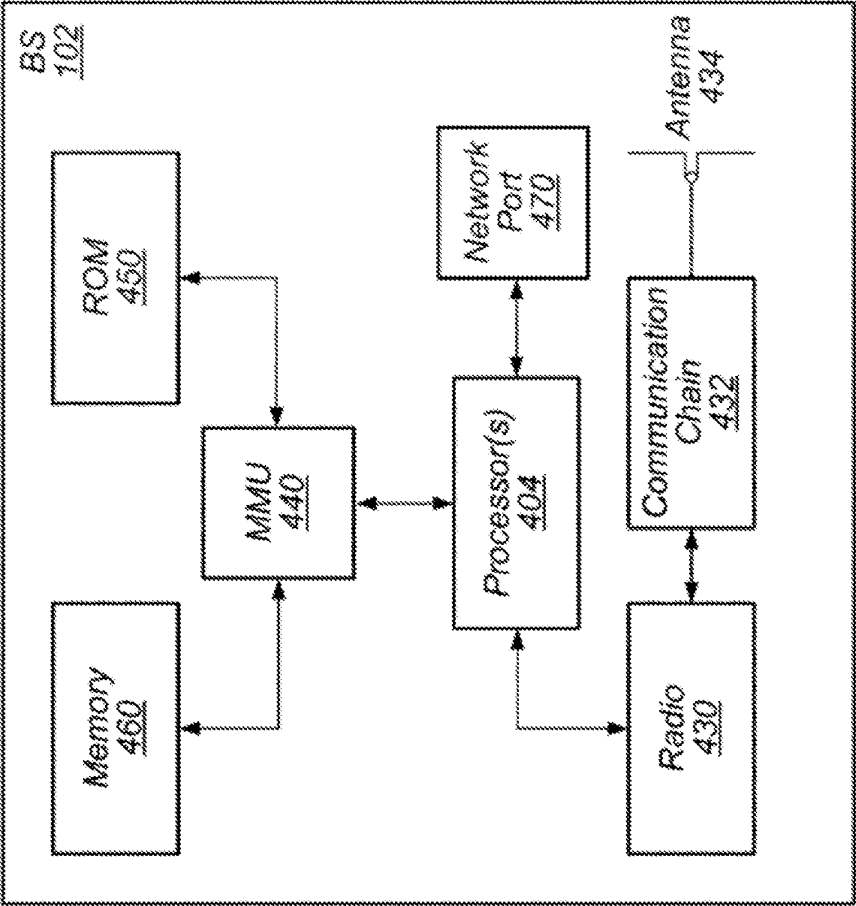


FIG.4

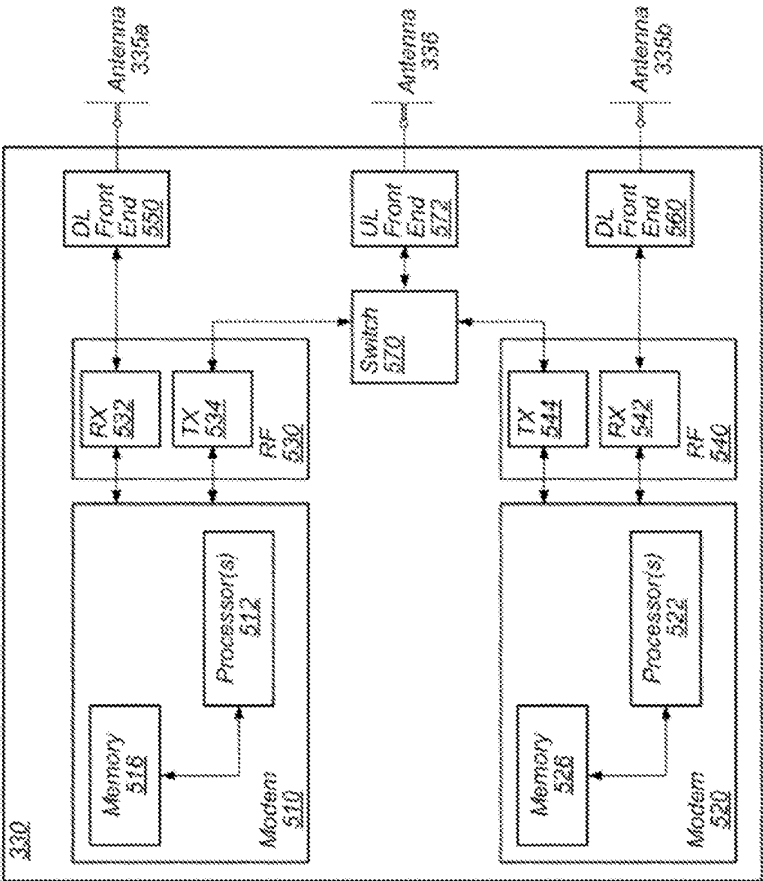


FIG.5

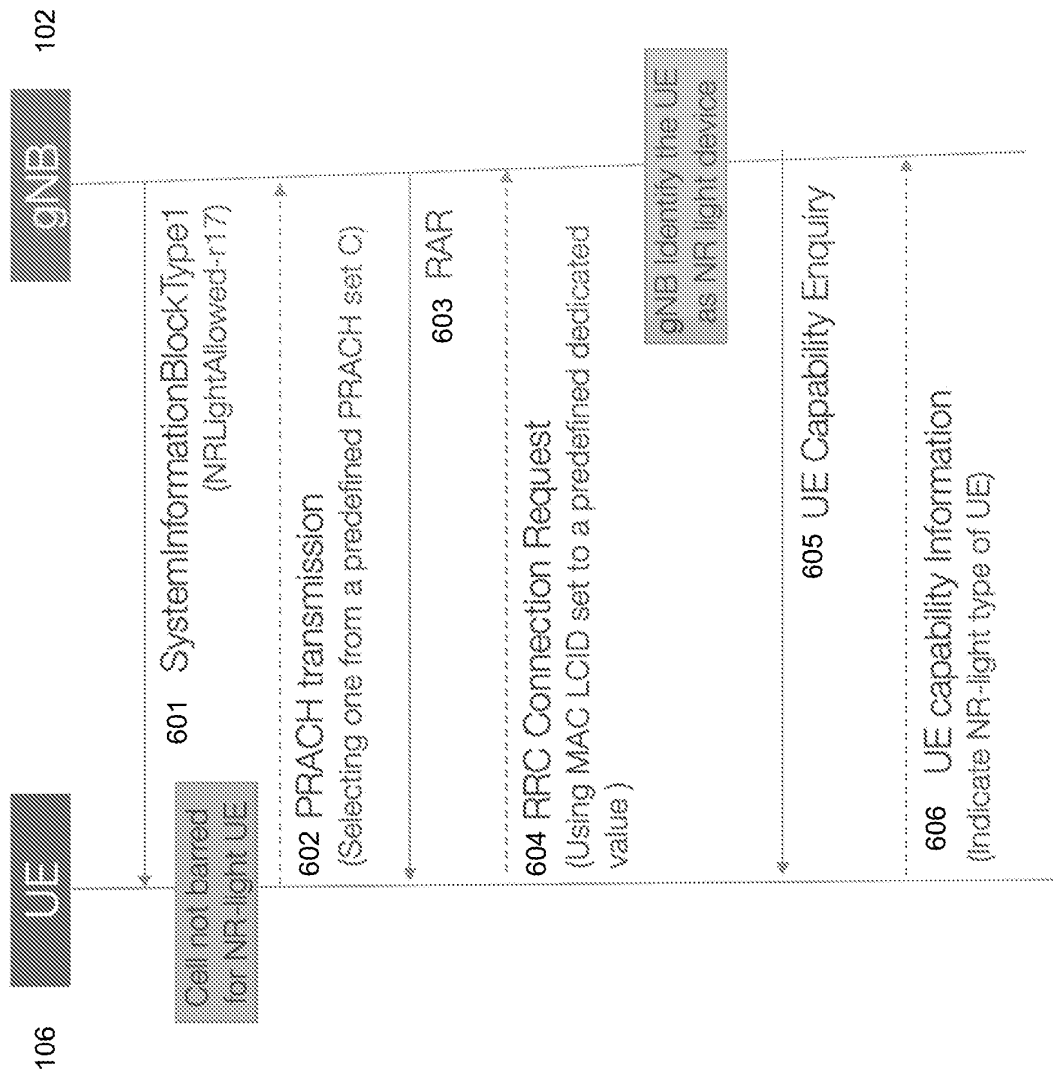


FIG. 6

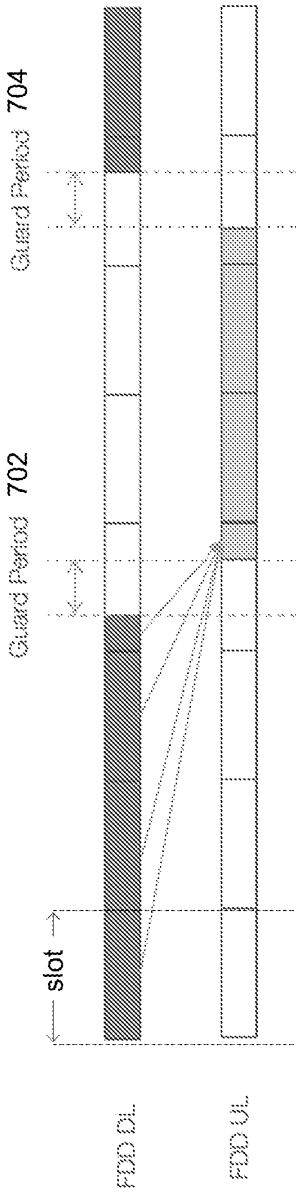


FIG. 7 Tx and Rx switching with Guard Period for Half-Duplex FDD UEs

Guard period configuration index	Guard period configuration	Slot for DL-to-UL	Slot for UL-to-DL
0	DTX in last one or more OFDM symbols in the DL subframe prior to UL transmission	[D U]	[U D]
1	Two slots for guard period	[D G G U]	[U D]
2	Two slots for guard period	[D G U]	[U G D]

'D' denotes a downlink slot, 'U' denotes an uplink slot, 'G' denotes a guard period.

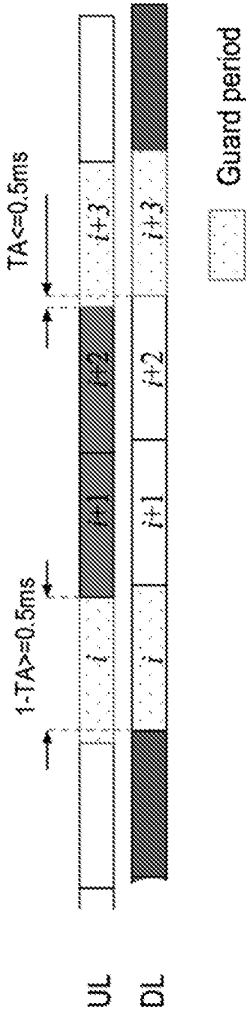


FIG. 8

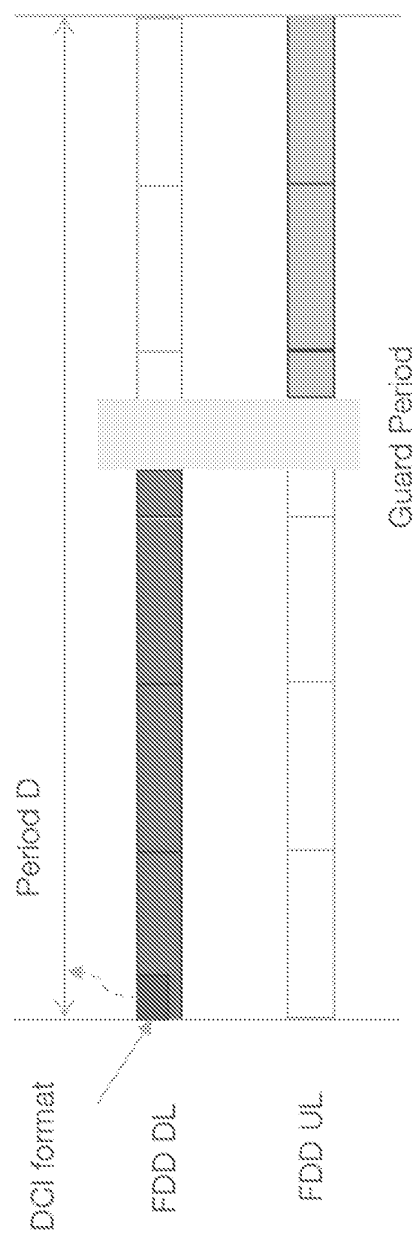


FIG. 9 DL/UL pattern configuration