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(71) Applicant: MICRON TECHNOLOGY INC. [US/US]; 8000 South Federal Way, Boise, ID 83706-9632 (US).

(72) Inventor: LABERGE, Paul, A.; 5772 Ridge Creek Road Ct., Shoreview, MN 55126 (US).

(74) Agent: POTE, Daniel, R.; Snell & Wilmer L.L.P., One Arizona Center, 400 East Van Buren, Phoenix, AZ 85004-2202 (US).

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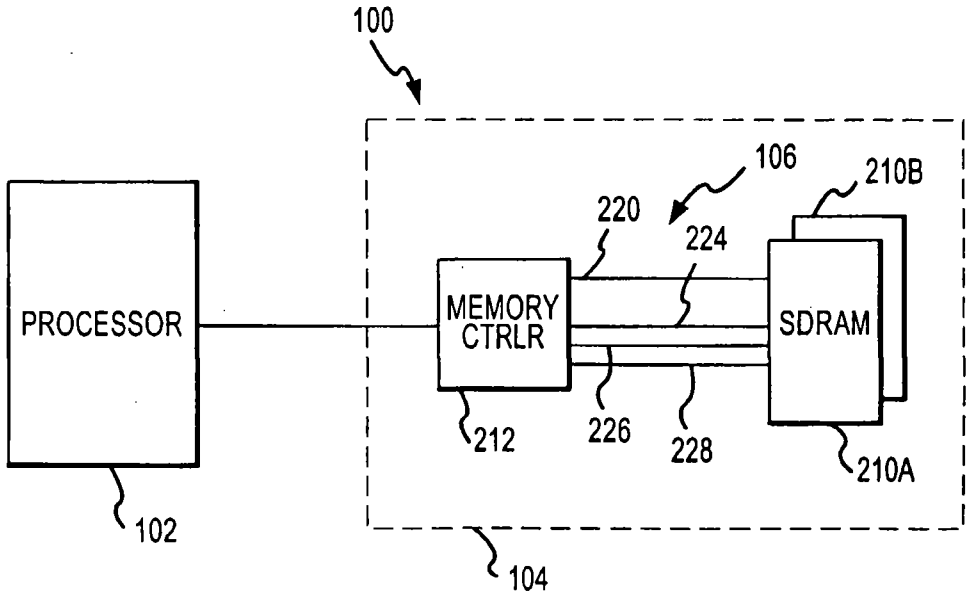
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(54) Title: METHOD AND APPARATUS FOR SUPPLEMENTARY COMMAND BUS IN A COMPUTER SYSTEM



(57) Abstract: An electronic system according to various aspects of the present invention includes a memory having a location-specific command interface and a general command interface. The memory communicates with other components in the system via a main command bus configured to transfer address-specific commands and a supplementary command bus configured to transfer general commands. Commands may be received by the memory simultaneously at the respective interfaces.

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