ABSTRACT OF THE DISCLOSURE

A VARIABLE POWER ADAPTIVE TRANSMITTER

A circuit comprises a delay module that receives a digital transmit signal and that generates a delayed transmit signal. A first digital to analog converter converts the delayed transmit signal to an analog transmit signal. An analog output circuit amplifies the analog transmit signal. An envelope generating module generates an envelope signal based on amplitude information in the digital transmit signal. A supply adjustment module supplies a voltage supply reference when the envelope signal is less than a threshold and boosts the bias voltage of the analog output circuit above the voltage supply reference when the envelope signal is greater than the threshold.

FIG. 6A

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