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(71) Applicant (for all designated States except US): **INTEL CORPORATION** [US/US]; 2200 Mission College Boulevard, Santa Clara, CA 95052 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **HALBERT, John, B.** [US/US]; 15045 Emerald Court, Beaverton, OR 97007

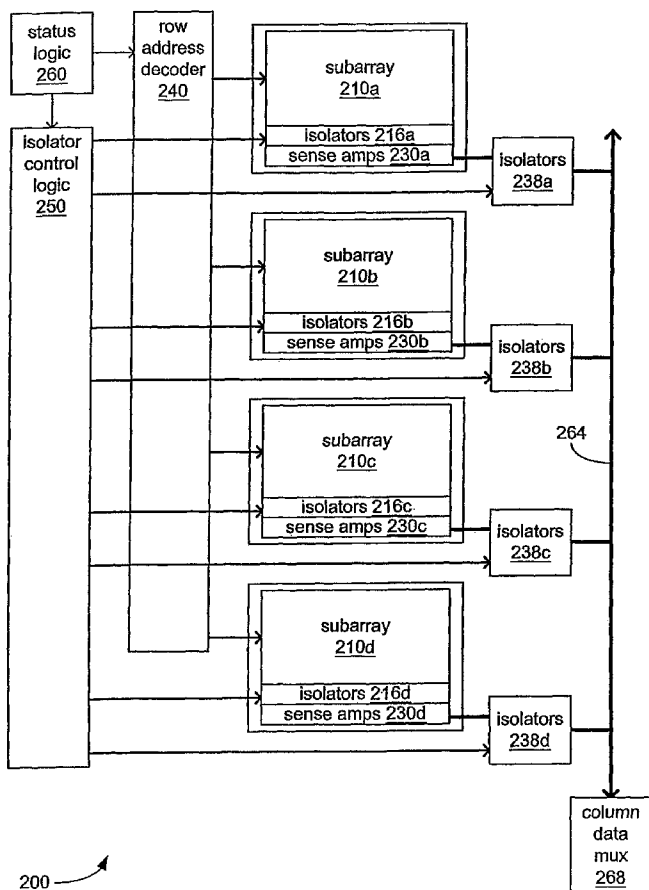
(US). **ELLIS, Robert, M.** [US/US]; 4177 SE Hemlock, Hillsboro, OR 97123 (US). **BAINS, Kuljit, S.** [IN/US]; 9146 52nd Lane NE, Olympia, WA 98516 (US). **FREE-MAN, Chris, B.** [US/US]; 12590 SW Bowmont Street, Portland, OR 97225 (US).

(74) Agent: **VINCENT, Lester, J.**; Blakely, Sokoloff, Taylor & Zafman LLP, 7th Floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025 (US).

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(54) Title: METHOD AND APPARATUS FOR MULTIPLE ROW CACHES PER BANK



(57) Abstract: Provision and use of sets of isolators to enable the caching of the contents of at least one row of memory cells within a subarray of a bank of a memory device by a row of sense amplifiers associated with the subarray to enable faster access to read the contents of that at least one row through a read operation causing the data to read from the row of sense amplifiers versus from the row of memory cells, directly.

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