

Abstract:

Circuit for horizontal scanning of a video display apparatus S provided with an S capacitance switched by a thyristor.

A diode modulator circuit constituted by a switch (TR) in parallel with two diodes (D1, D2) arranged in cascade in the opposite direction, to which a deflection coil (LD) in series with a first S capacitance (CS1) and a winding of a transformer (TR) is connected.

- 5 A second S capacitance (CS2) in series with a thyristor (TH) is connected, at one end, to the first S capacitance and, at the other end, to the common point (PMED) of the two diodes of the modulator, and the thyristor is correctly triggered by insulating its cathode with respect to the common point (PMED) by means of a diode (D4) and by arranging a divider bridge consisting of three resistors (R1, R2, R3) between said common
0 point (PMED) and ground, the gate-cathode space being connected to the terminals of one of the resistors (R2).

Application: inter alia, controlling the size of the image ("zoom effect").

Figure. Television circuit for horizontal scanning, provided with an S capacitance switched by a thyristor.



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