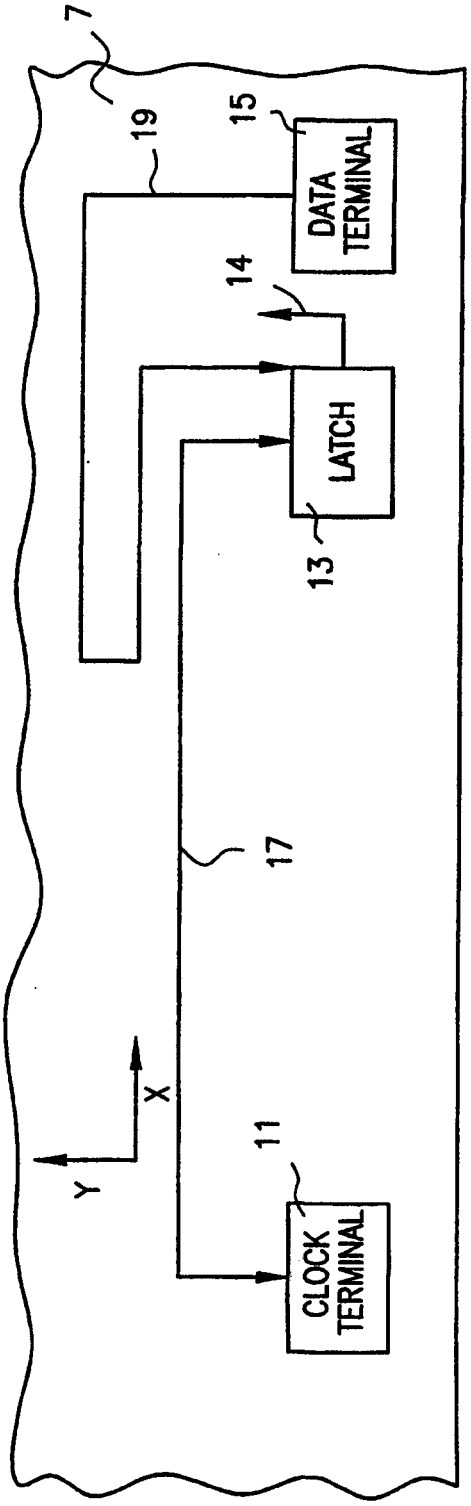

7
19
15
DATA TERMINAL
14
LATCH
13
17
X
Y
11
CLOCK TERMINAL

FIG. 1

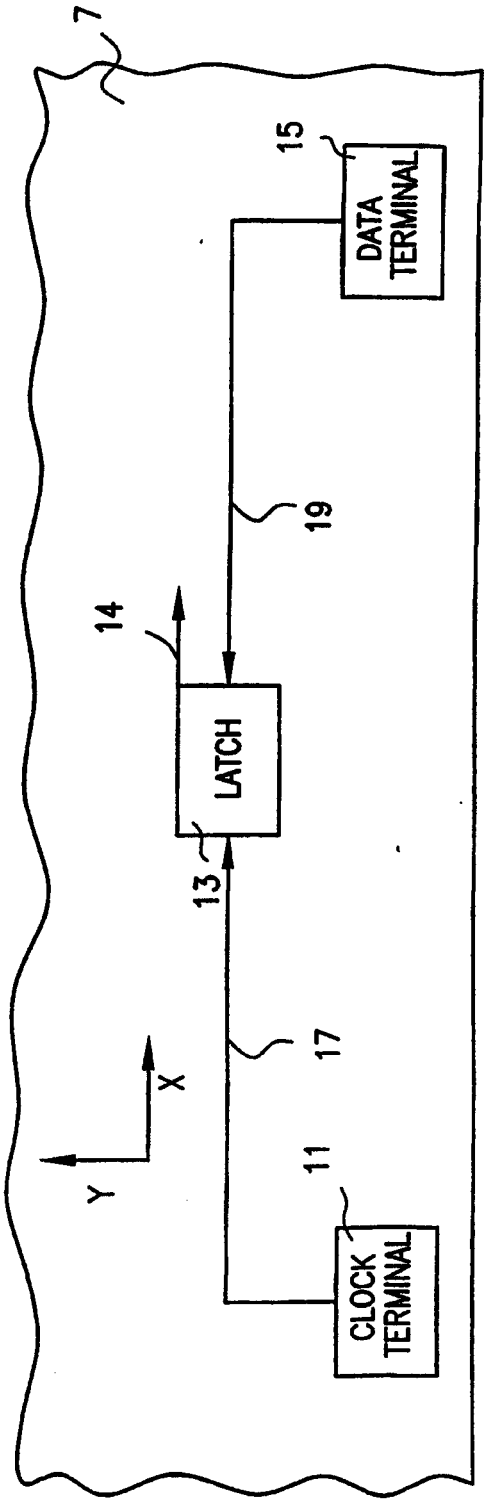

7
15
DATA TERMINAL
19
14
LATCH
13
17
X
Y
11
CLOCK TERMINAL

FIG. 2

FIG. 3

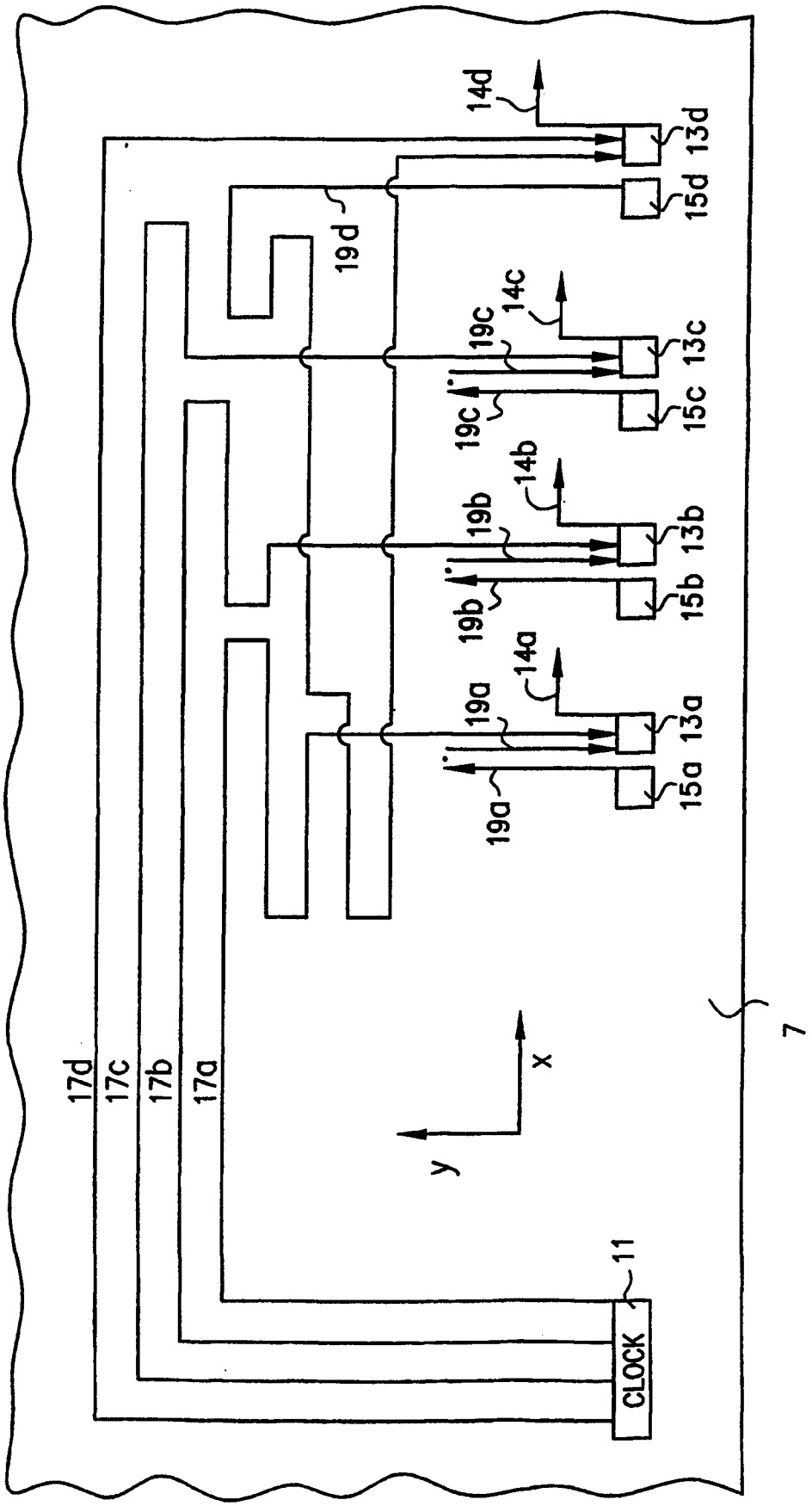

CLOCK
11
17d
17c
17b
17a
19d
19c
19b
19a
14d
14c
14b
14a
13d
13c
13b
13a
15d
15c
15b
15a
x
y
7

FIG. 4

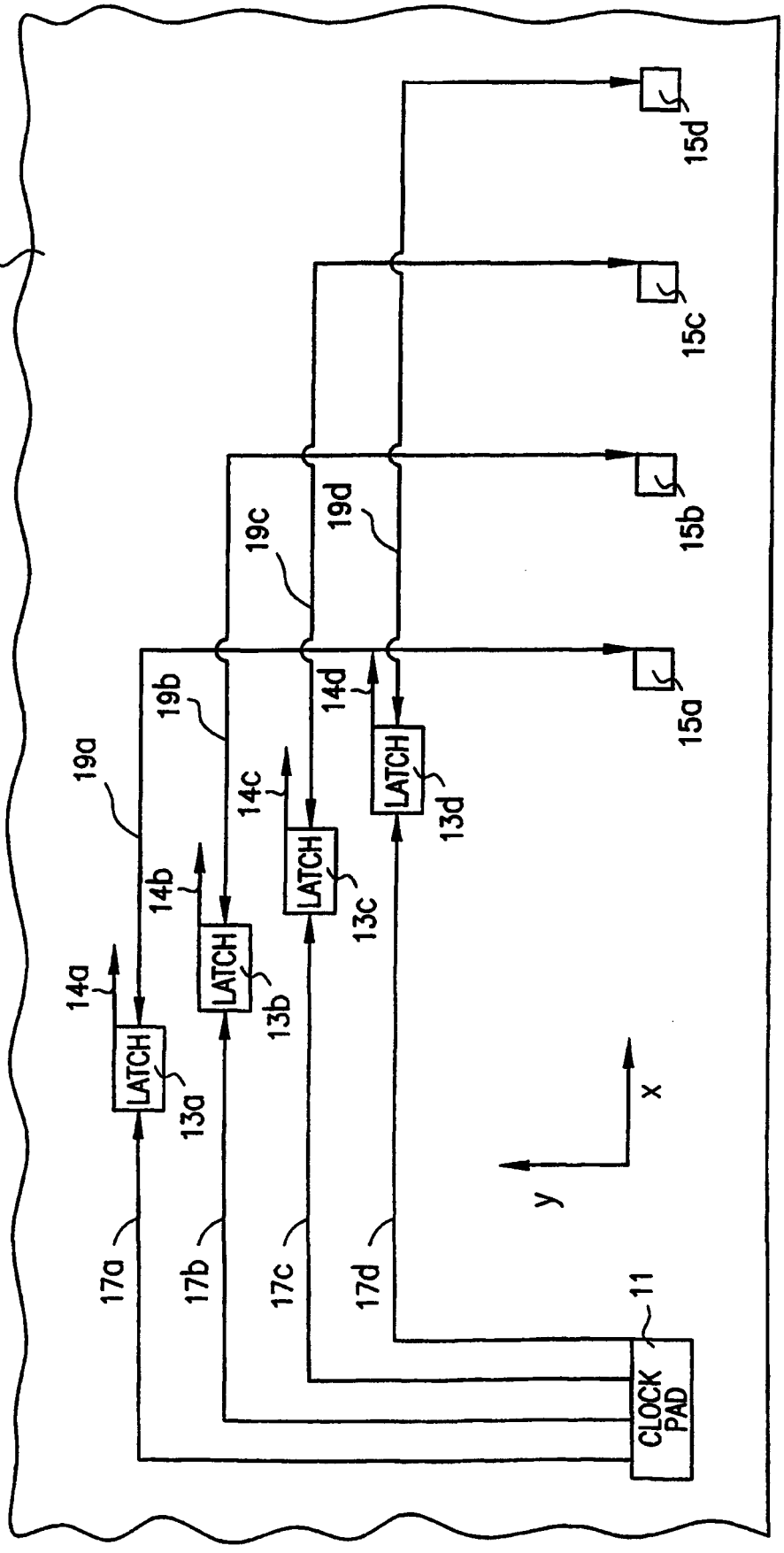

7
CLOCK PAD
11
17a
17b
17c
17d
LATCH
13a
LATCH
13b
LATCH
13c
LATCH
13d
14a
14b
14c
14d
19a
19b
19c
19d
15a
15b
15c
15d
x
y

FIG. 5

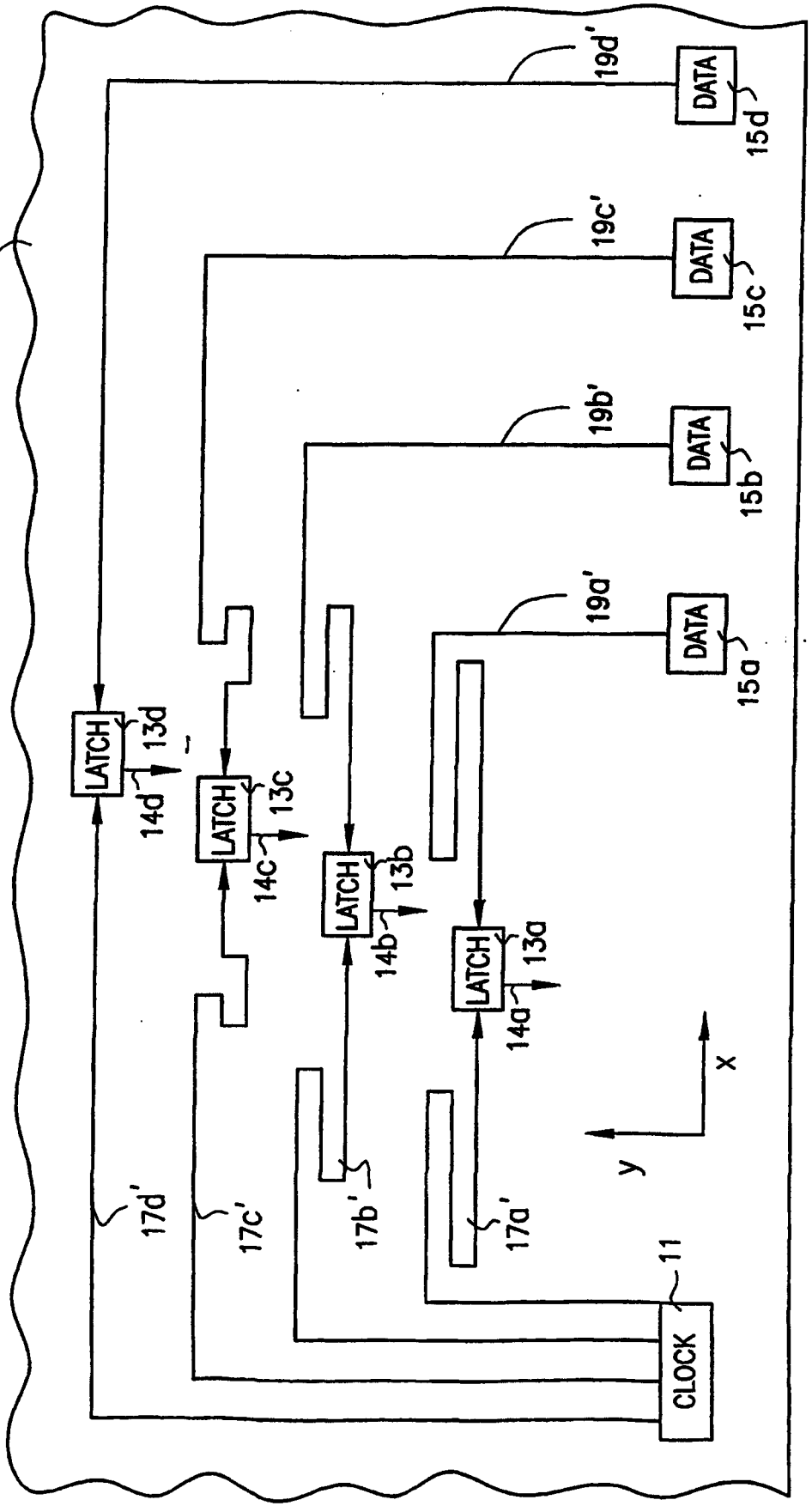

7
DATA
15d
19d'
DATA
15c
19c'
DATA
15b
19b'
DATA
15a
19a'
LATCH
13d
14d
LATCH
13c
14c
LATCH
13b
14b
LATCH
13a
14a
17d'
17c'
17b'
17a'
11
CLOCK
x
y

FIG. 6

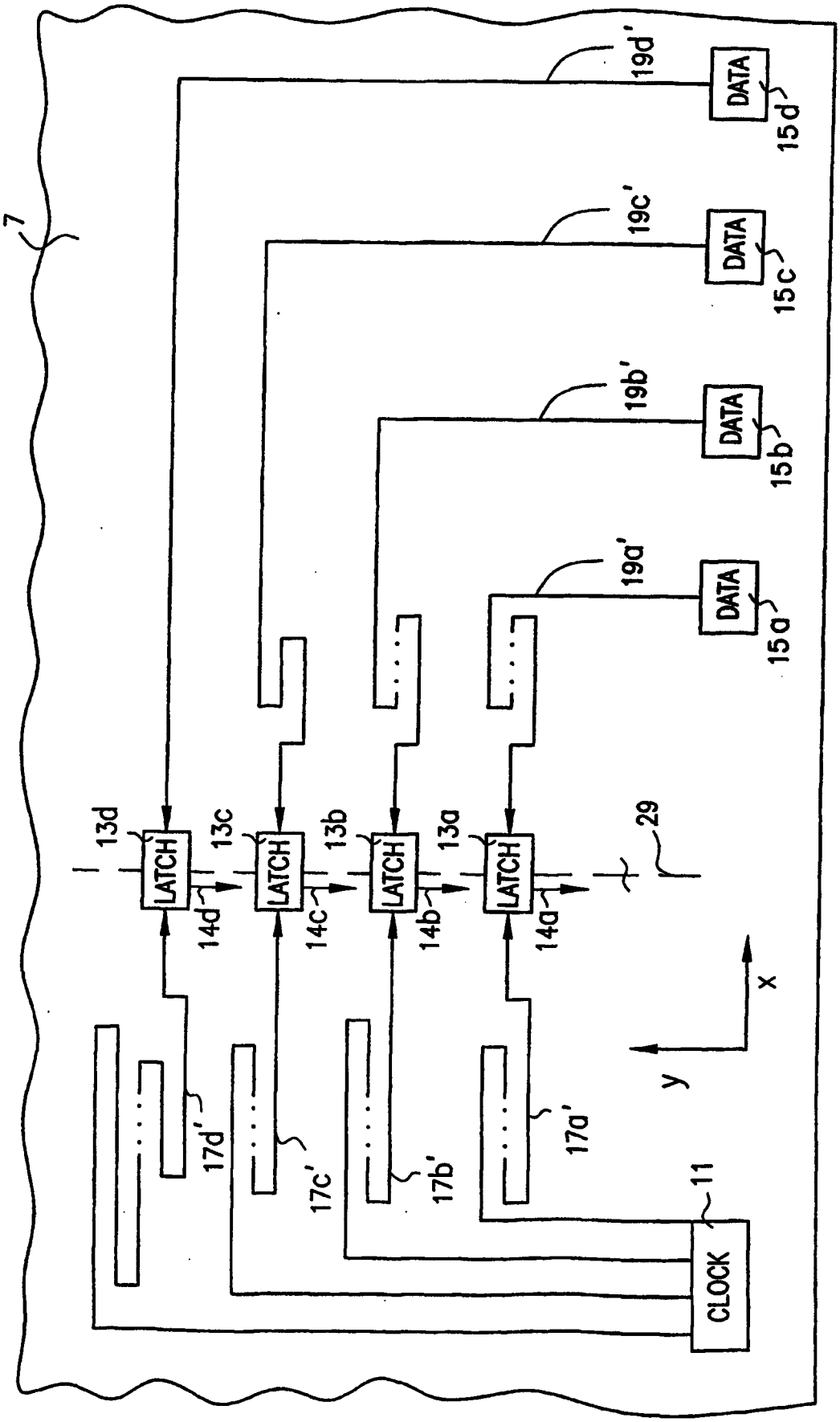

7
19d'
DATA
15d
19c'
DATA
15c
19b'
DATA
15b
19a'
DATA
15a
13d
13c
13b
13a
LATCH
LATCH
LATCH
LATCH
14d
14c
14b
14a
29
x
y
17d'
17c'
17b'
17a'
11
CLOCK

FIG. 7

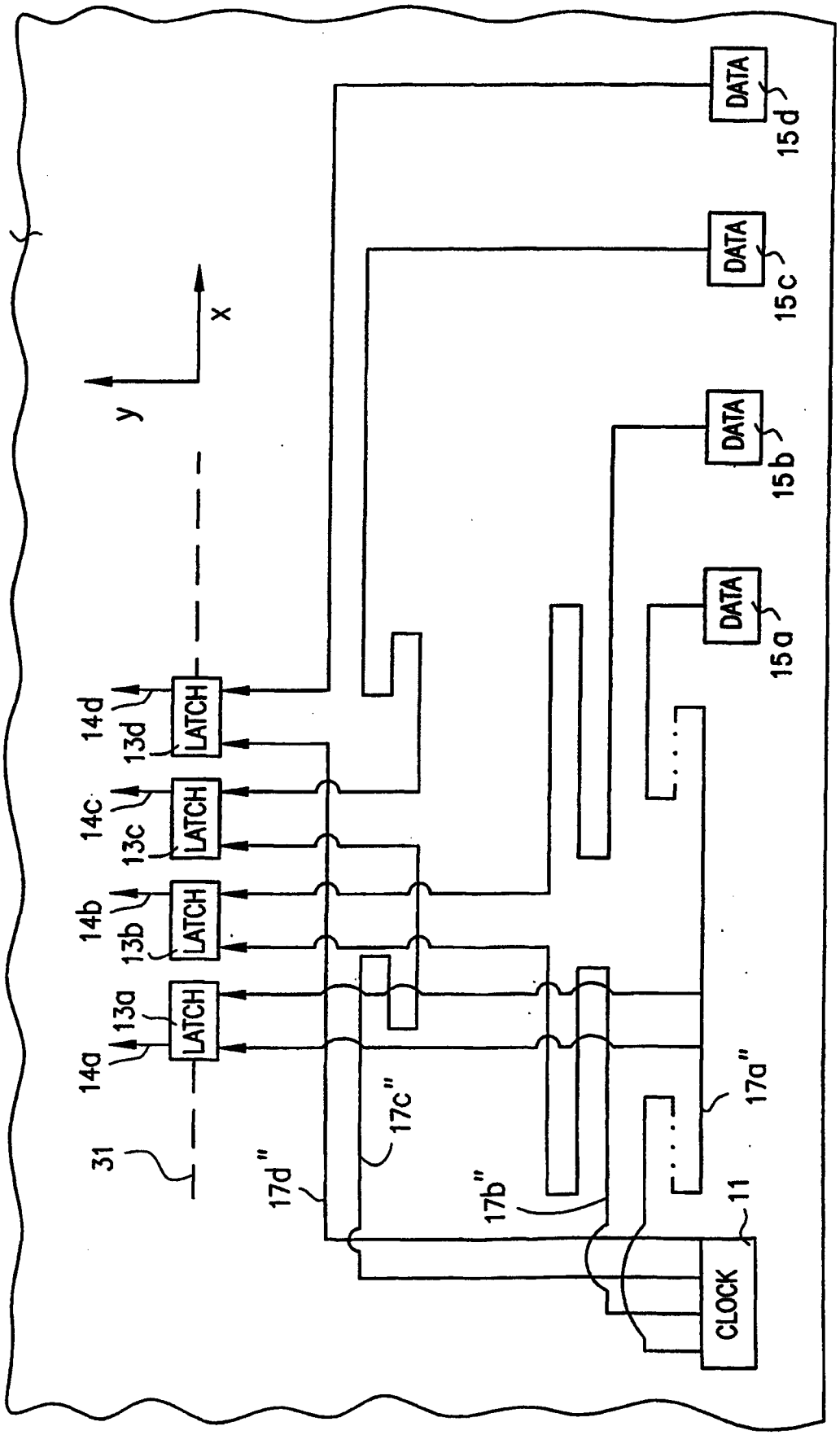
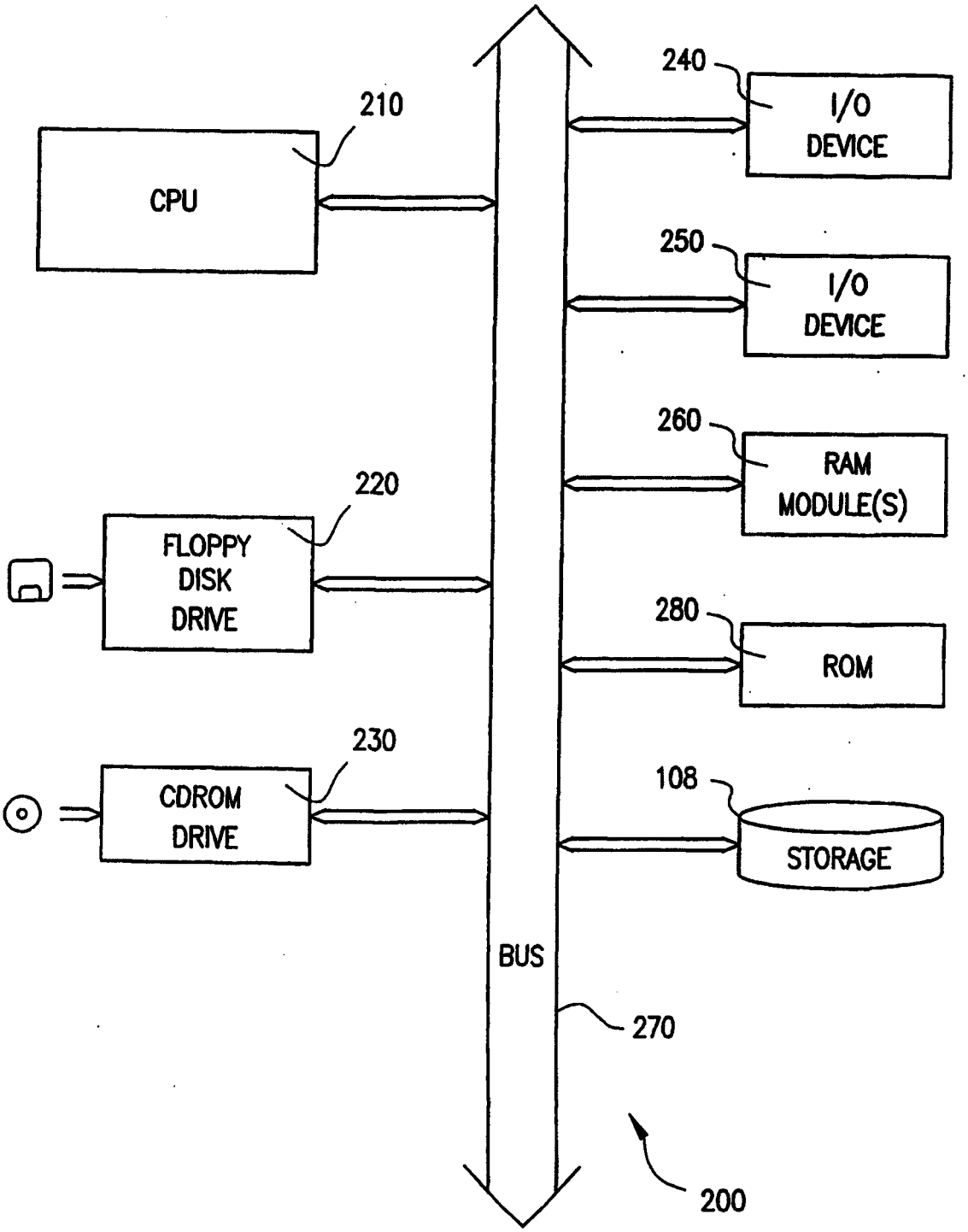

7
x
y
31
14a
13a
LATCH
14b
13b
LATCH
14c
13c
LATCH
14d
13d
LATCH
17d"
17c"
17b"
17a"
11
CLOCK
DATA
15a
DATA
15b
DATA
15c
DATA
15d

FIG. 8


240
I/O DEVICE
210
CPU
250
I/O DEVICE
260
RAM MODULE(S)
220
FLOPPY DISK DRIVE
280
ROM
230
CDROM DRIVE
108
STORAGE
BUS
270
200