

WHAT IS CLAIMED IS:

1. A decoder, the decoder comprising circuitry configured to:
receive a bitstream including a coded picture, the coded picture including a first region
5 comprising a first contiguous plurality of coding blocks having global motion and a
second region comprising a second contiguous plurality of coding blocks having local
motion;
detect a header for the coded picture in the bitstream;
determine, using the header, a motion model, the motion model being global for the
10 blocks in the first region and being one of translational motion, 4-parameter affine
motion, or 6-parameter affine motion, wherein parameters for the determined motion
model for the first region being determined from control point motion vectors signaled in
the bitstream; and
decode each block in the second region using motion information determined individually
15 for each block to reconstruct the local motion in the second region.
2. The decoder of claim 1, wherein the header includes a sequence parameter set.
3. The decoder of claim 1, wherein the header includes a picture parameter set.
- 20 4. The decoder of claim 1 further comprising:
an entropy decoder processor configured to receive the bit stream and decode the
bitstream into quantized coefficients;
an inverse quantization and inverse transformation processor configured to process the
25 quantized coefficients including performing an inverse discrete cosine;
a deblocking filter;
a frame buffer; and
an intra prediction processor.