

FIG. 1

1

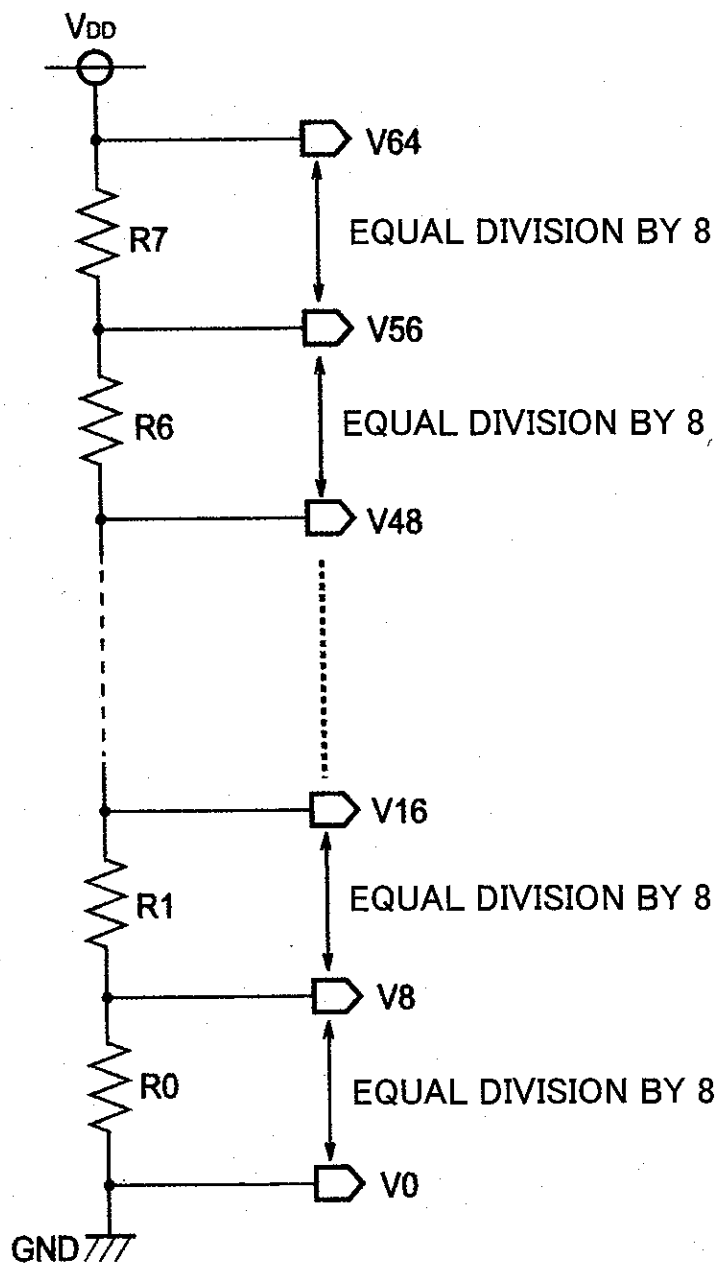
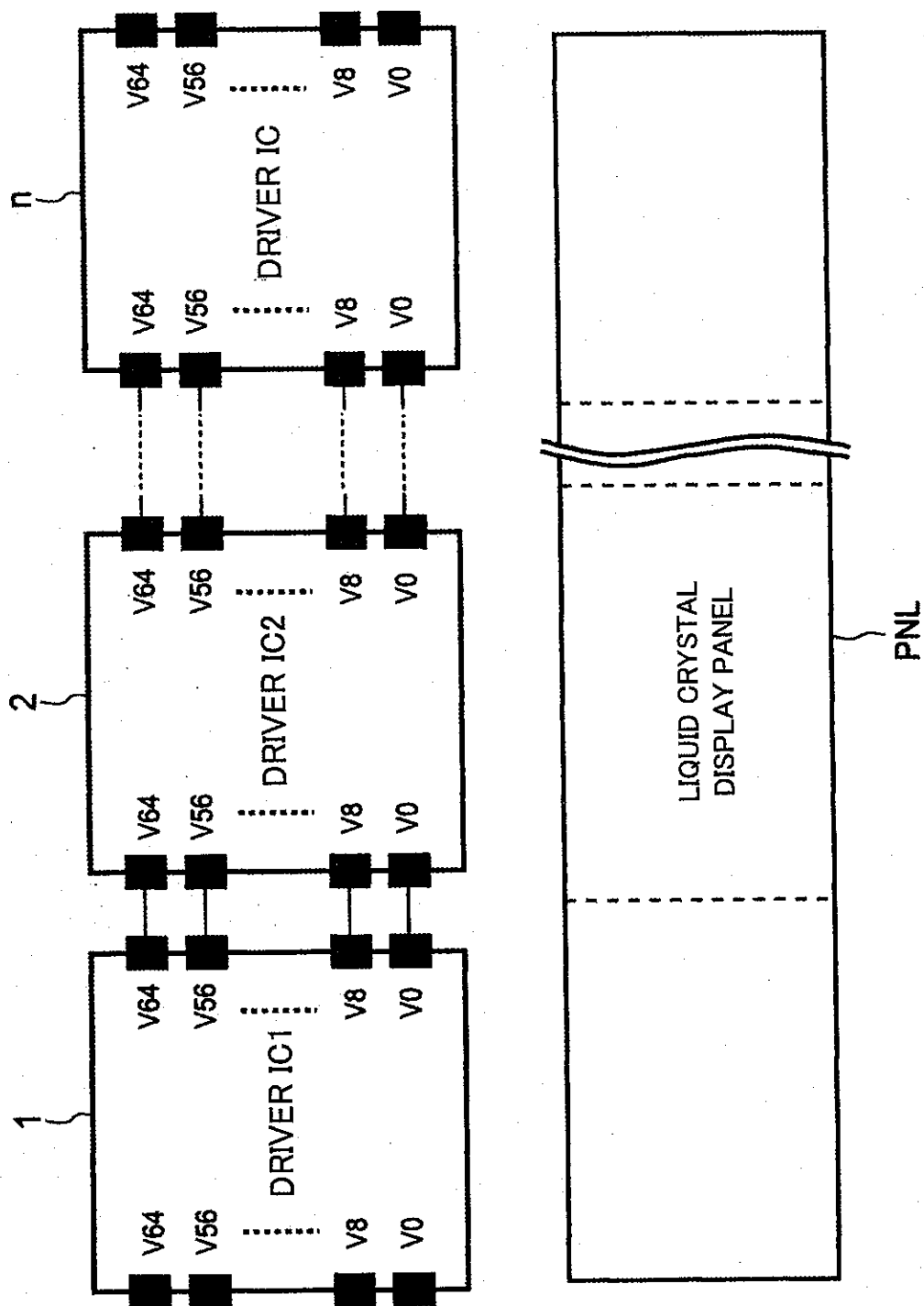


FIG. 2



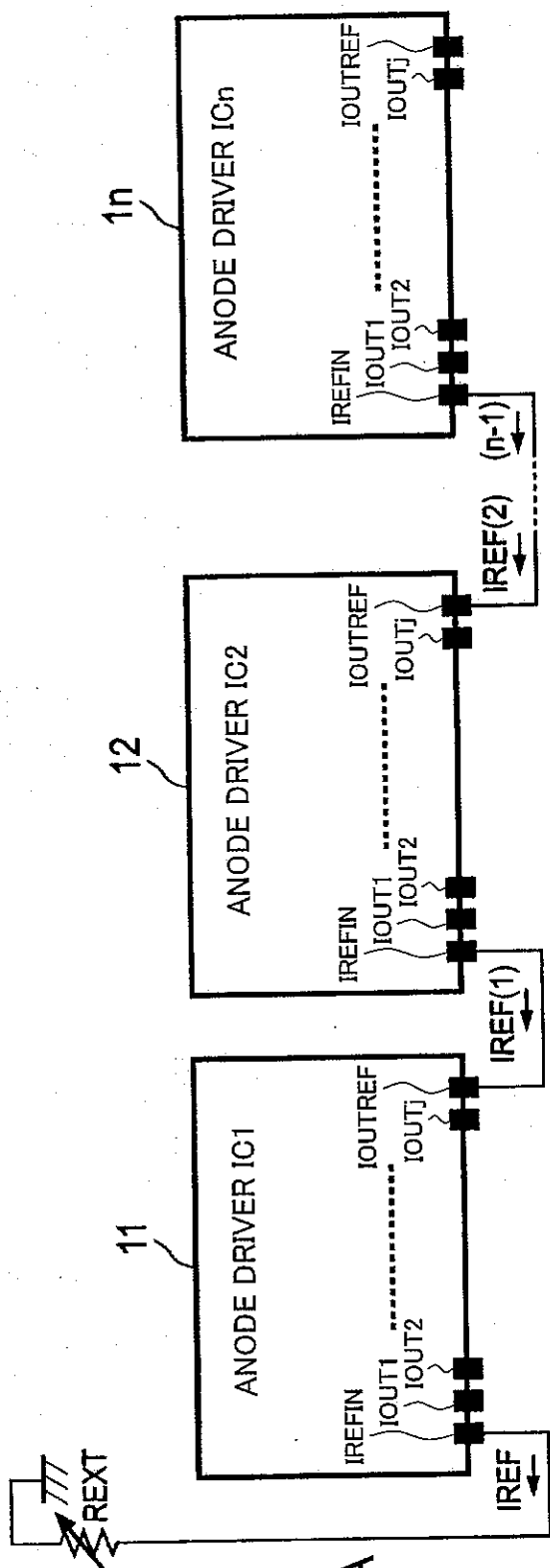


FIG. 3A

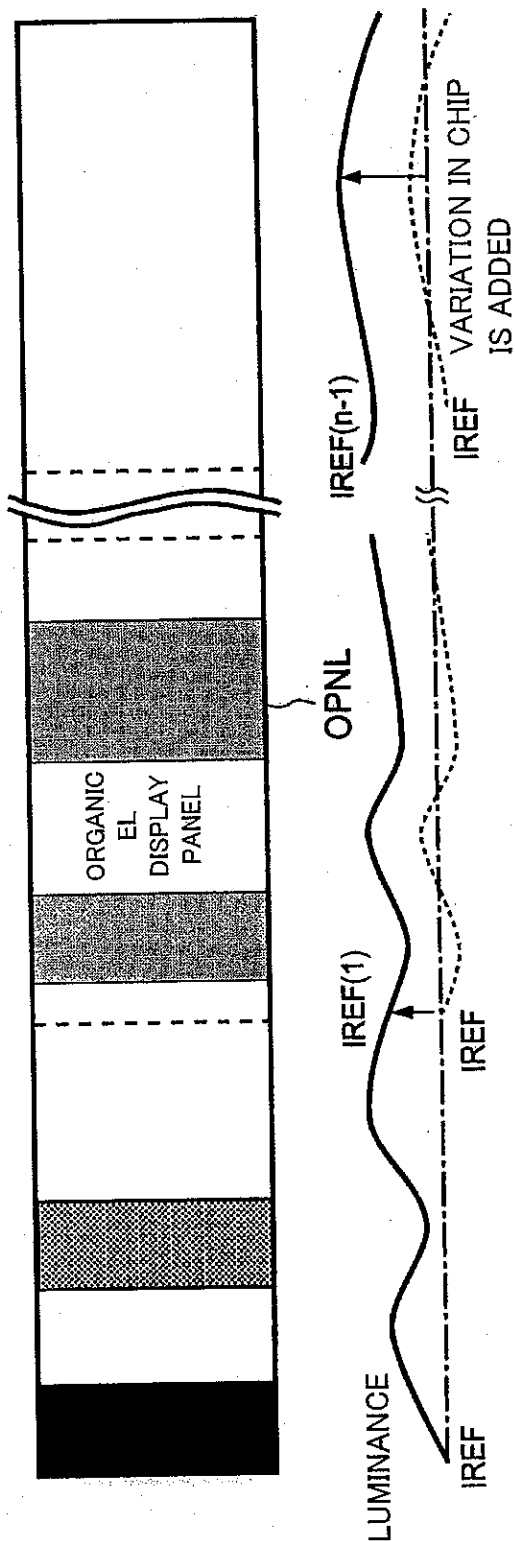
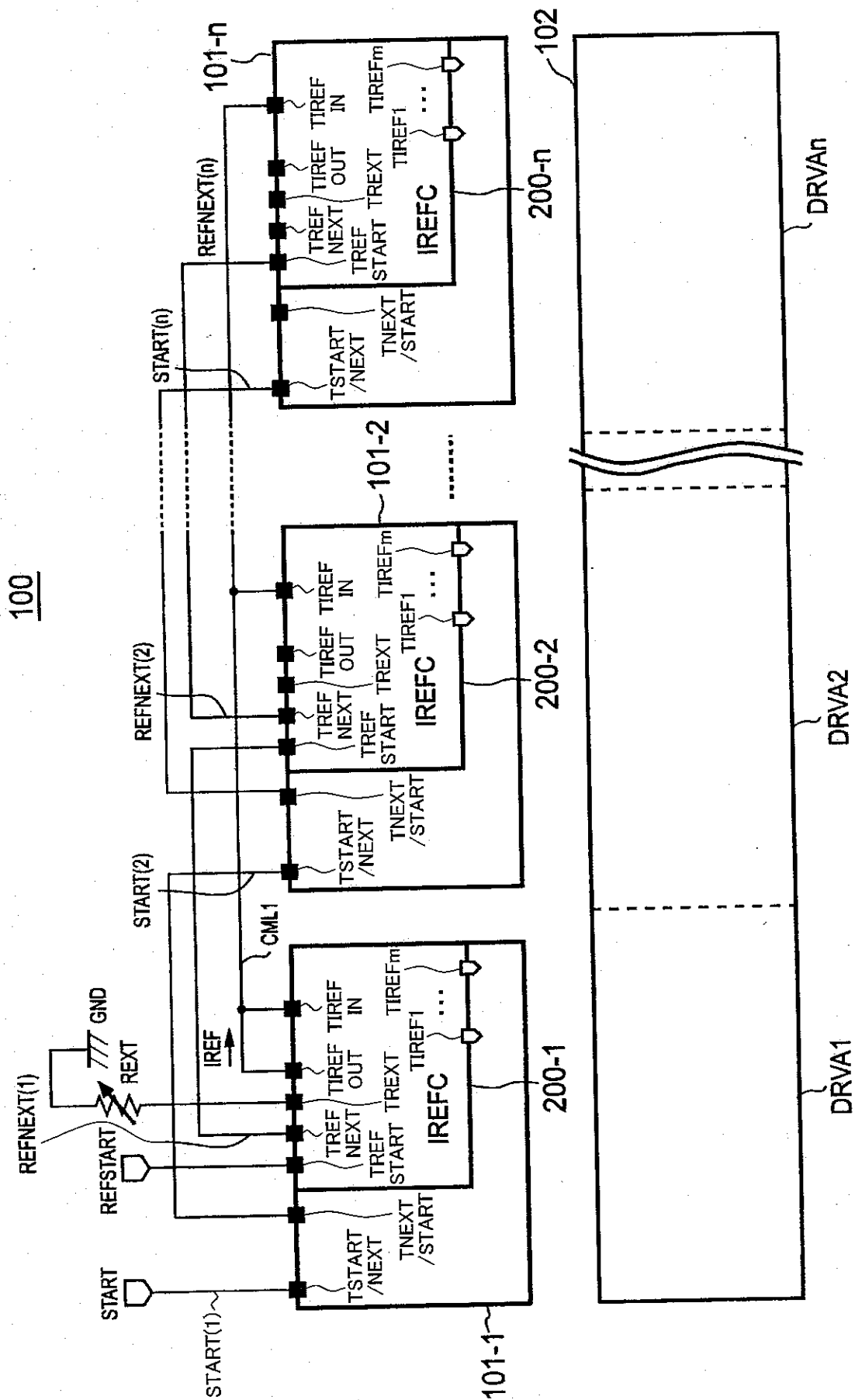


FIG. 3B

FIG. 4



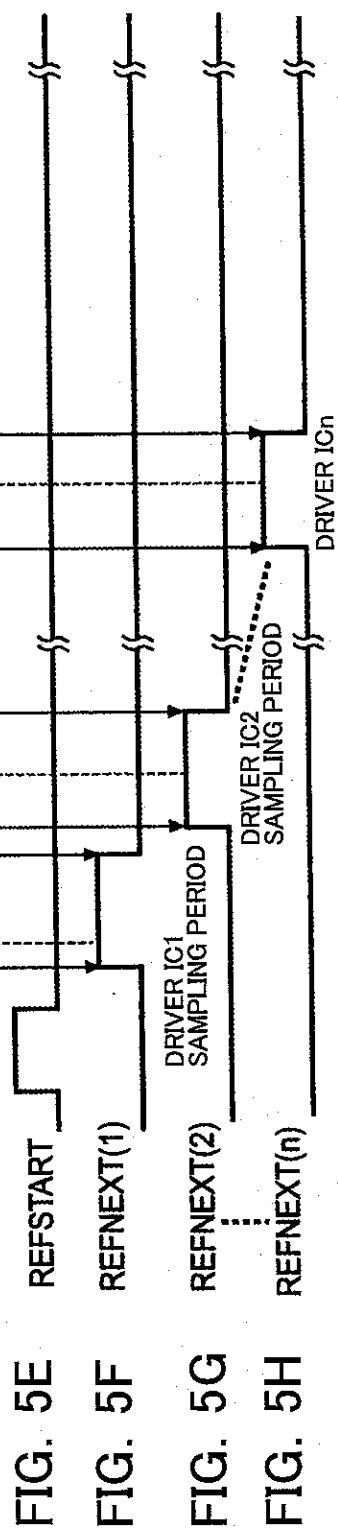
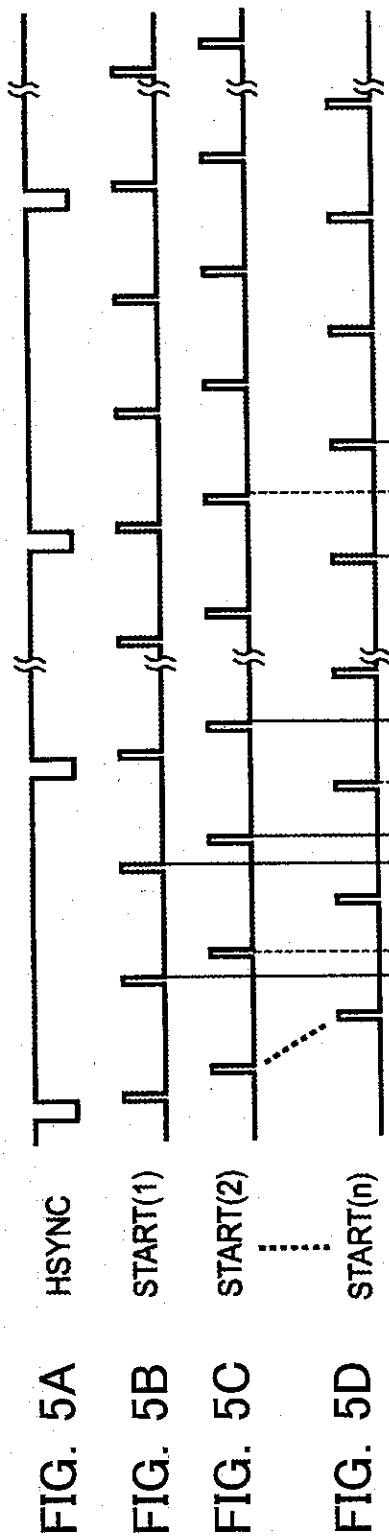


FIG. 6

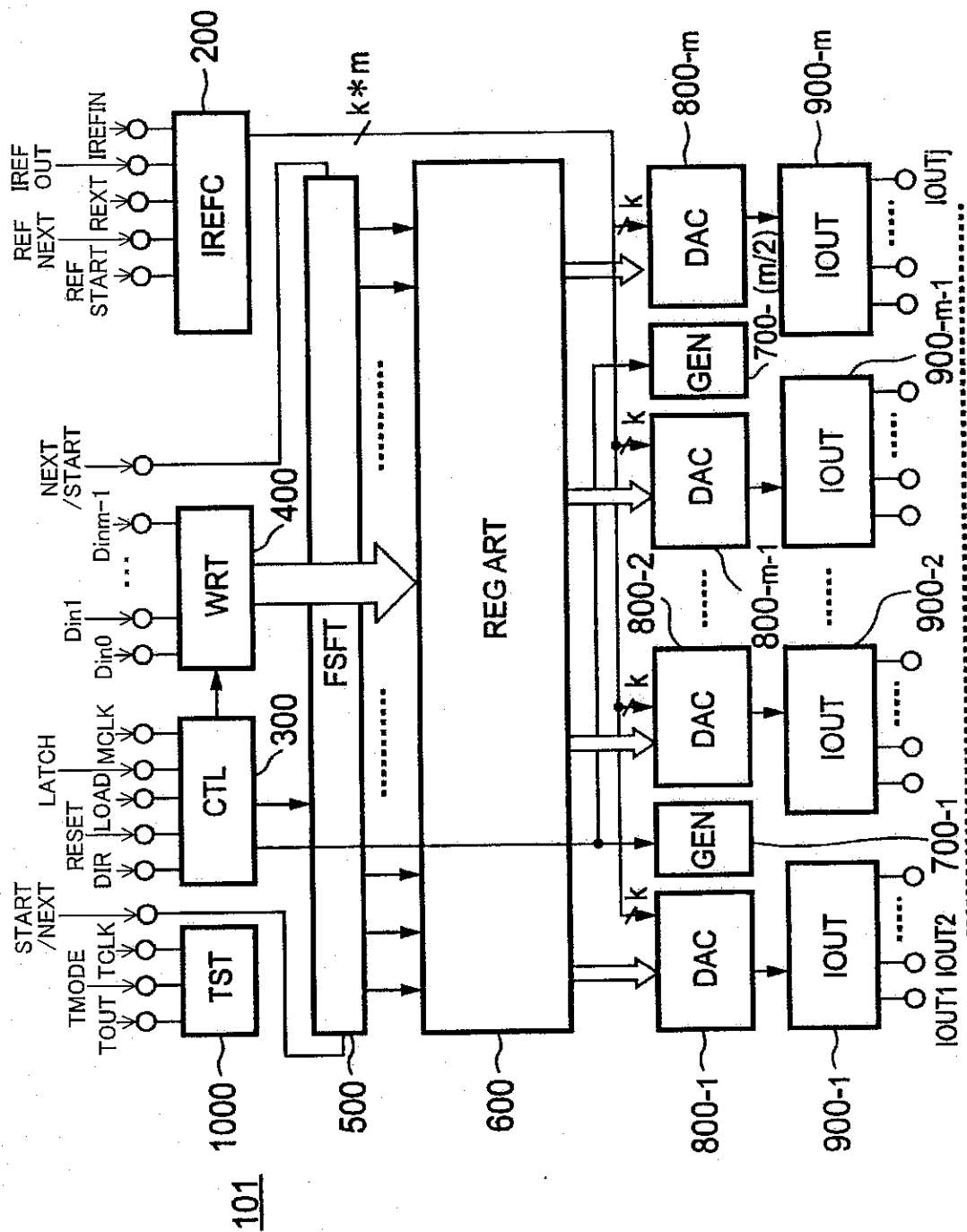


FIG. 7

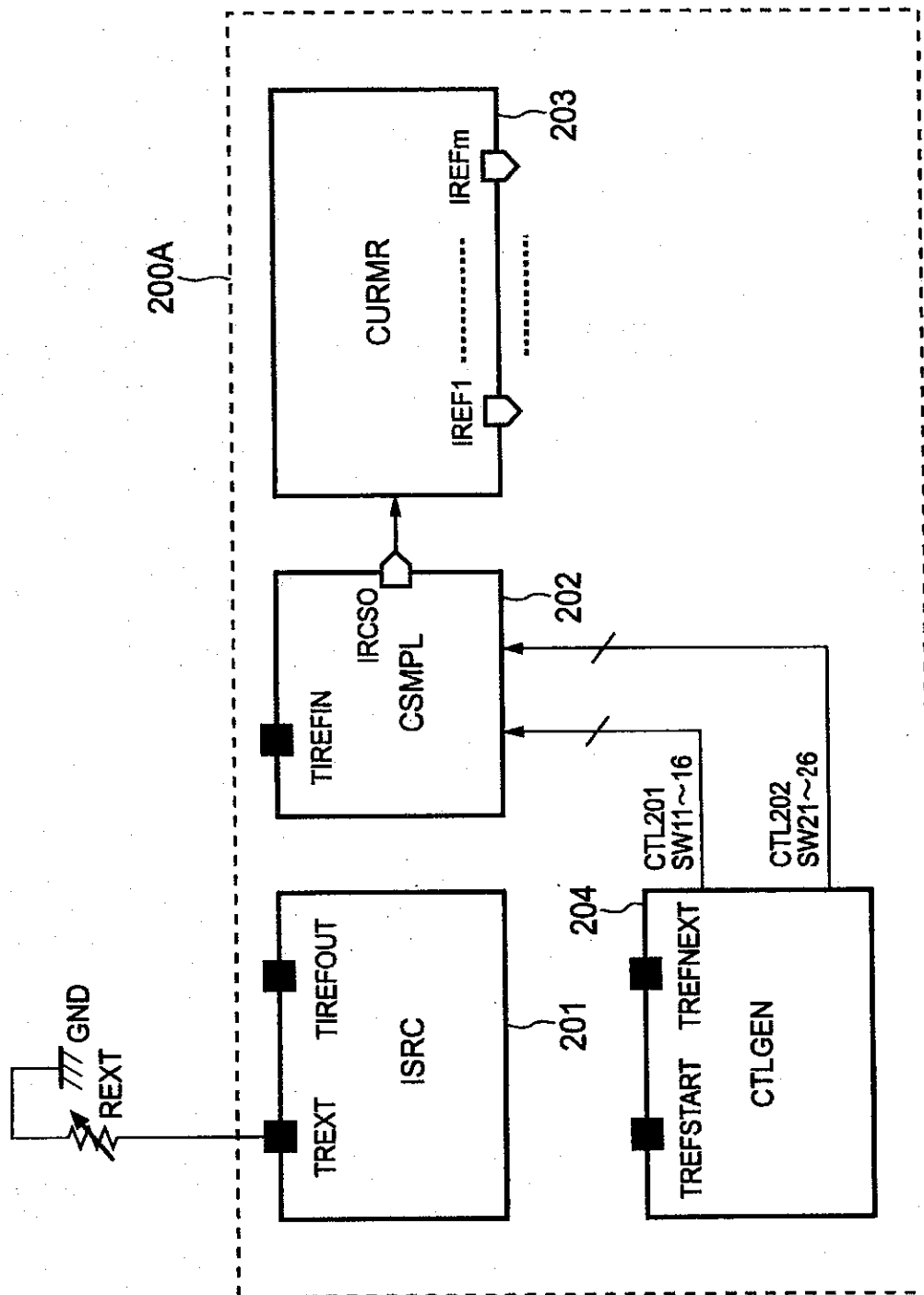


FIG. 8

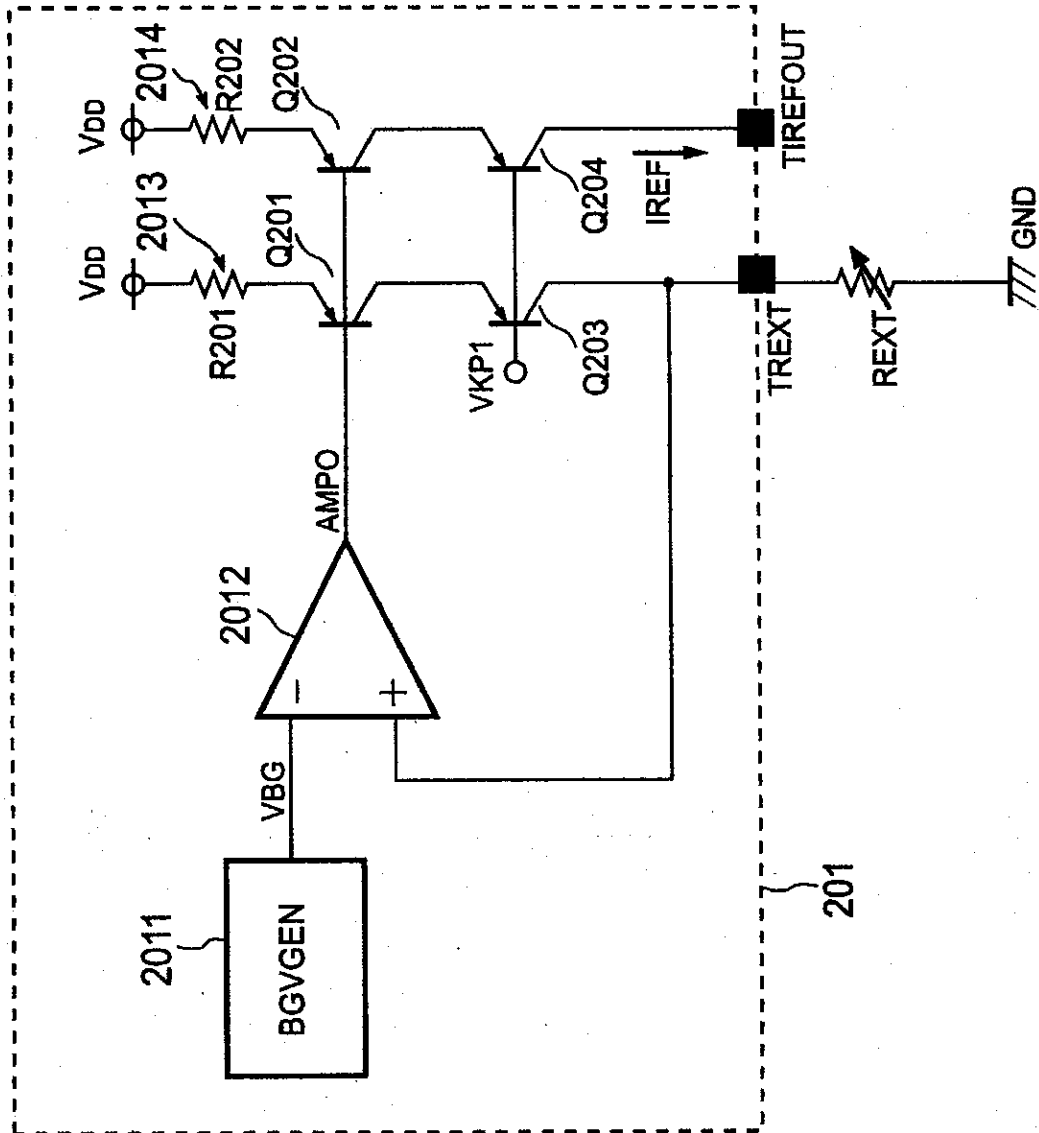
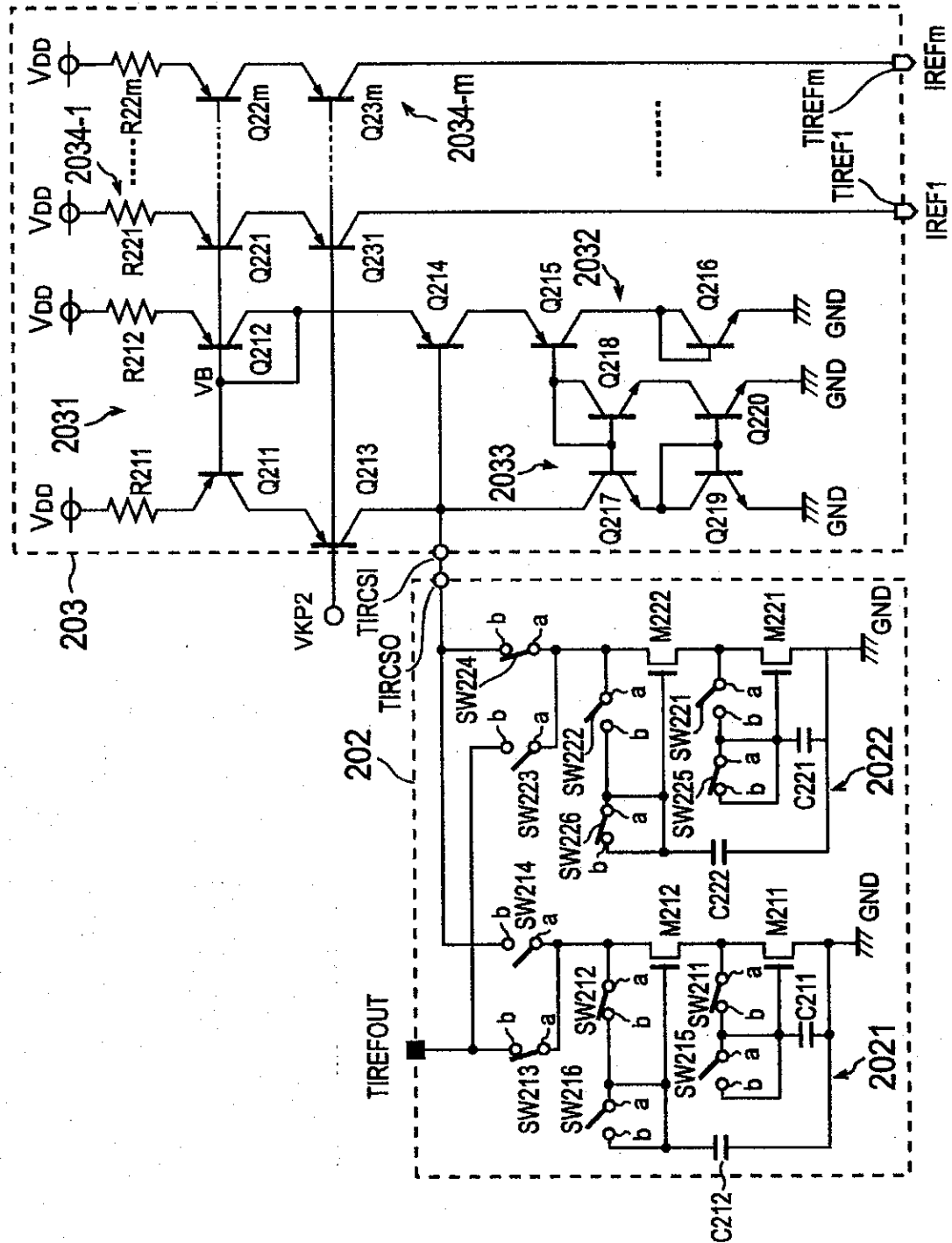


FIG. 9



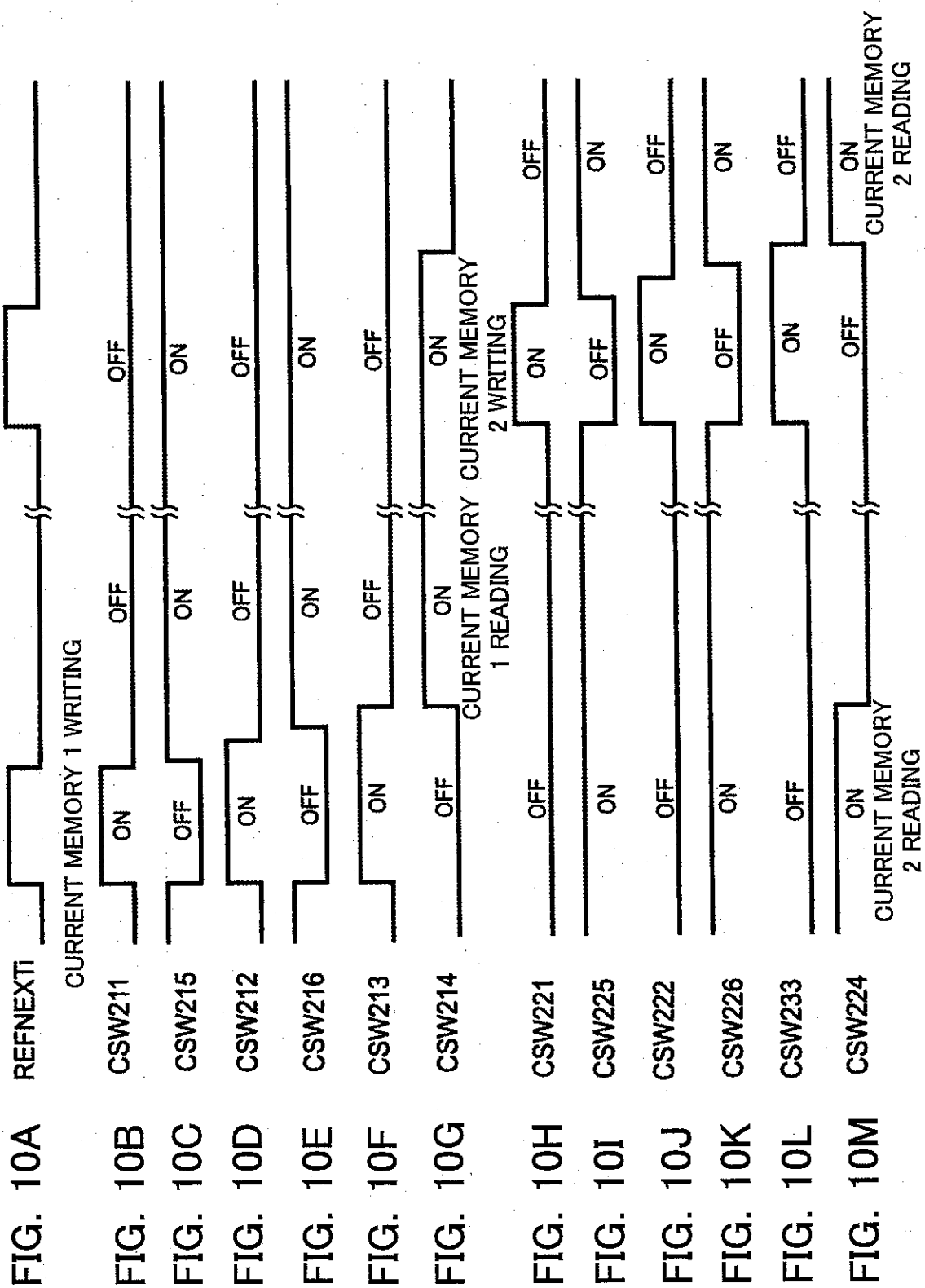


FIG. 11C

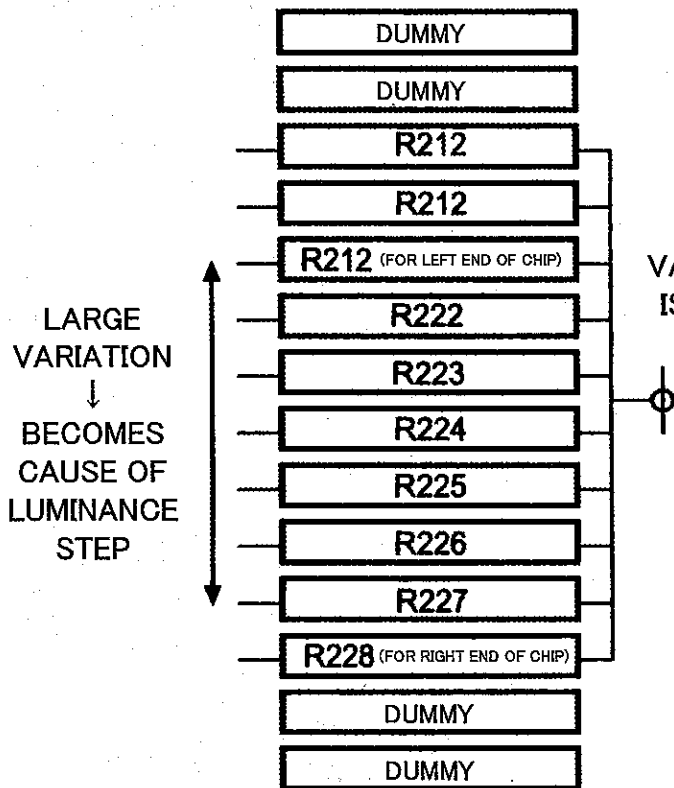


FIG. 11A

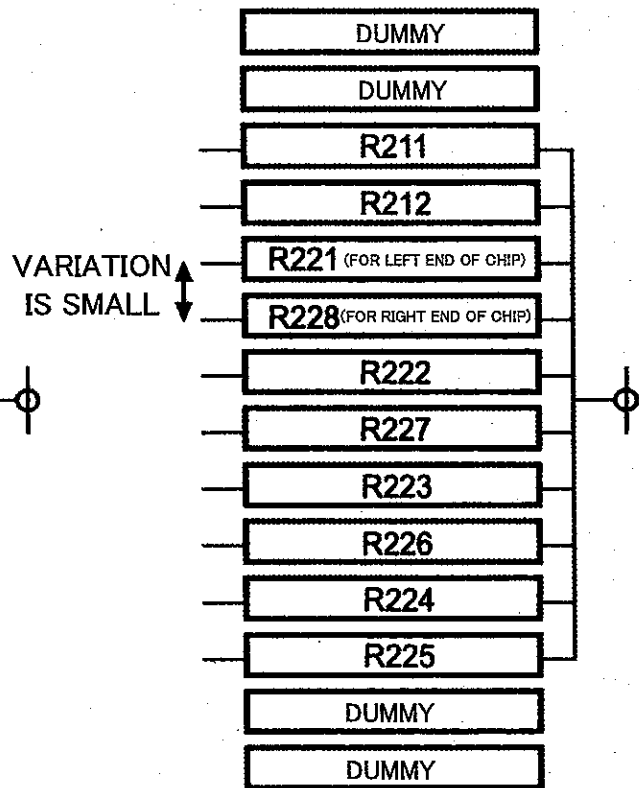


FIG. 11B

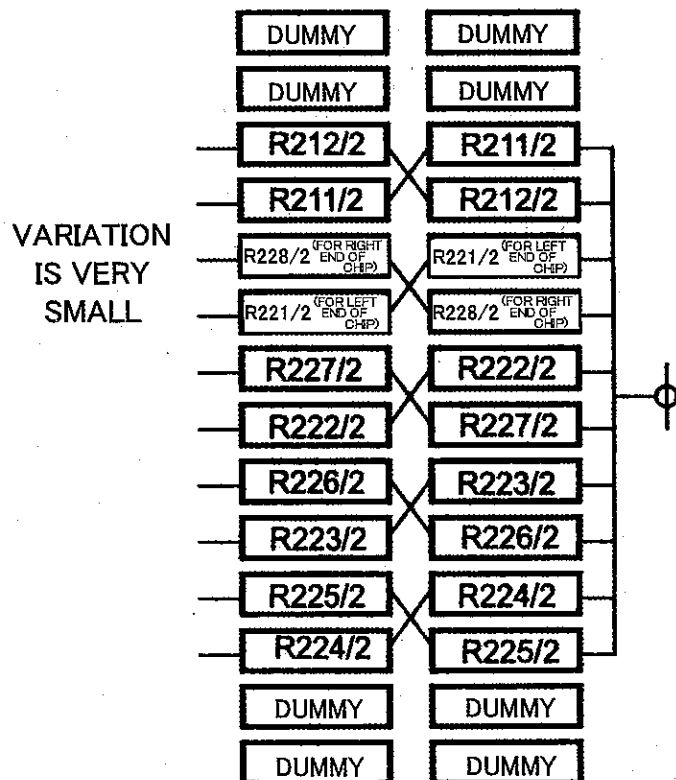
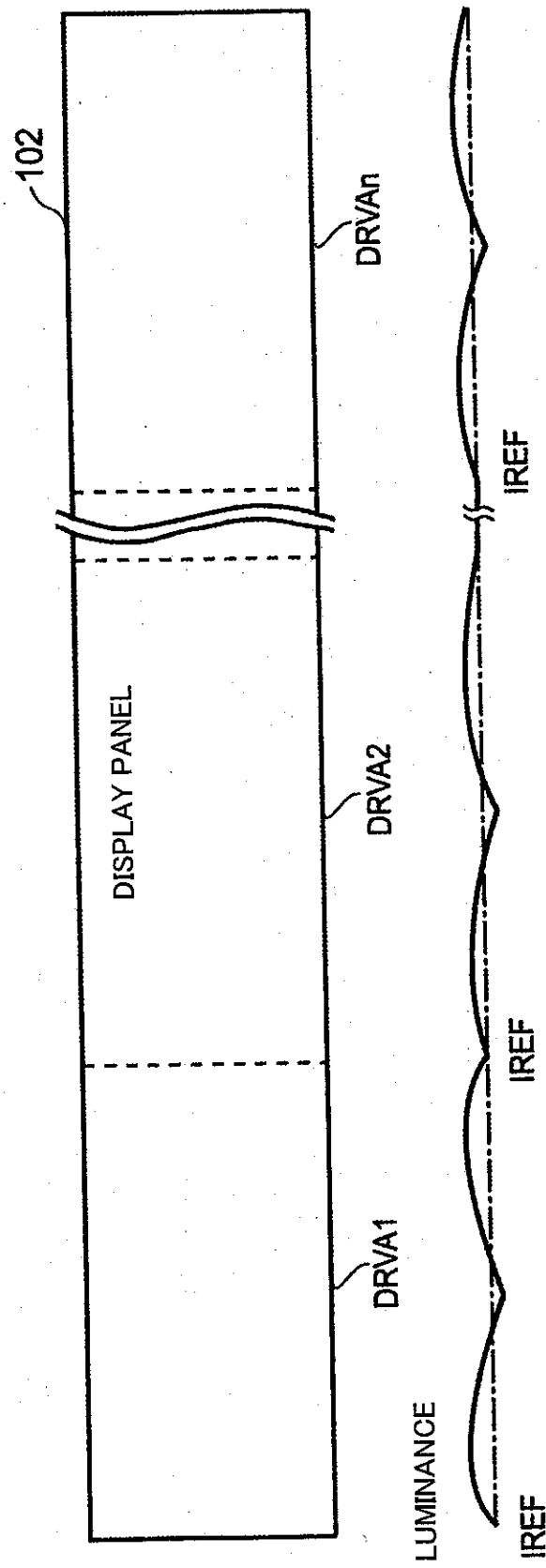


FIG. 12



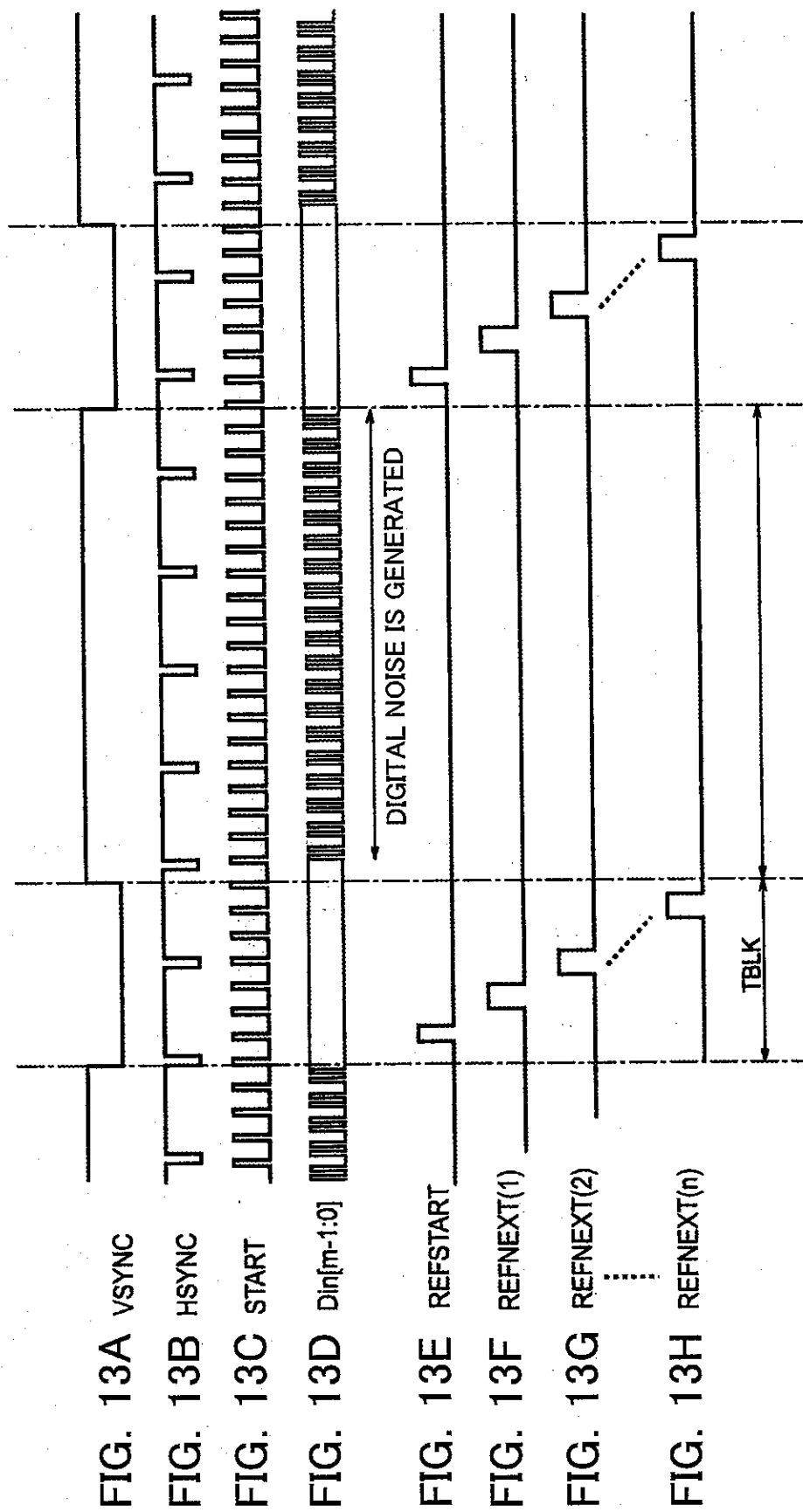


FIG. 14

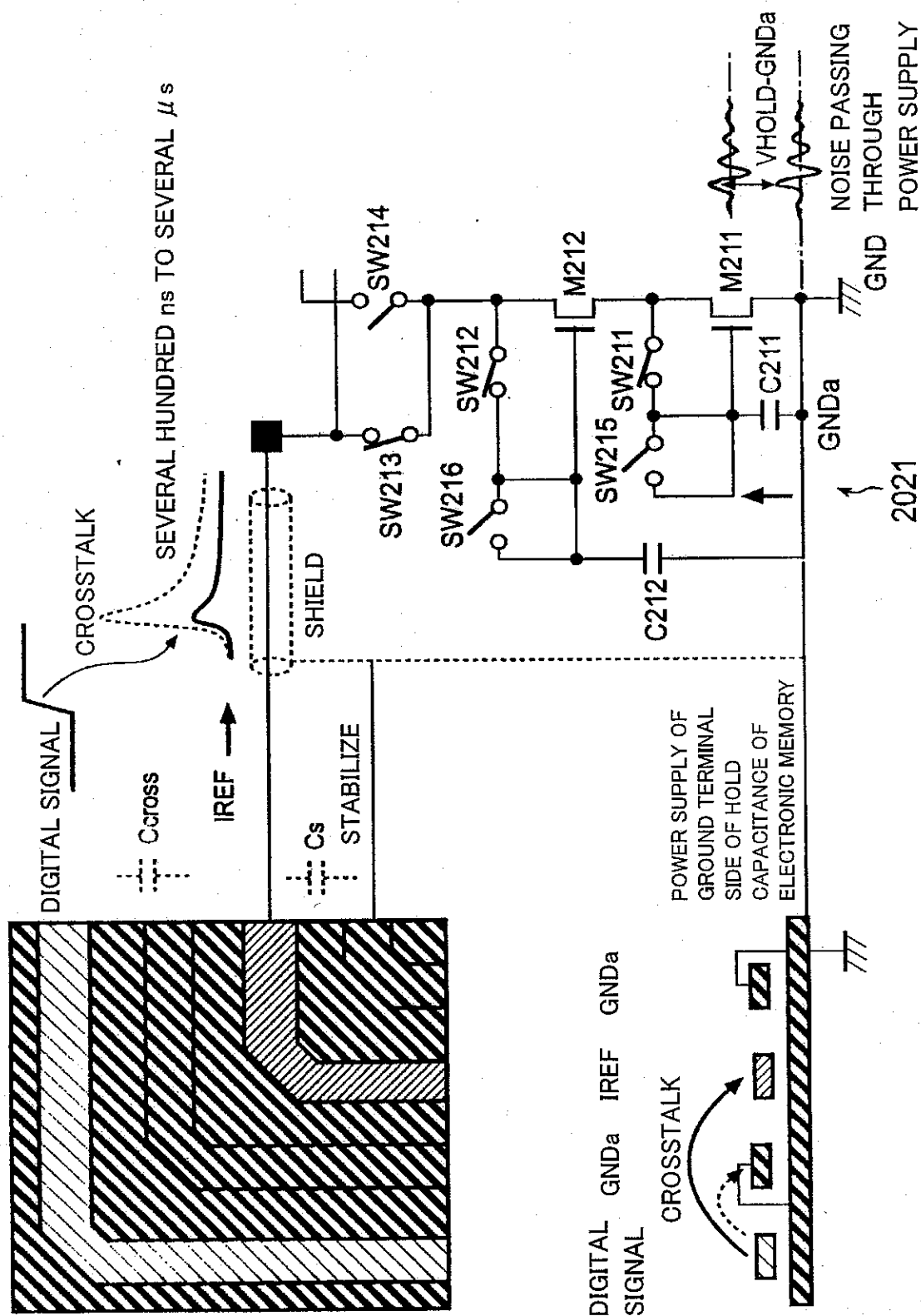


FIG. 15

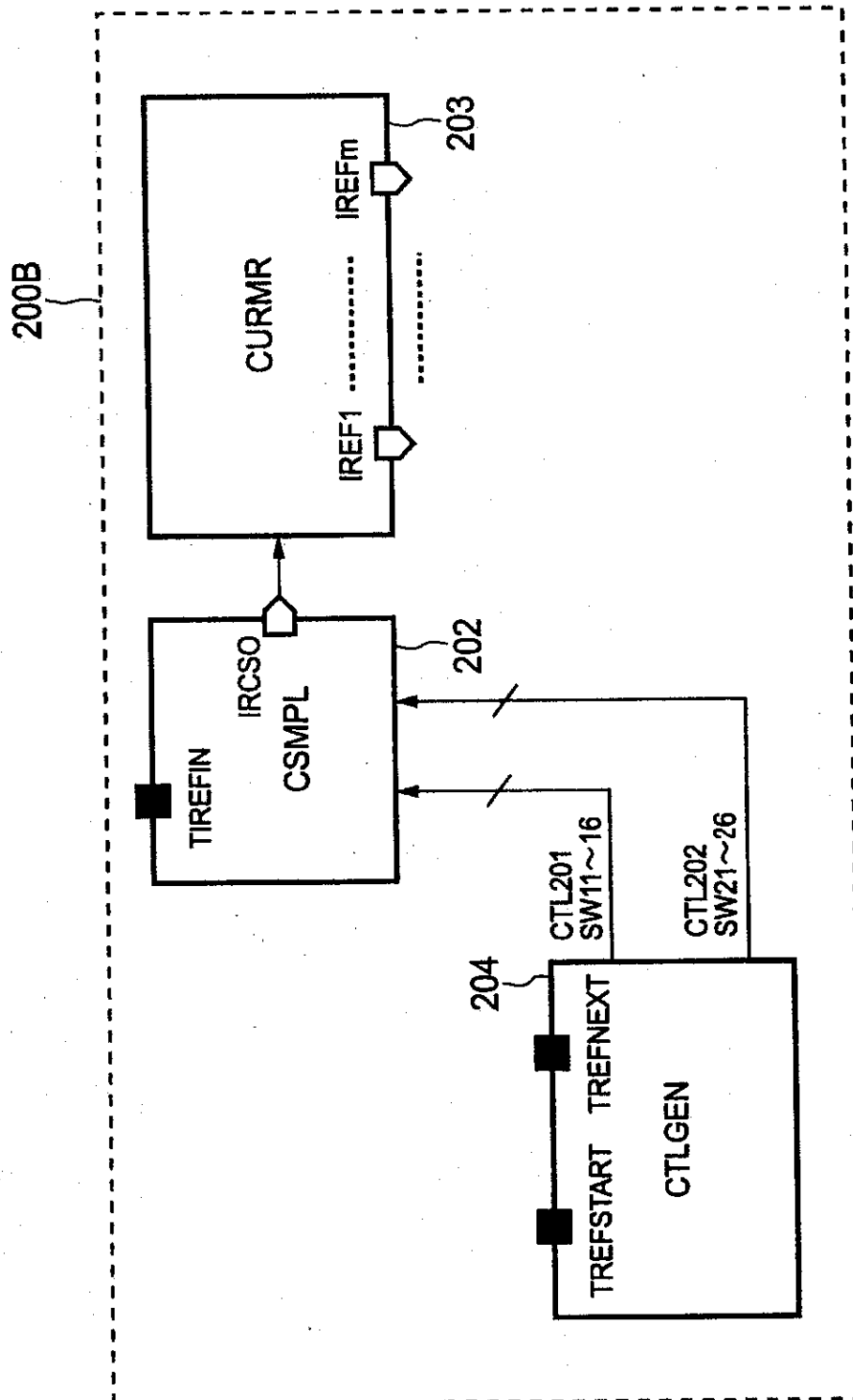
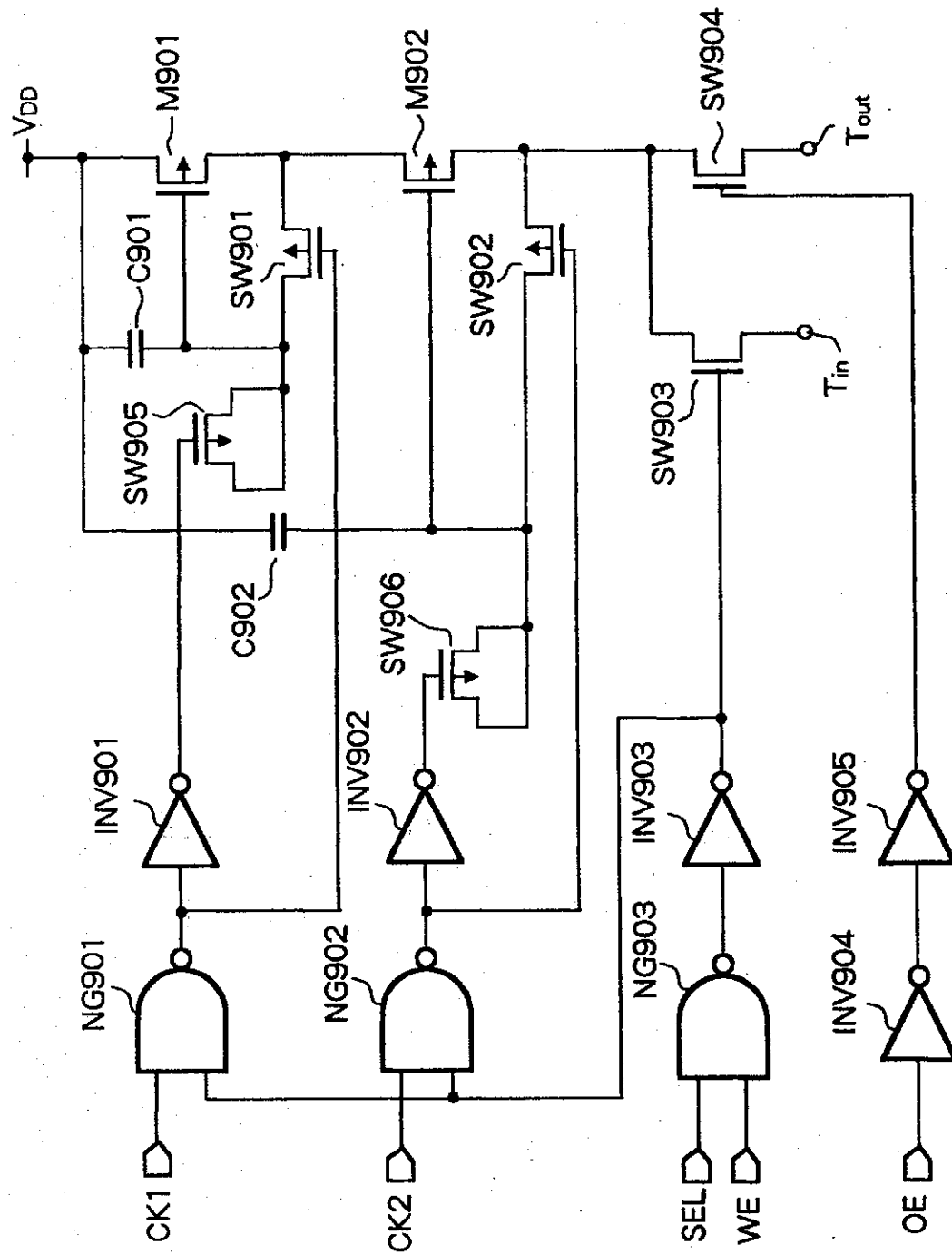


FIG. 17



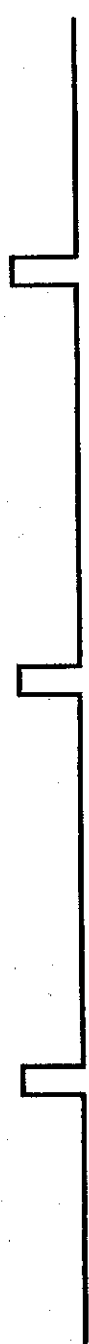


FIG. 18A LATCH



FIG. 18B CK1



FIG. 18C CK2



FIG. 18D SEL

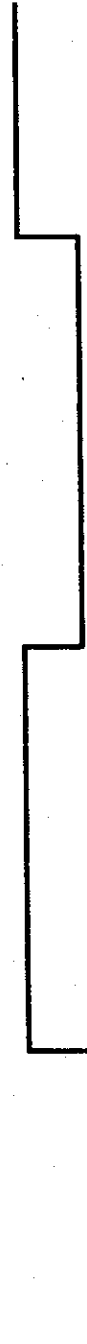


FIG. 18E OE0



FIG. 18F OE1



FIG. 18G IOUT (901-1)

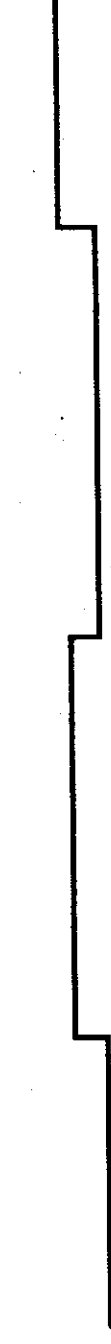


FIG. 18H IOUT (902-1)

FIG. 19

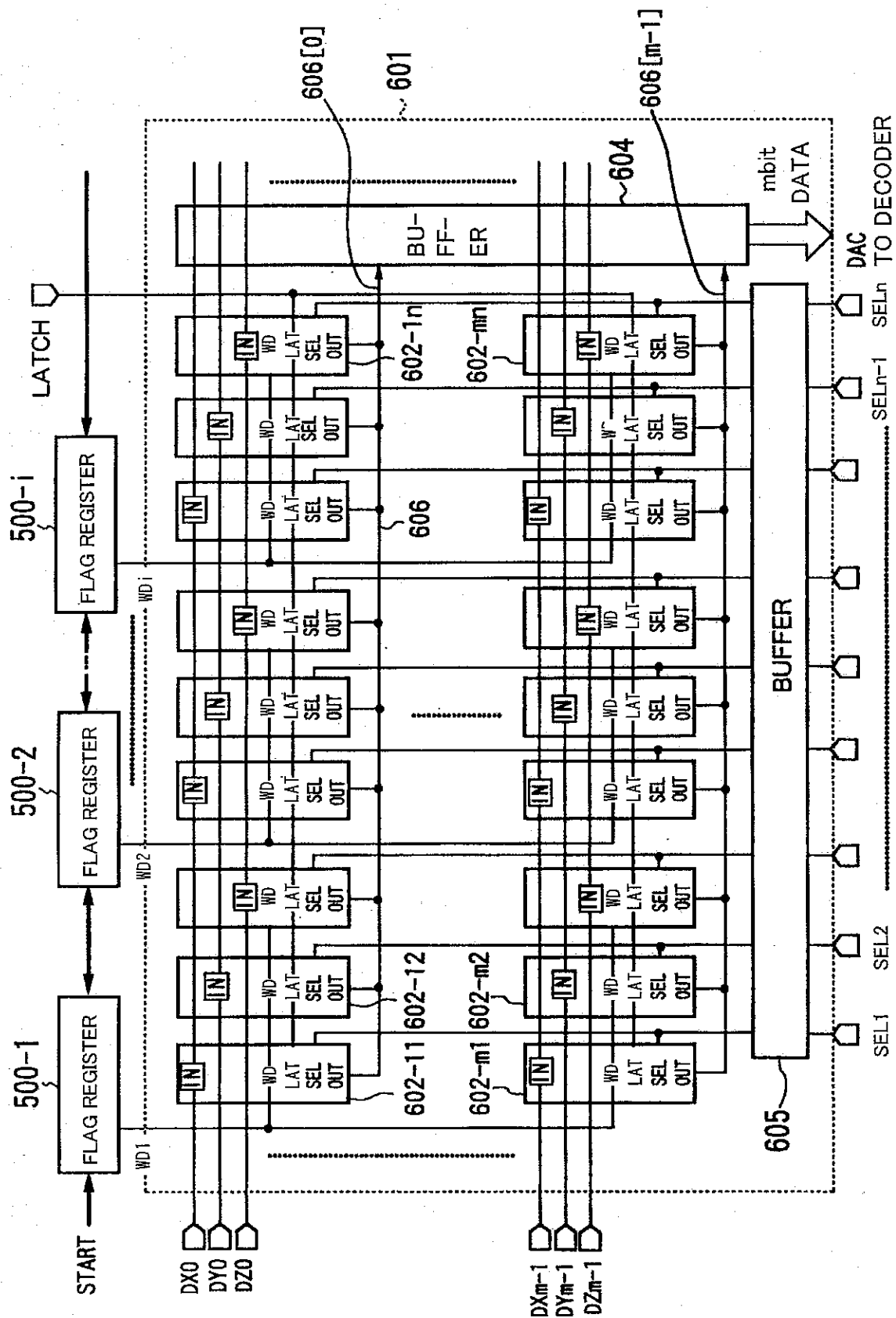


FIG. 20

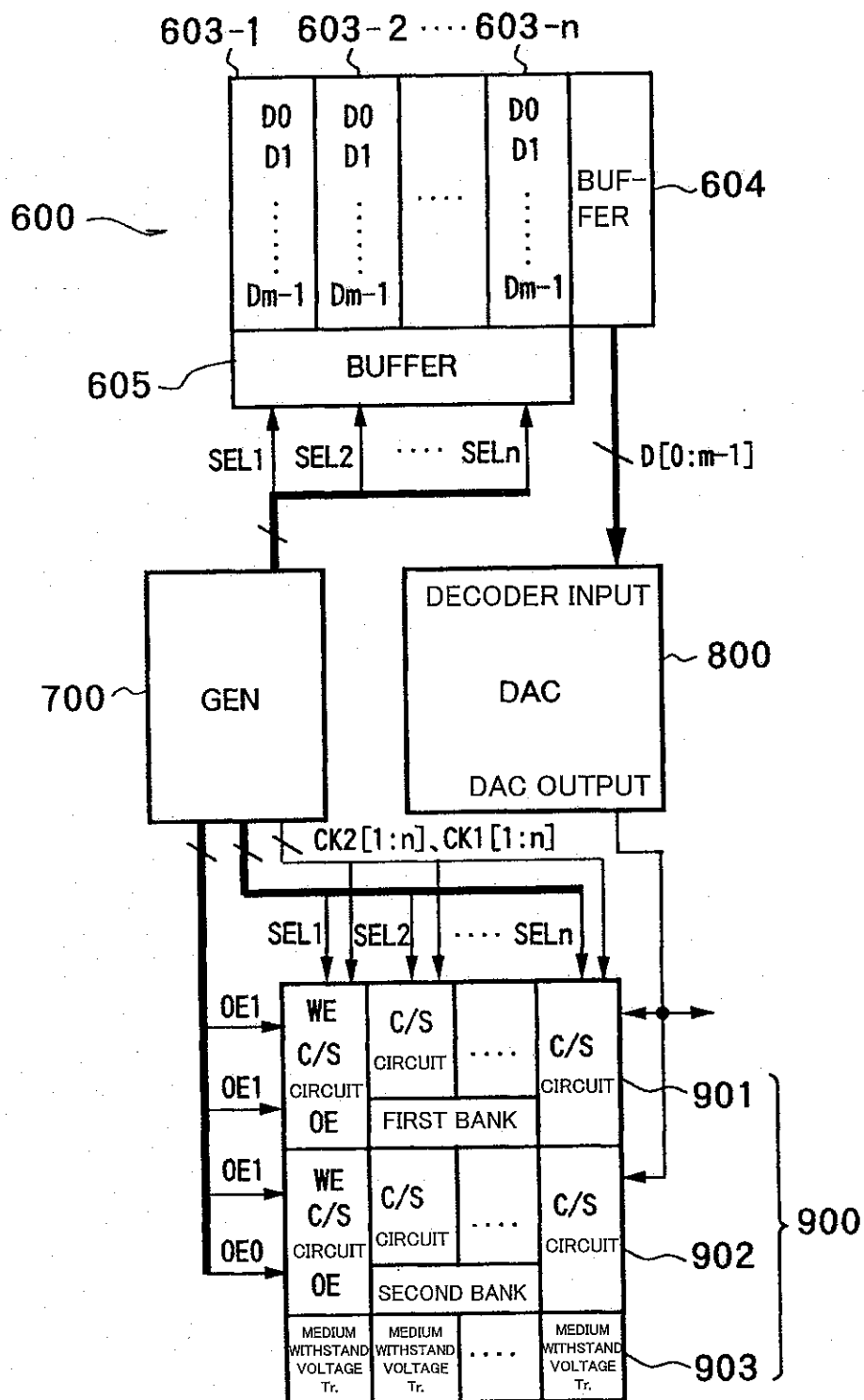


FIG. 21A

LATCH



FIG. 21B

SEL1
(CK11, CK21)



FIG. 21C

SEL2
(CK12, CK22)



FIG. 21D

SELn
(CK1n, CK2n)



FIG. 21E

LOAD



FIG. 21F

OE0



FIG. 21G

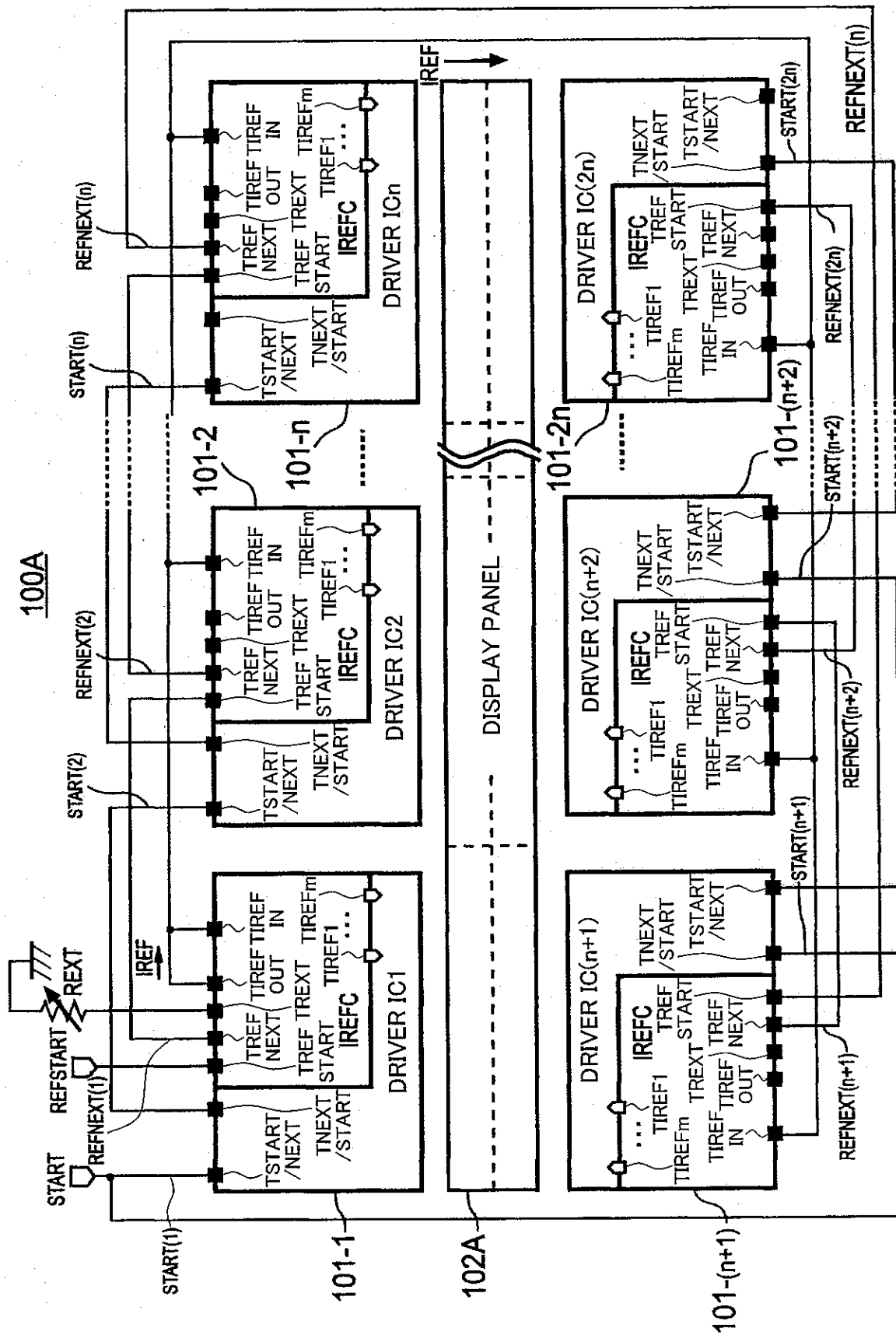
OE1

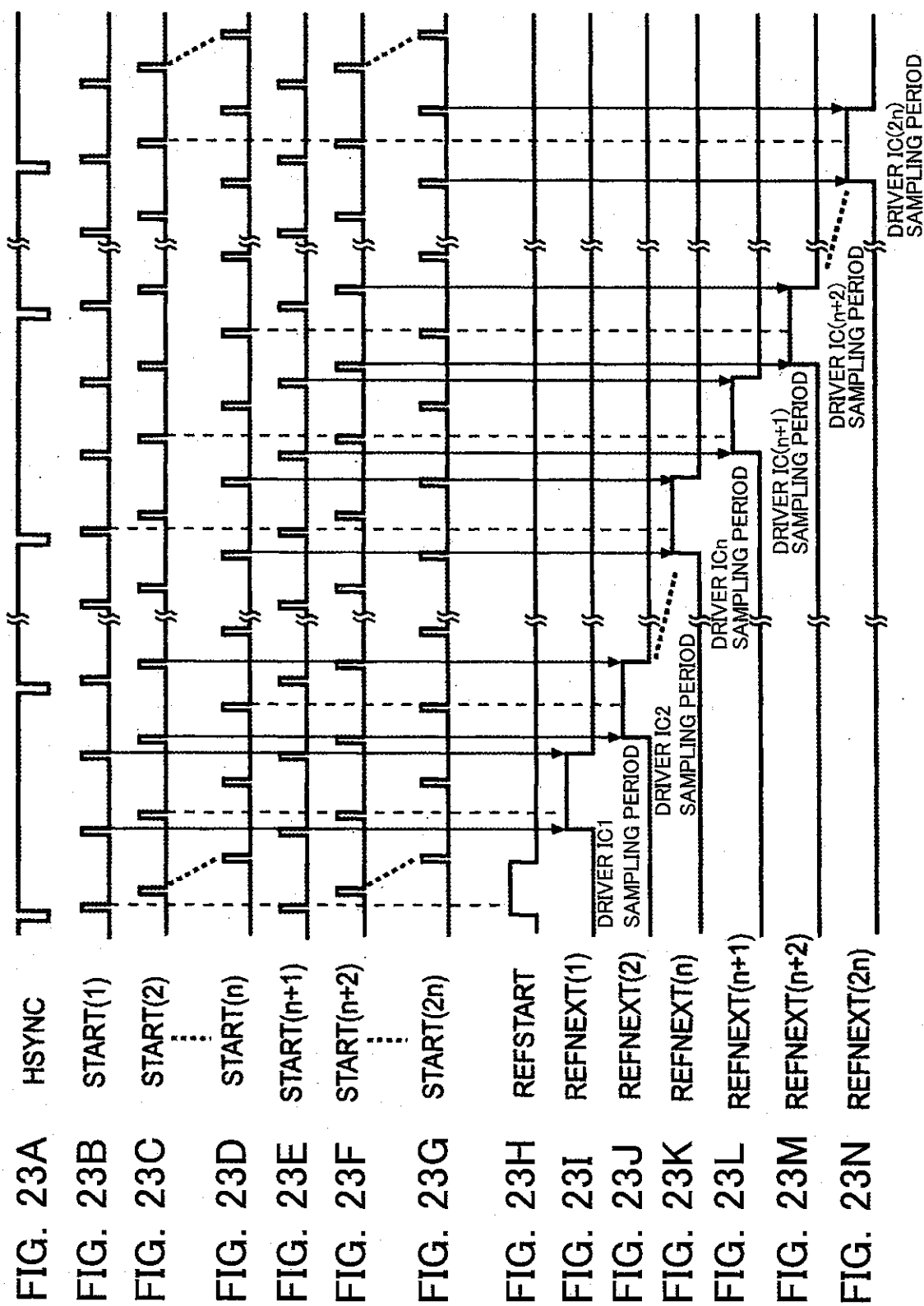


FIRST BANK READING
SECOND BANK WRITING

FIRST BANK WRITING
SECOND BANK READING

FIG. 22





LIST OF REFERENCES

- 100... organic EL display device
- 101, 101-1 to 101-n... current output type data line driver (driver IC)
- 5 200 (-1 to -n), 200A, 200B... reference current source circuit (IREFC)
- 300... control circuit (CTL)
- 400... write circuit (WRT)
- 500... flag use bi-directional shift register (FSFT)
- 10 600... image data use register array (REGARY)
- 700-1, 700-(m/2)... control signal generation circuit (GEN)
- 800-1 to 800-m... current output type DAC (digital/analog converter)
- 15 900-1 to 900-m... current output circuit (IOUT)
- 901... first bank
- 902... second bank
- 903... current output transistor array
- 1000... test circuit (TST)