ABSTRACT OF THE DISCLOSURE

PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS

An integrated circuit package comprises an integrated circuit die comprising a first pad, a second pad adjacent to the first pad, a third pad adjacent to the second pad, and a fourth pad adjacent to the third pad. A lead frame comprises a first lead, a second lead adjacent to the first lead, a third lead adjacent to the second lead, and a fourth lead adjacent to the third lead, wherein a first end of the fourth lead extends beyond at least one of the first, second, and third leads and in a direction towards a path defined by the third lead. First, second, third and fourth bondwires connecting the first, second, fourth and third leads to the first, second, third and fourth pads, respectively.

FIG. 12A



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