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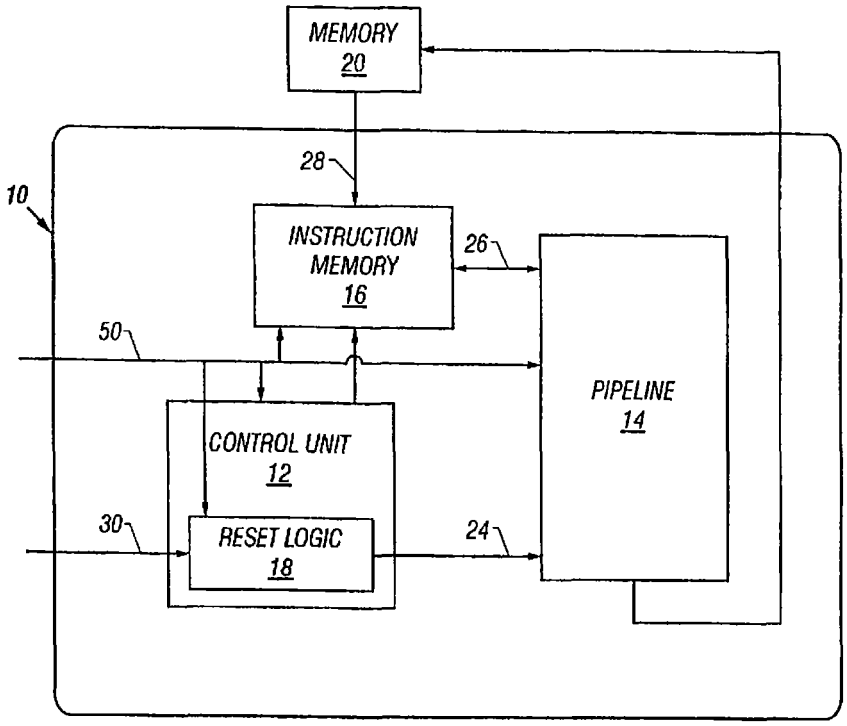
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(54) Title: INSTRUCTION LOADING AFTER PROCESSOR RESET



(57) Abstract: In one embodiment, a method is disclosed for holding instruction fetch requests of a processor in an extended reset. Fetch requests are disabled when the processor undergoes a reset. When the reset is completed, fetch requests remain disabled when the instruction memory is being loaded When loading of the instruction memory is completed, fetch request are enabled.

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