ABSTRACT

A semiconductor device may include a substrate including a core region, a cell block region, and a peripheral region, which are sequentially arranged in a first direction, and a bit line structure on the cell block region. The bit line structure may include a first bit line and a second bit line, which extend in the first direction and are adjacent to each other in a second direction crossing the first direction, a bit line connector, which electrically connects the first bit line to the second bit line and is adjacent to the peripheral region, and a bit line pad, which is electrically connected to the first bit line and is adjacent to the core region.

FIG. 4A