

ABSTRACT OF THE DISCLOSURE



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SYSTEMS AND METHODS FOR FACILITATING TESTING OF PAD
DRIVERS OF INTEGRATED CIRCUITS

A preferred integrated circuit (IC) includes a first pad electrically communicating with at least a portion of the IC. The first pad includes a first driver and a first receiver, with the first driver being configured to provide a first pad output
5 signal to a component external to the IC, and the first receiver being configured to receive a first pad input signal from a component external to the IC. The first receiver also is configured to provide, to a component internal to the IC, a first receiver digital output signal in response to the first pad input signal. A first test circuit also is provided that is internal to the IC. The first test circuit is adapted to provide
10 information corresponding to the driver clock-to-q time of the first pad. Systems, methods and computer-readable media also are provided.



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