

FIG. 1

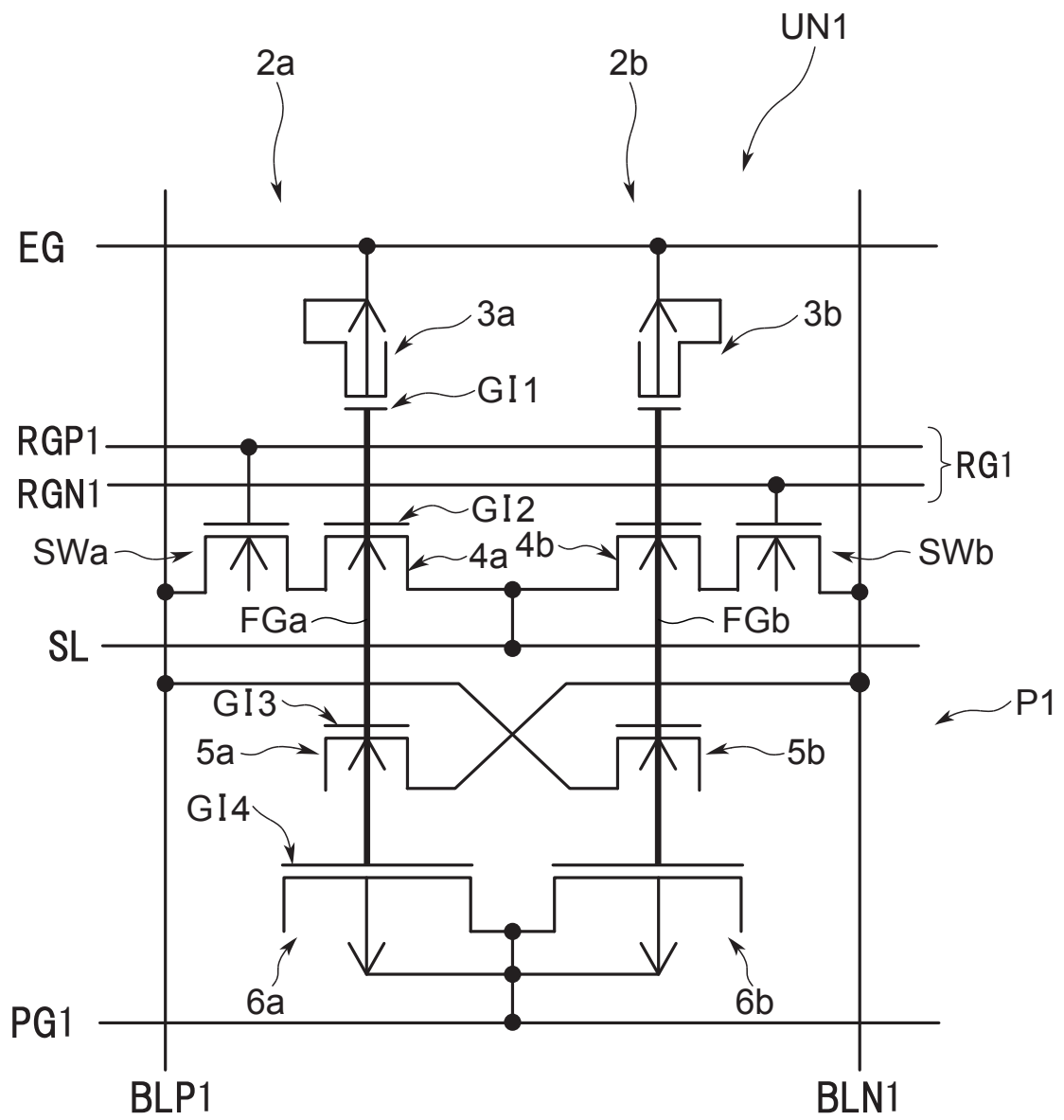


FIG. 2

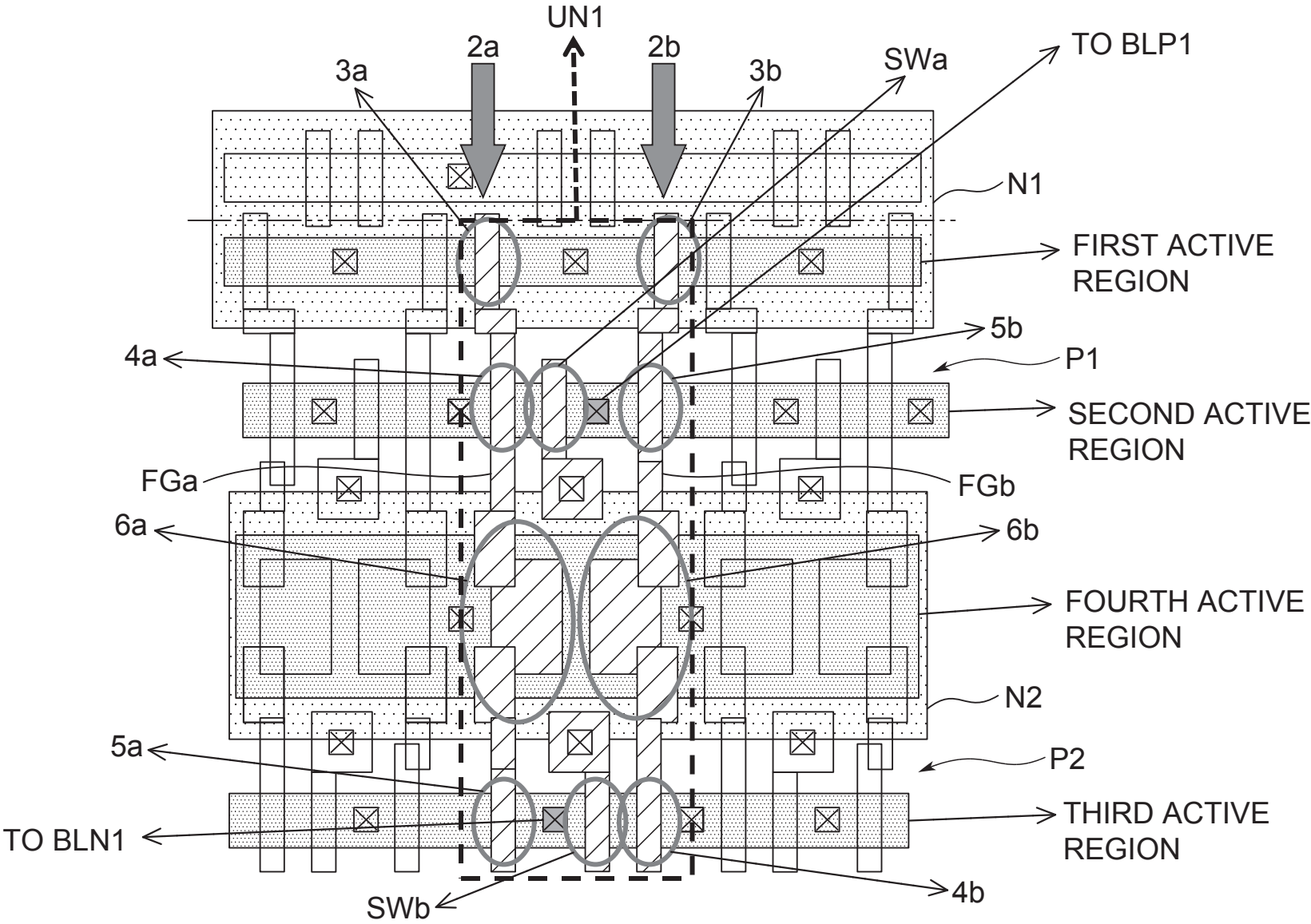


FIG. 3

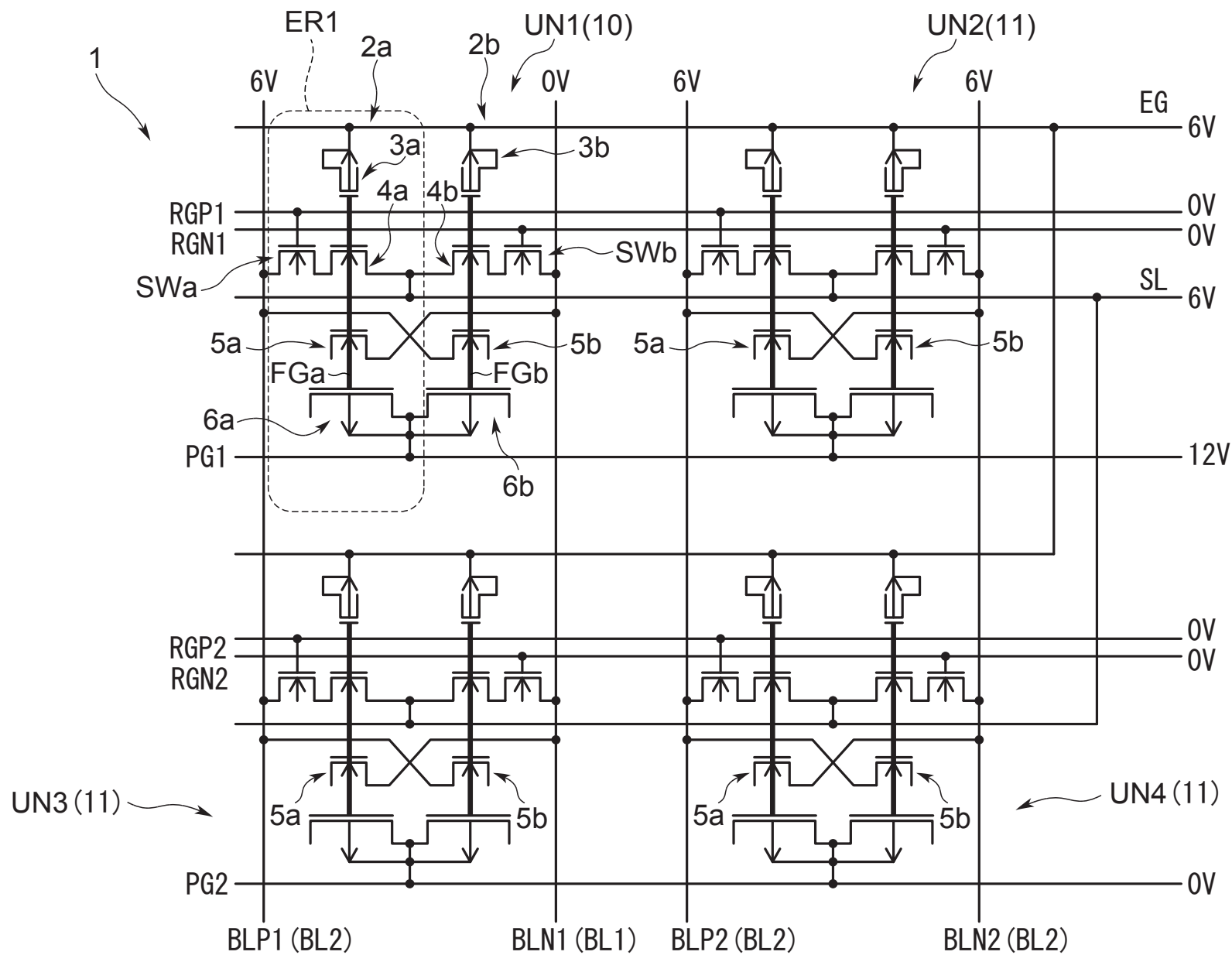


FIG. 4

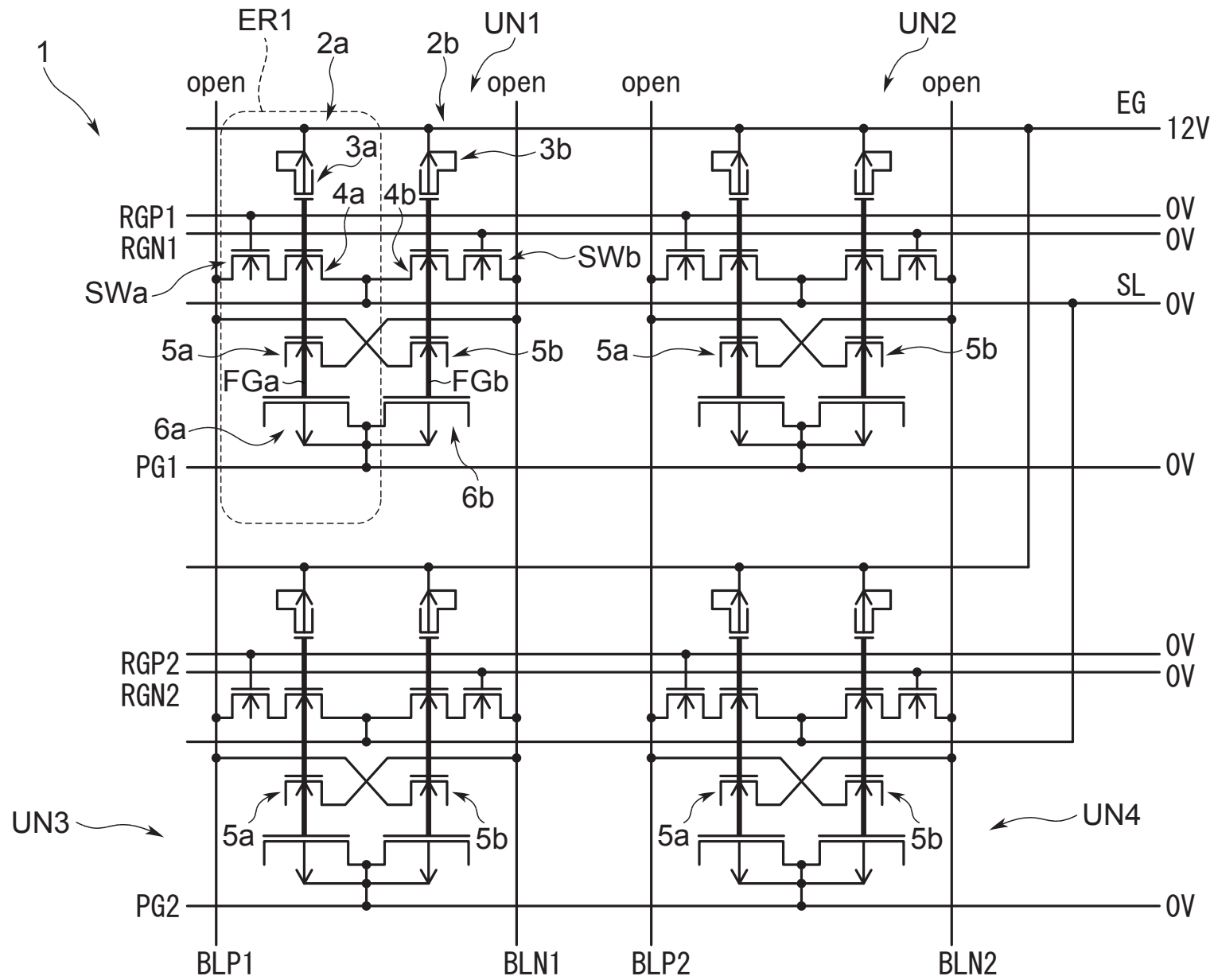


FIG. 5

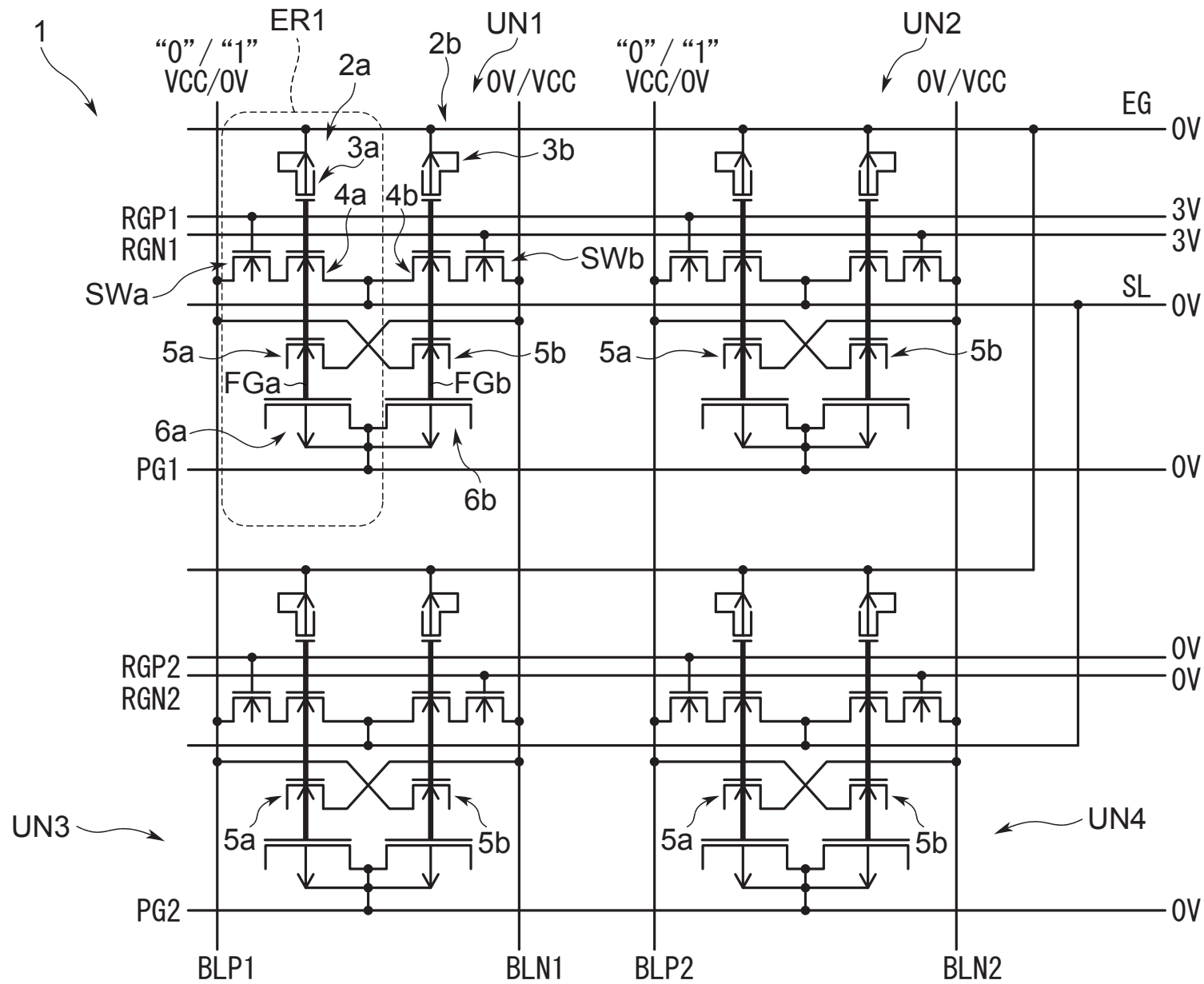


FIG. 6

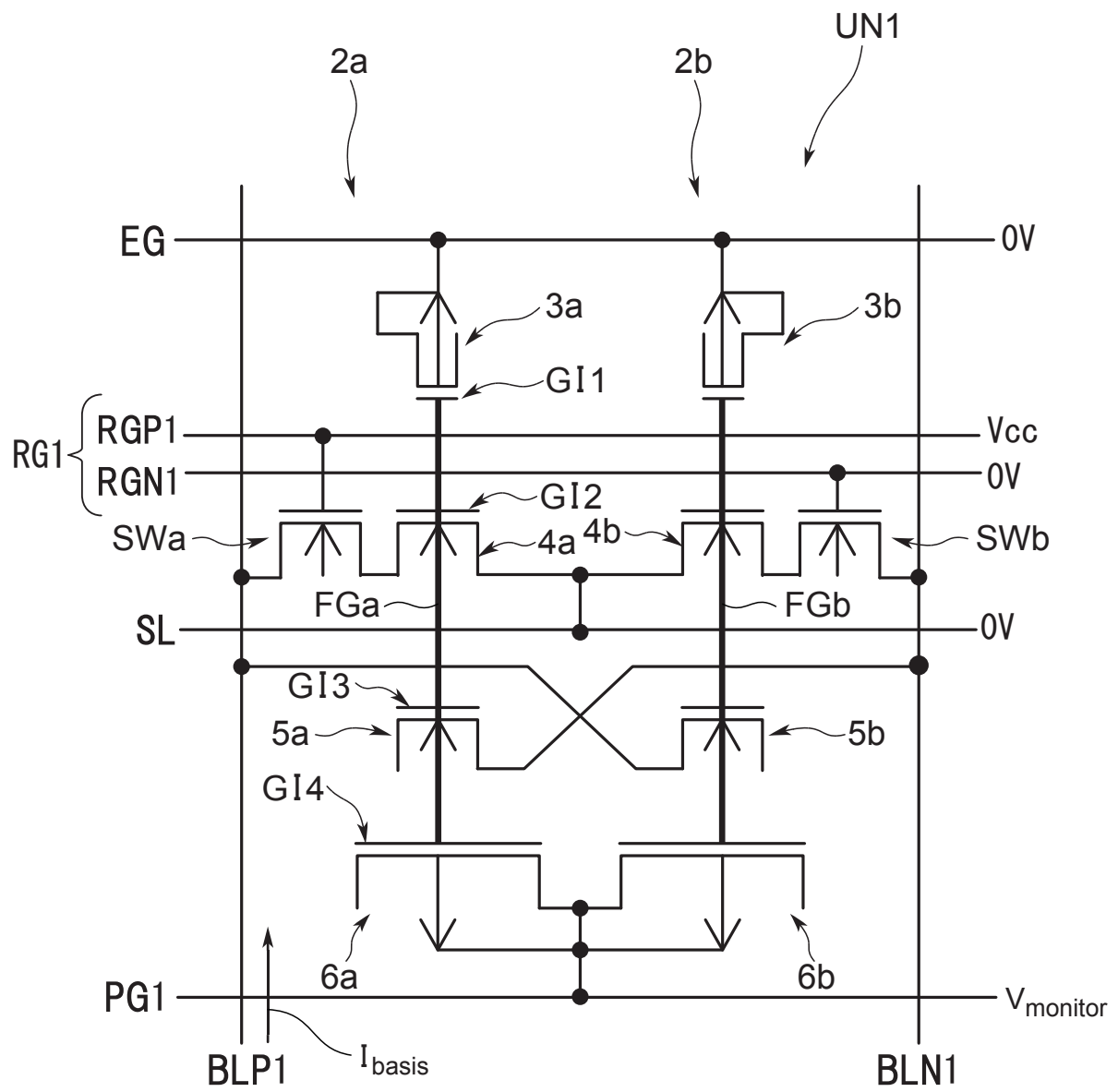


FIG. 7

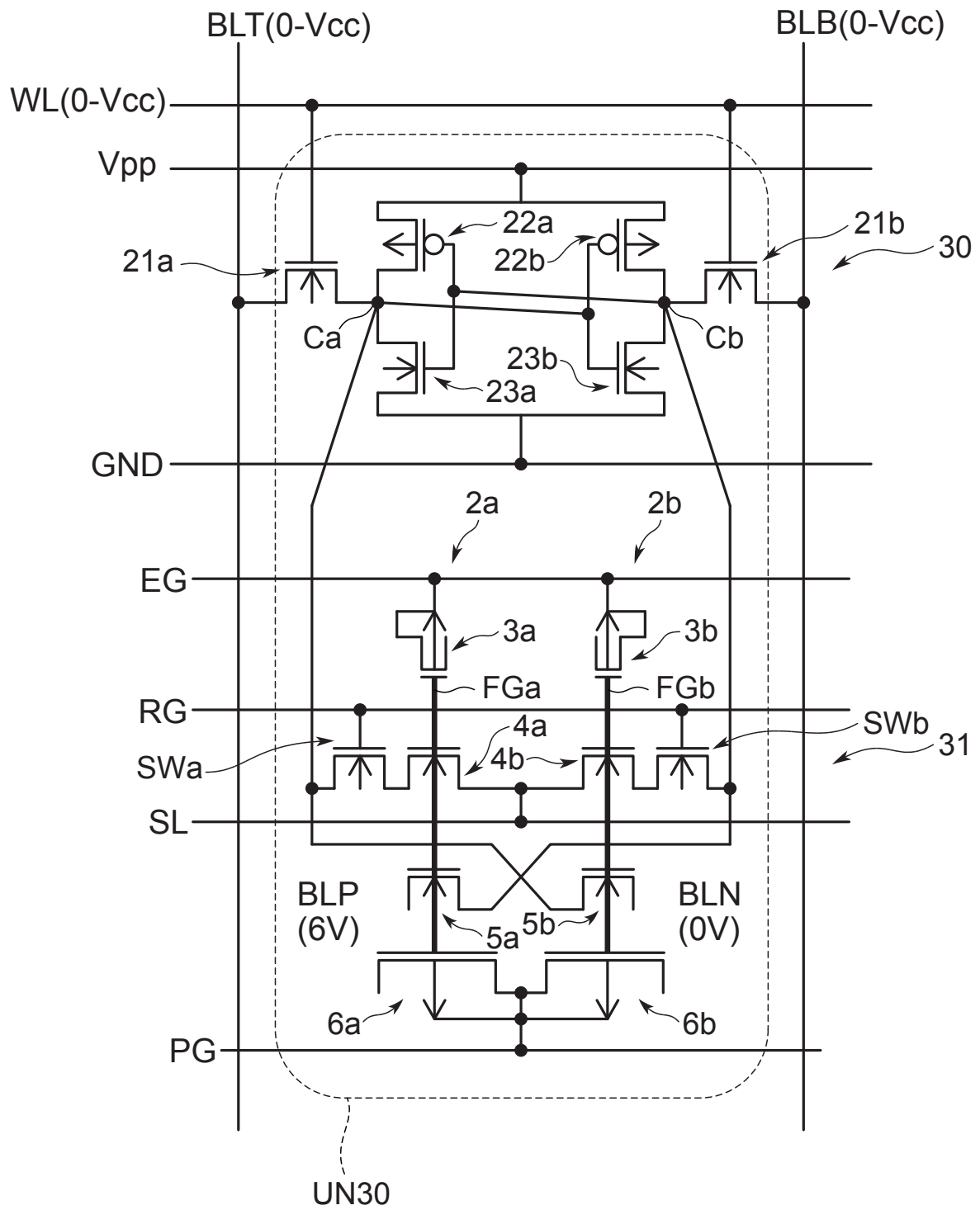


FIG. 8

	EG	RG	SL	PG	Vpp	WL
PROGRAMMING (FROM SRAM TO NON-VOLATILE MEMORY UNIT)	6	0	6	12	6	0
ERASURE (DATA ERASURE IN NON-VOLATILE MEMORY UNIT)	12	0	0	0	Vcc	0
DATA LOADING (FROM NON-VOLATILE MEMORY UNIT TO SRAM)	0	Vcc	0	0	Vcc	0
WRITING (WRITING INTO SRAM)	Don't care	0	Don't care	Don't care	Vcc	Vcc/0
READING (READING FROM SRAM)	Don't care	0	Don't care	Don't care	Vcc	Vcc/0