31 AUG 2005

Fig.1A

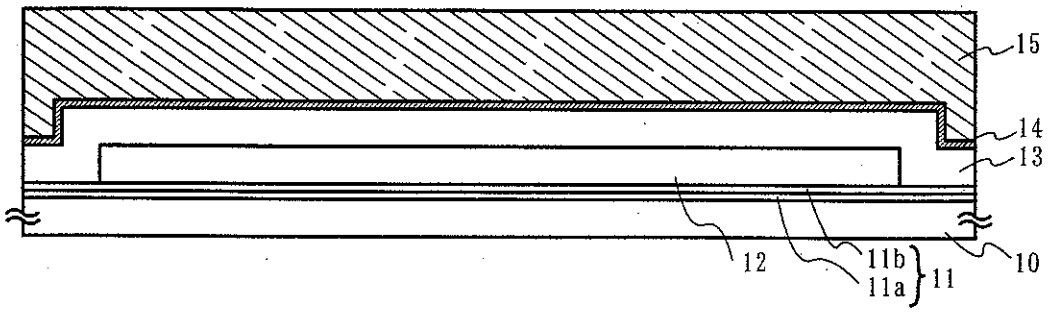


Fig.1B

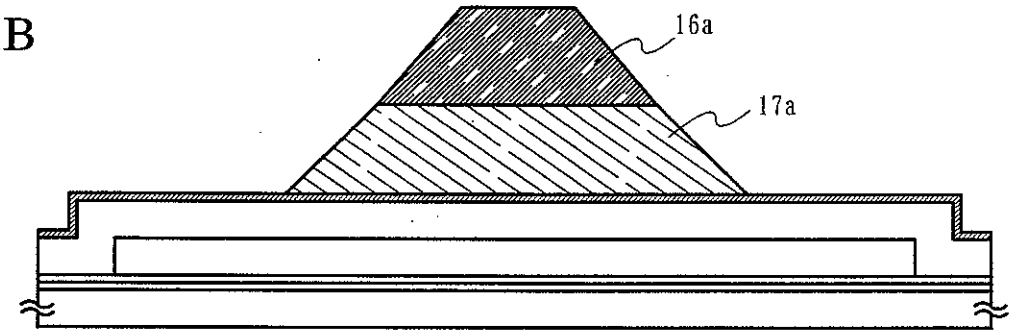


Fig.1C

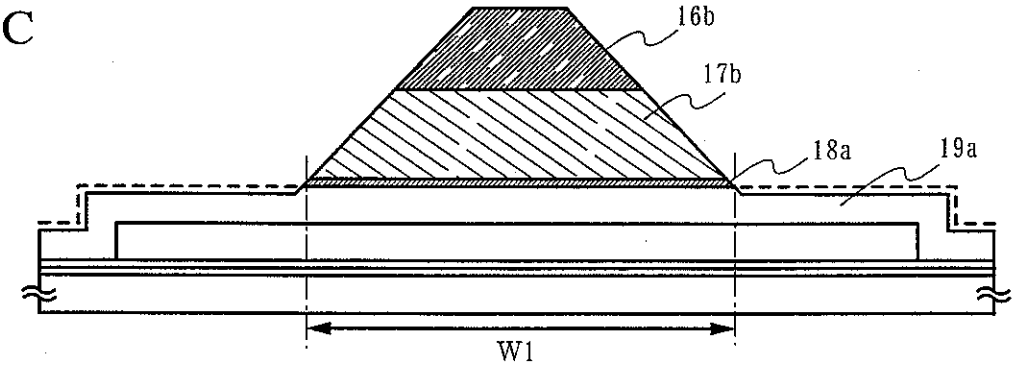
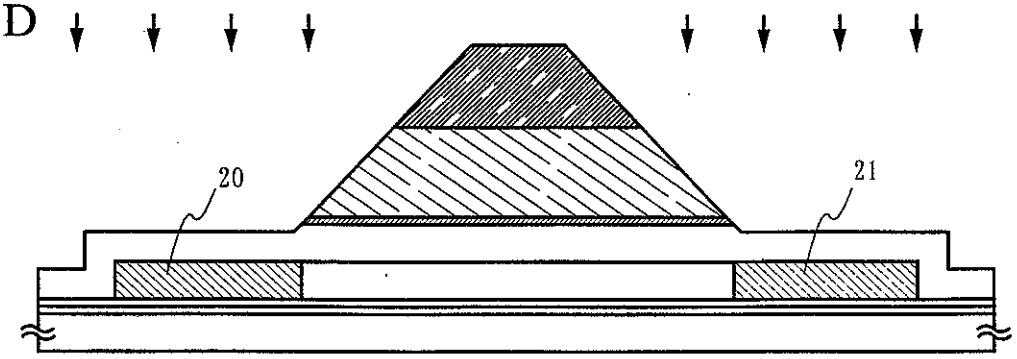


Fig.1D



31 AUG 2005

Fig.2A

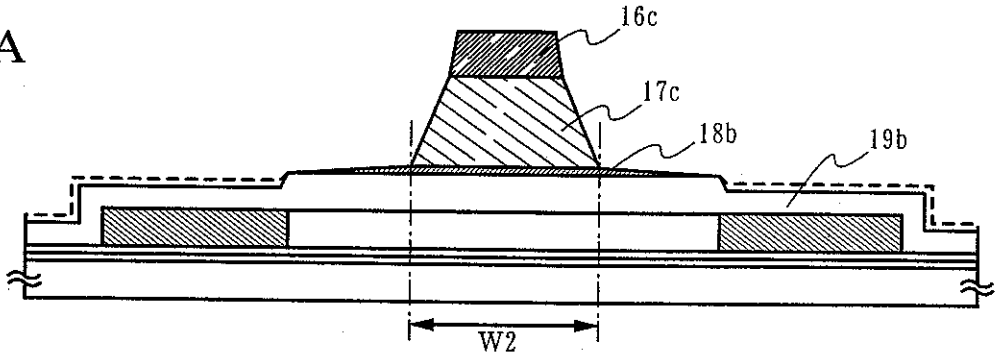


Fig.2B

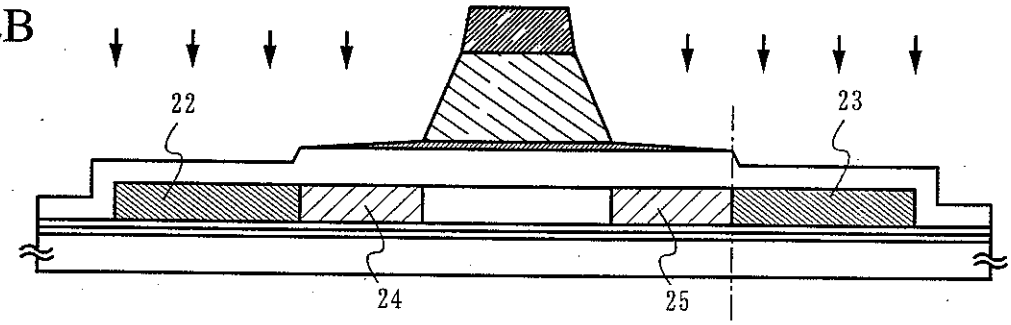


Fig.2C

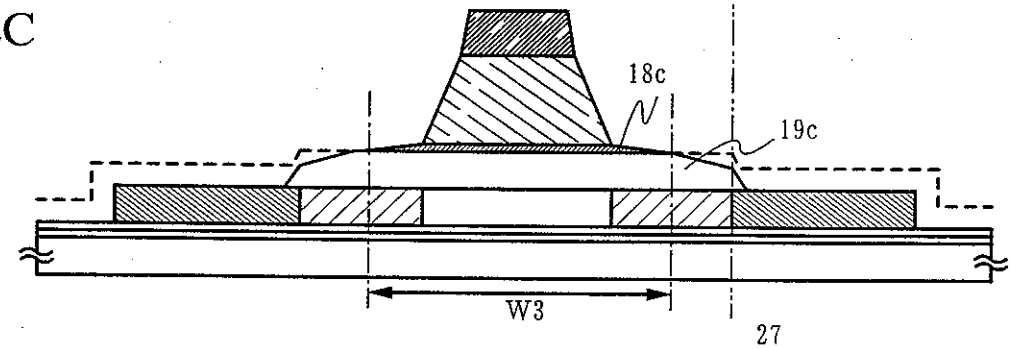
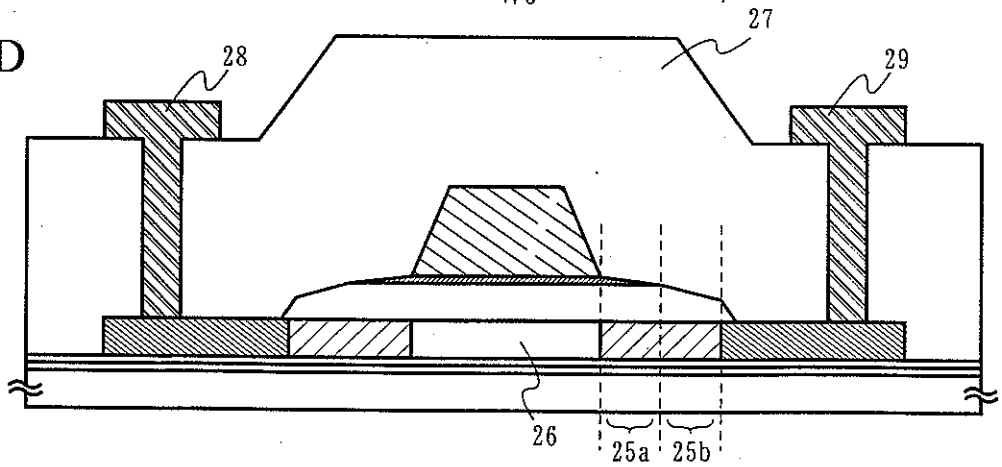
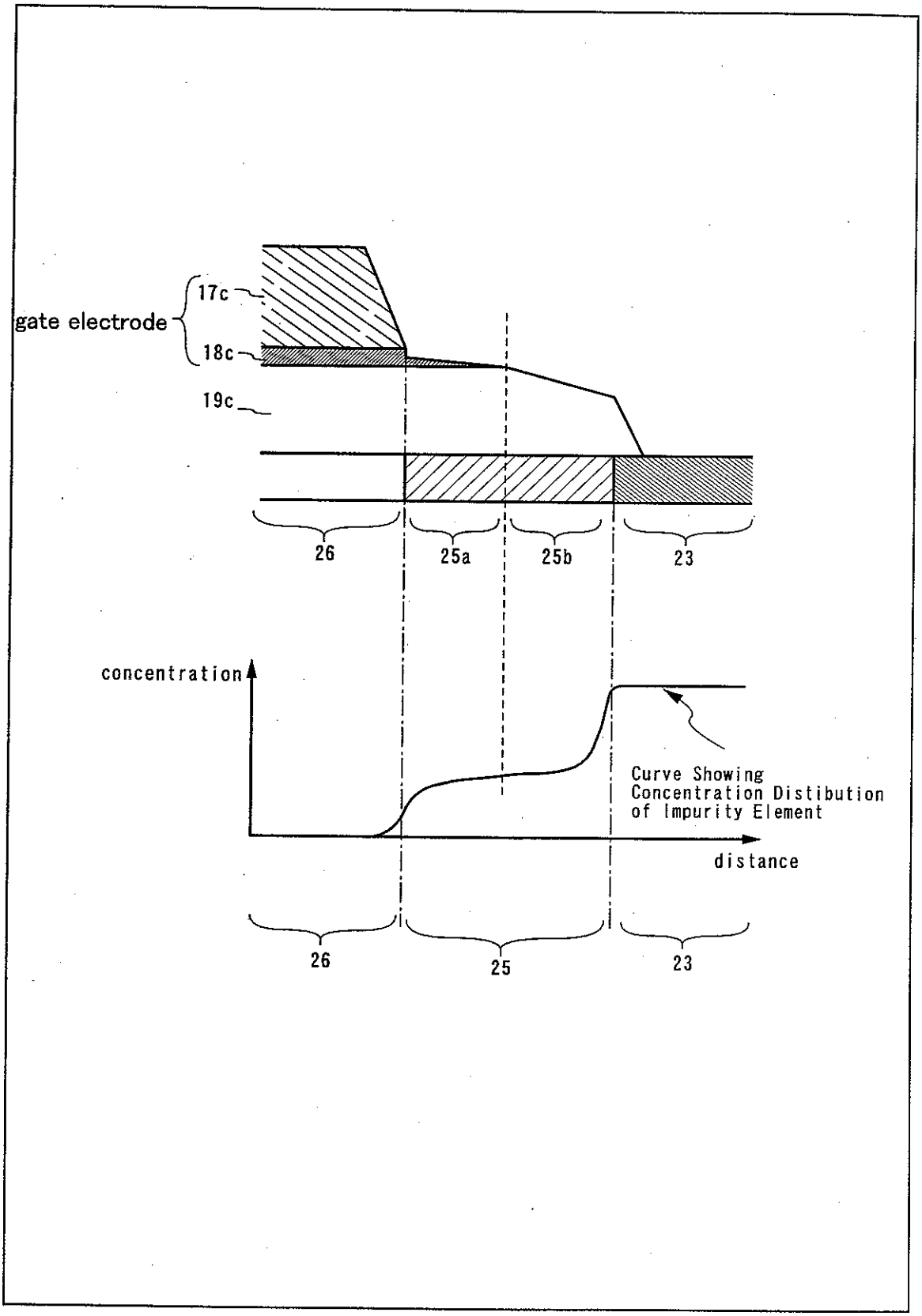


Fig.2D



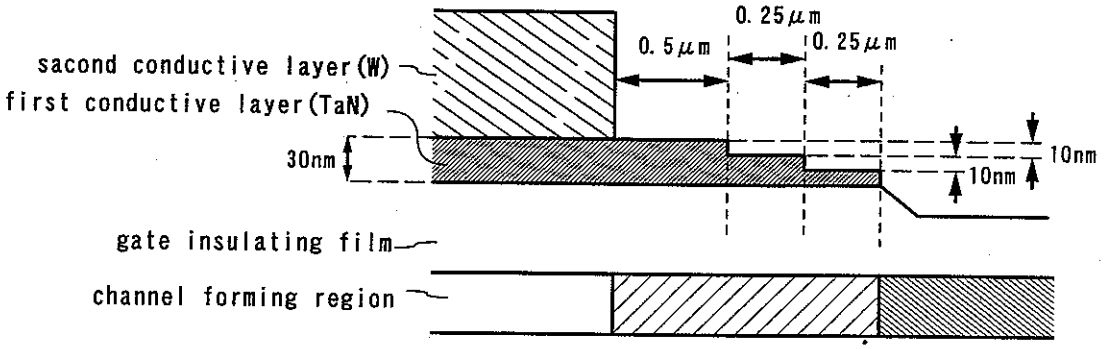
31 AUG 2005

Fig.3



31 AUG 2005

Fig.4



Schematic Structual View in Simulation

Fig. 5

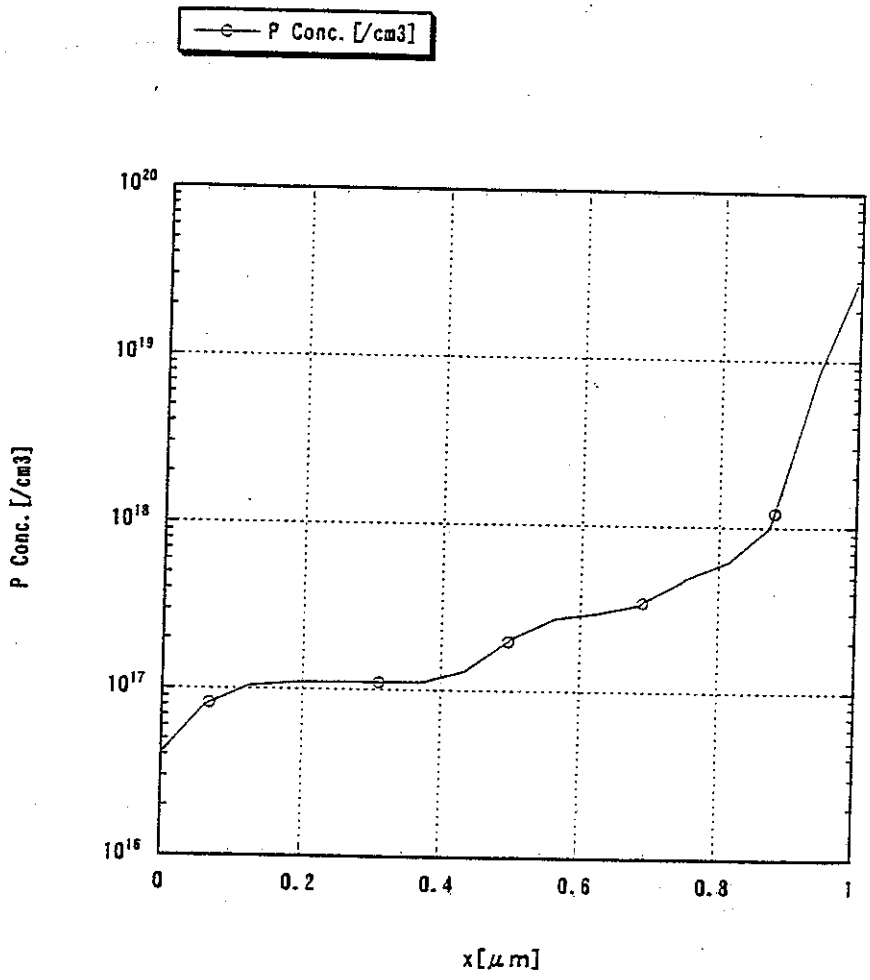
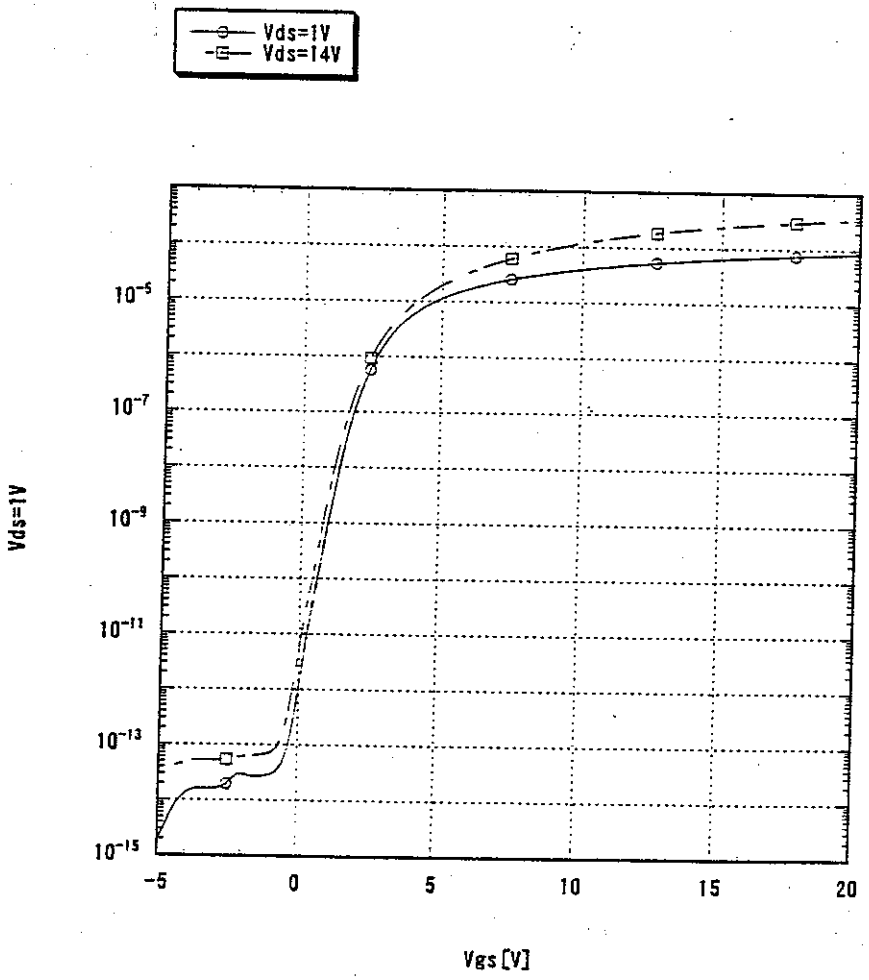


Fig. 6


Vds=1V
Vds=14V
Vds=1V
10^-5
10^-7
10^-9
10^-11
10^-13
10^-15
-5
0
5
10
15
20
Vgs[V]

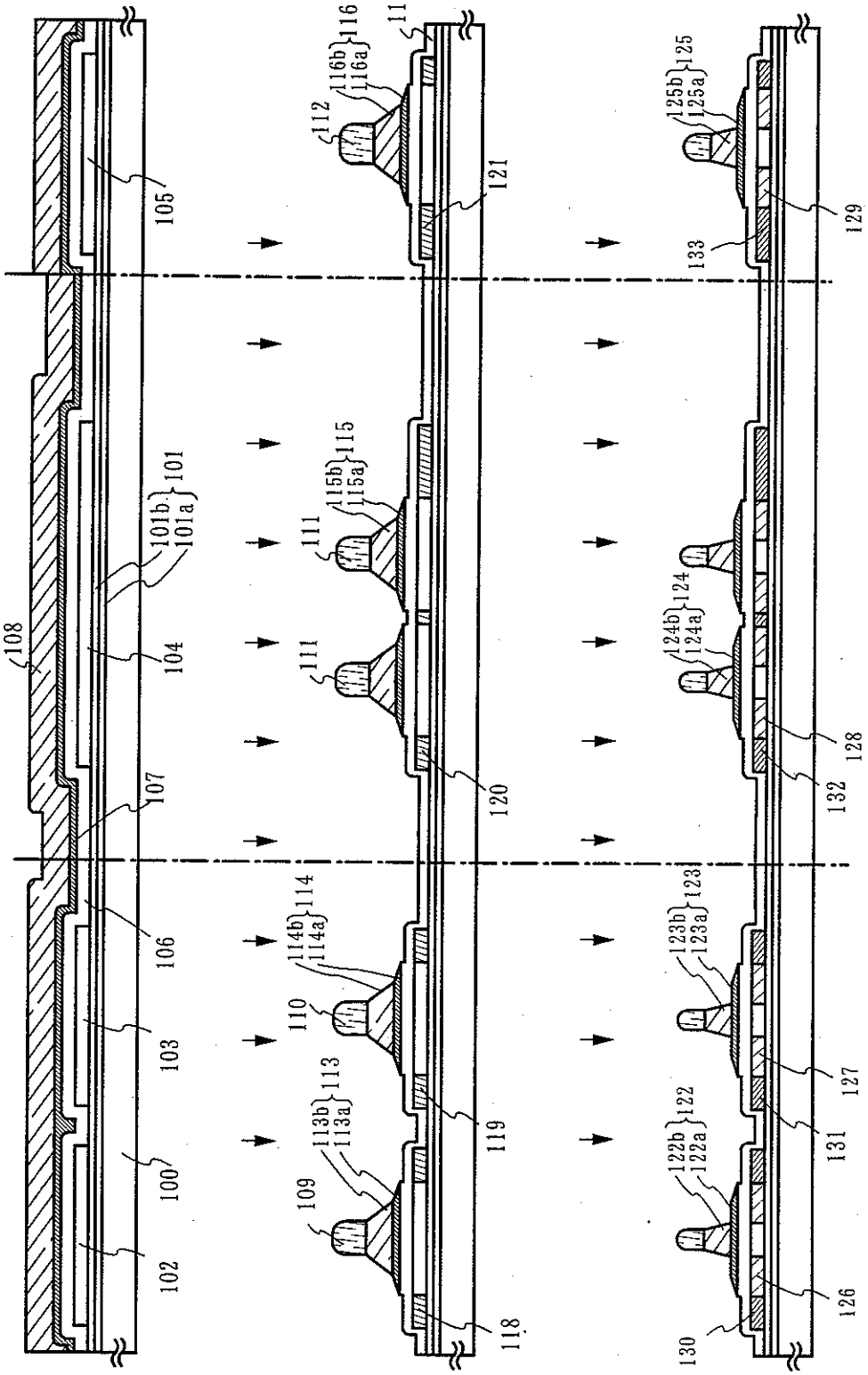


Fig.7A Fig.7B Fig.7C

31 AUG 2005

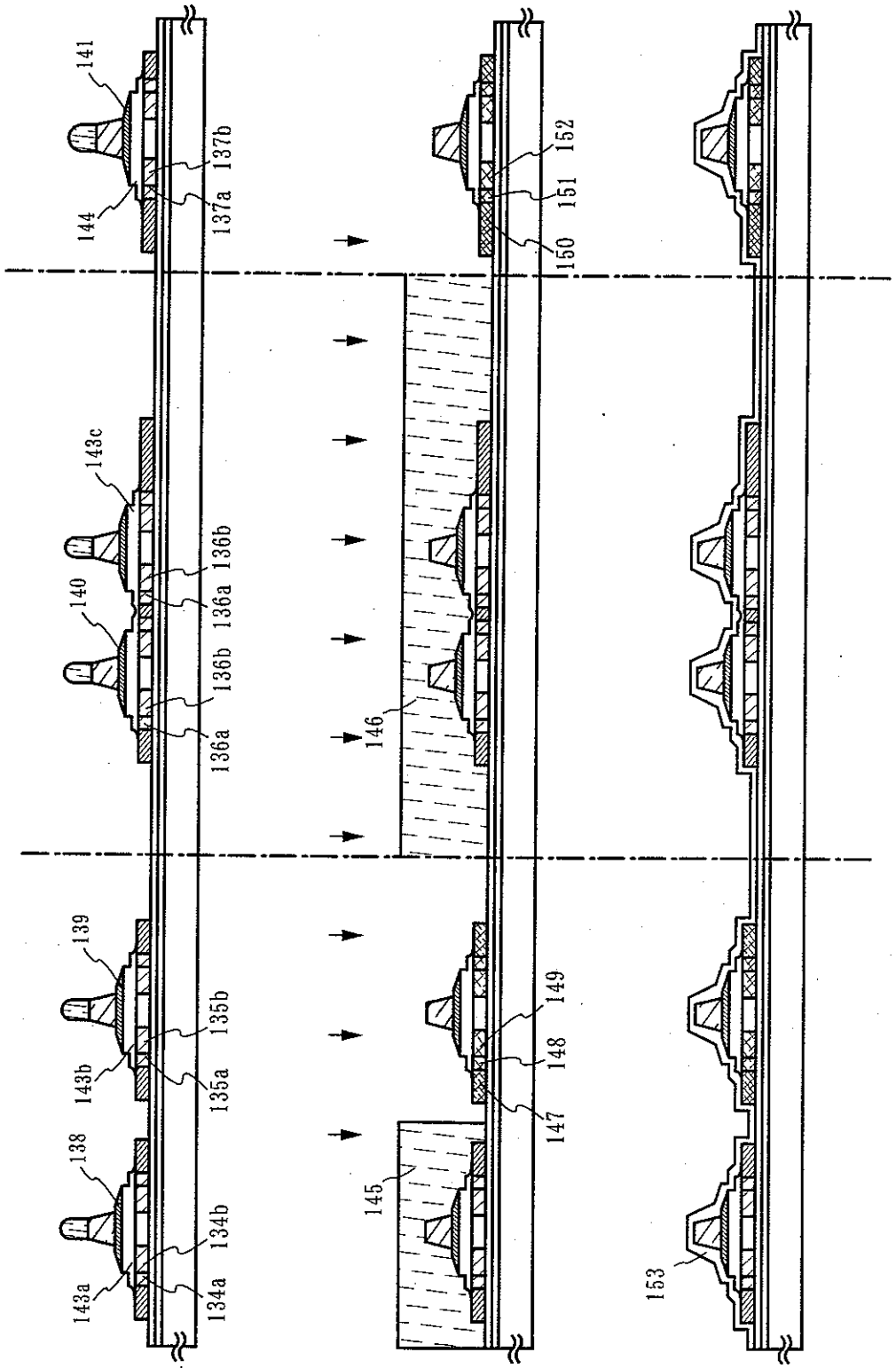
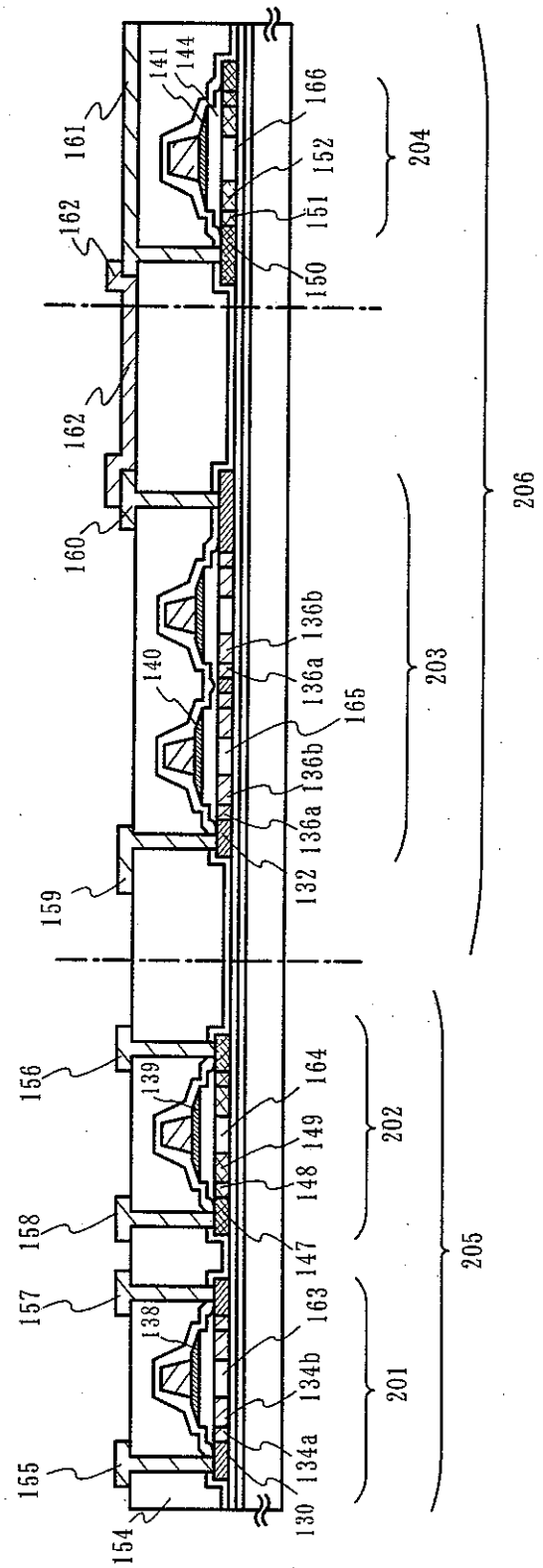


Fig.8A Fig.8B Fig.8C

Fig.9


141
144
166
161
152
151
204
162
150
162
160
140
136b
136a
165
203
206
159
132
136a
136b
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201
205
155
134a
130
154

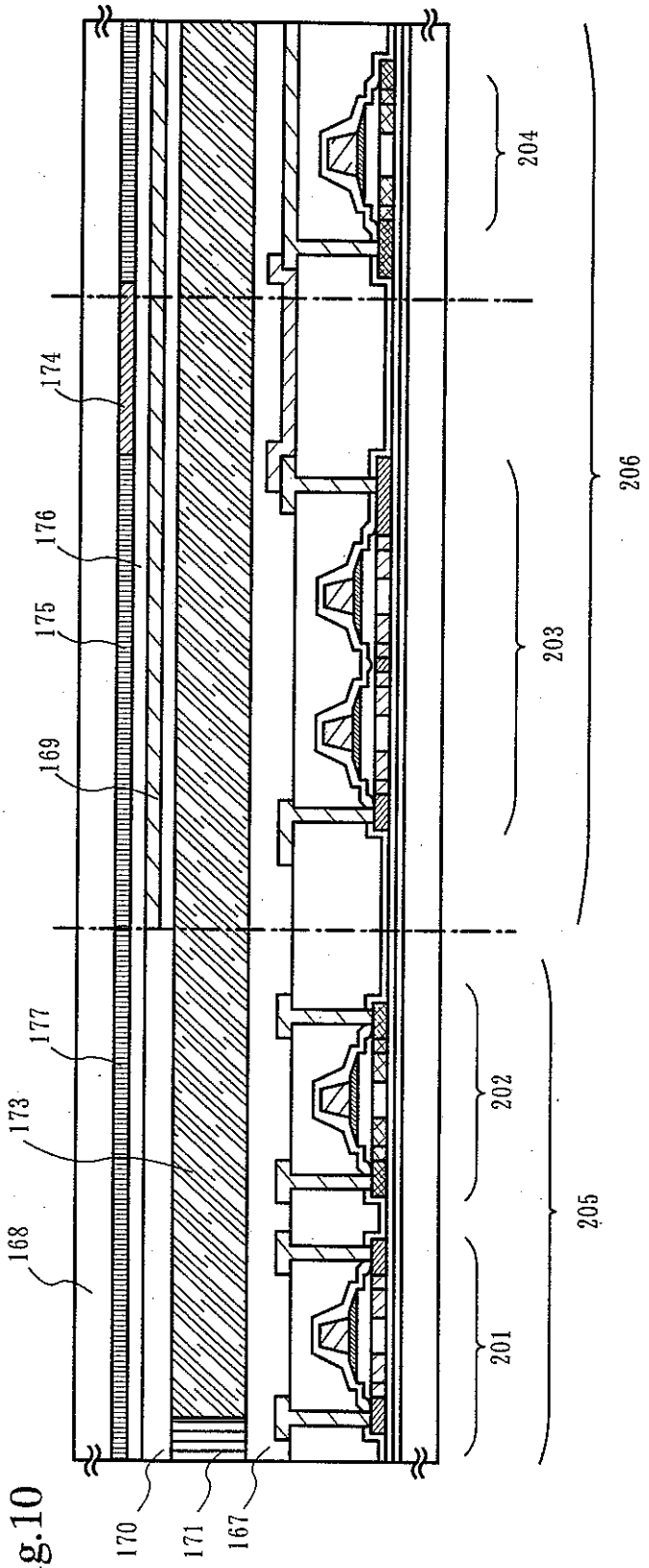


Fig.10

Fig.11A

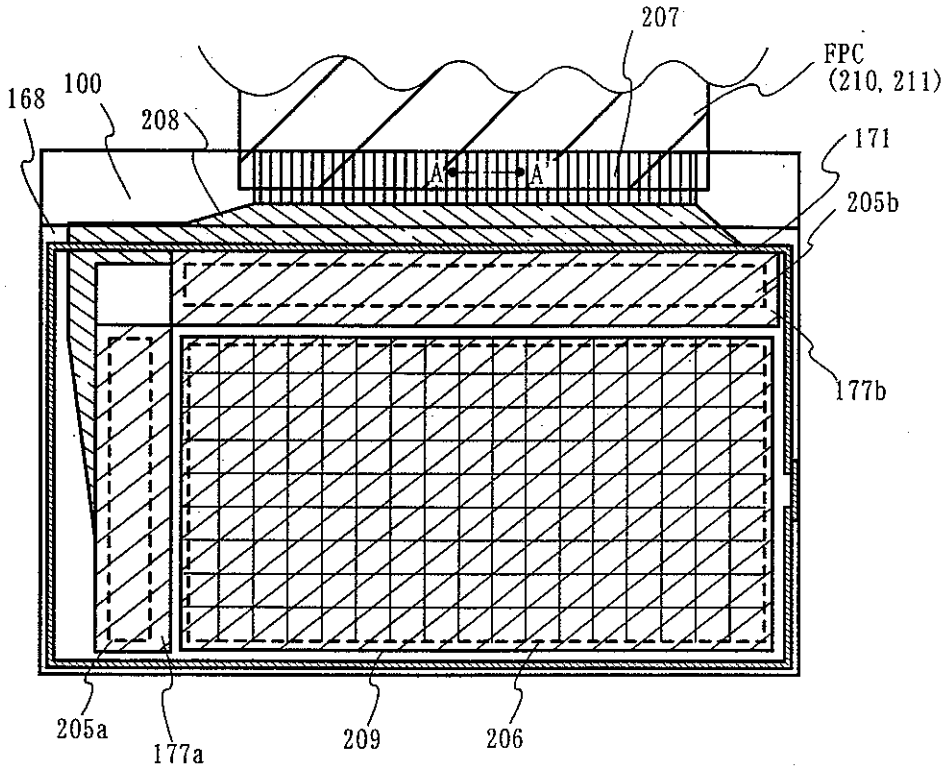


Fig.11B

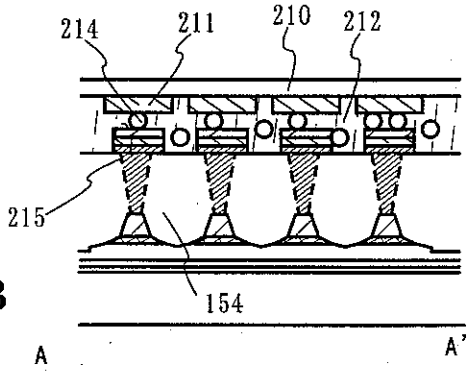


Fig.12A

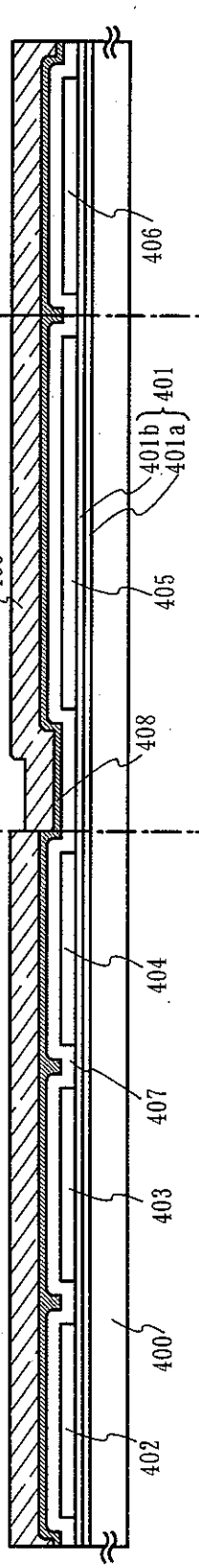


Fig.12B

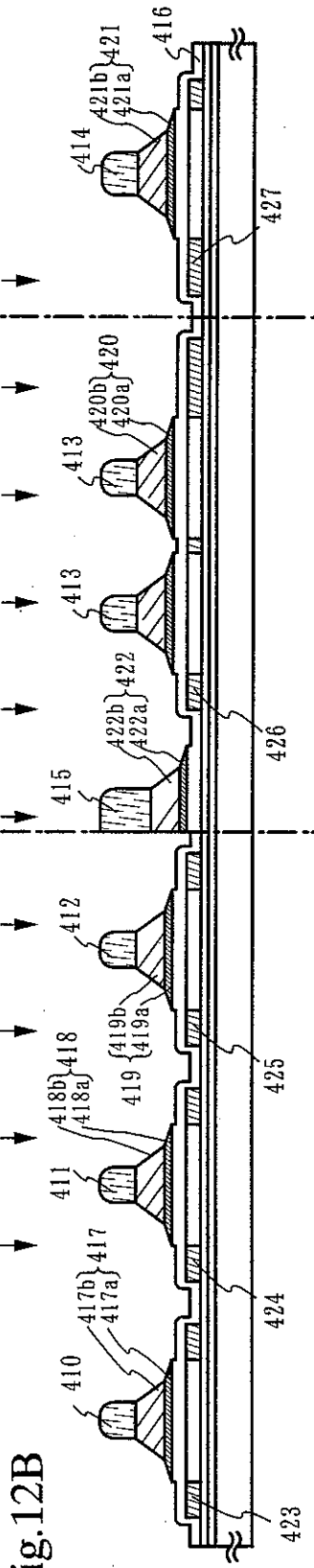
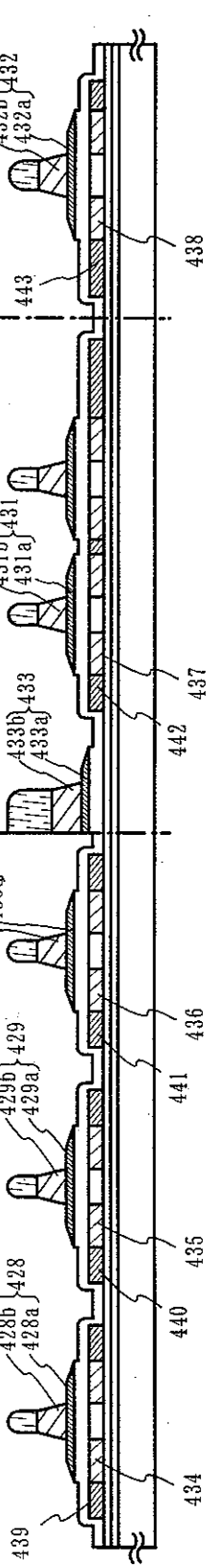


Fig.12C



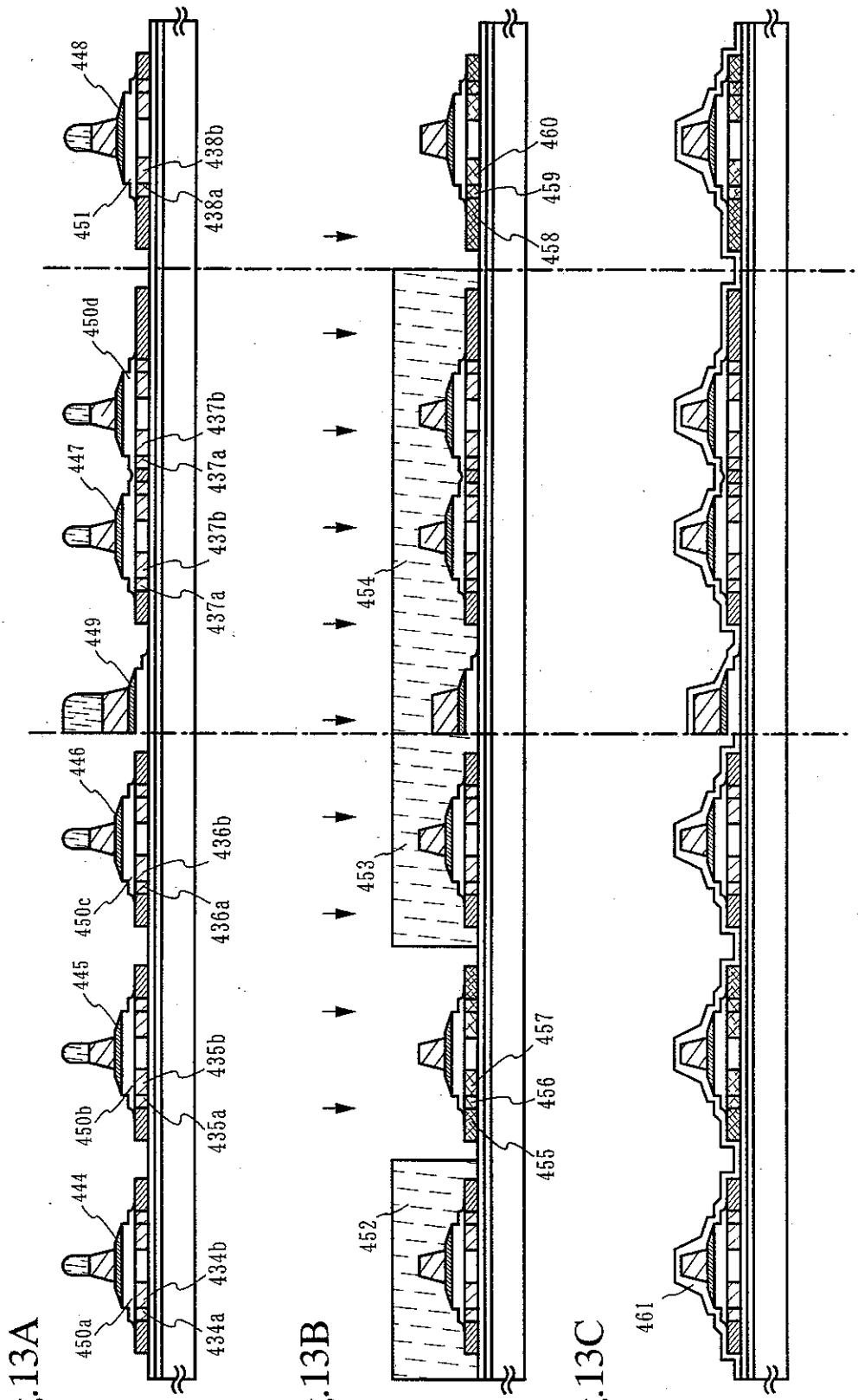
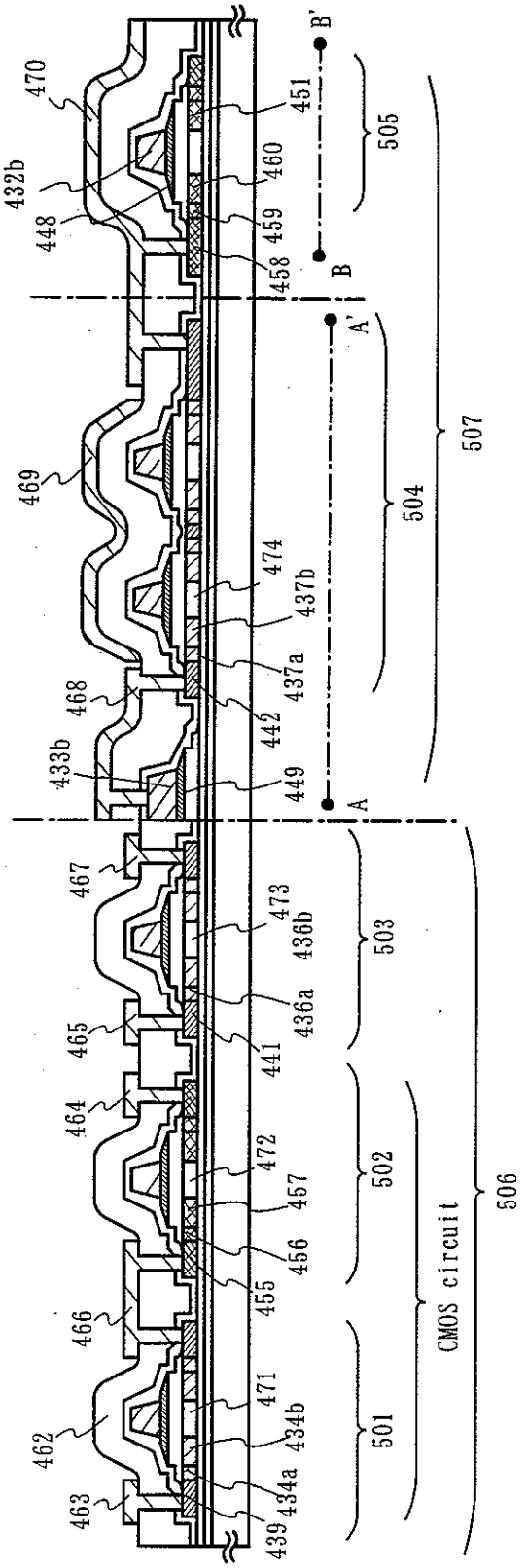


Fig.13A

Fig.13B

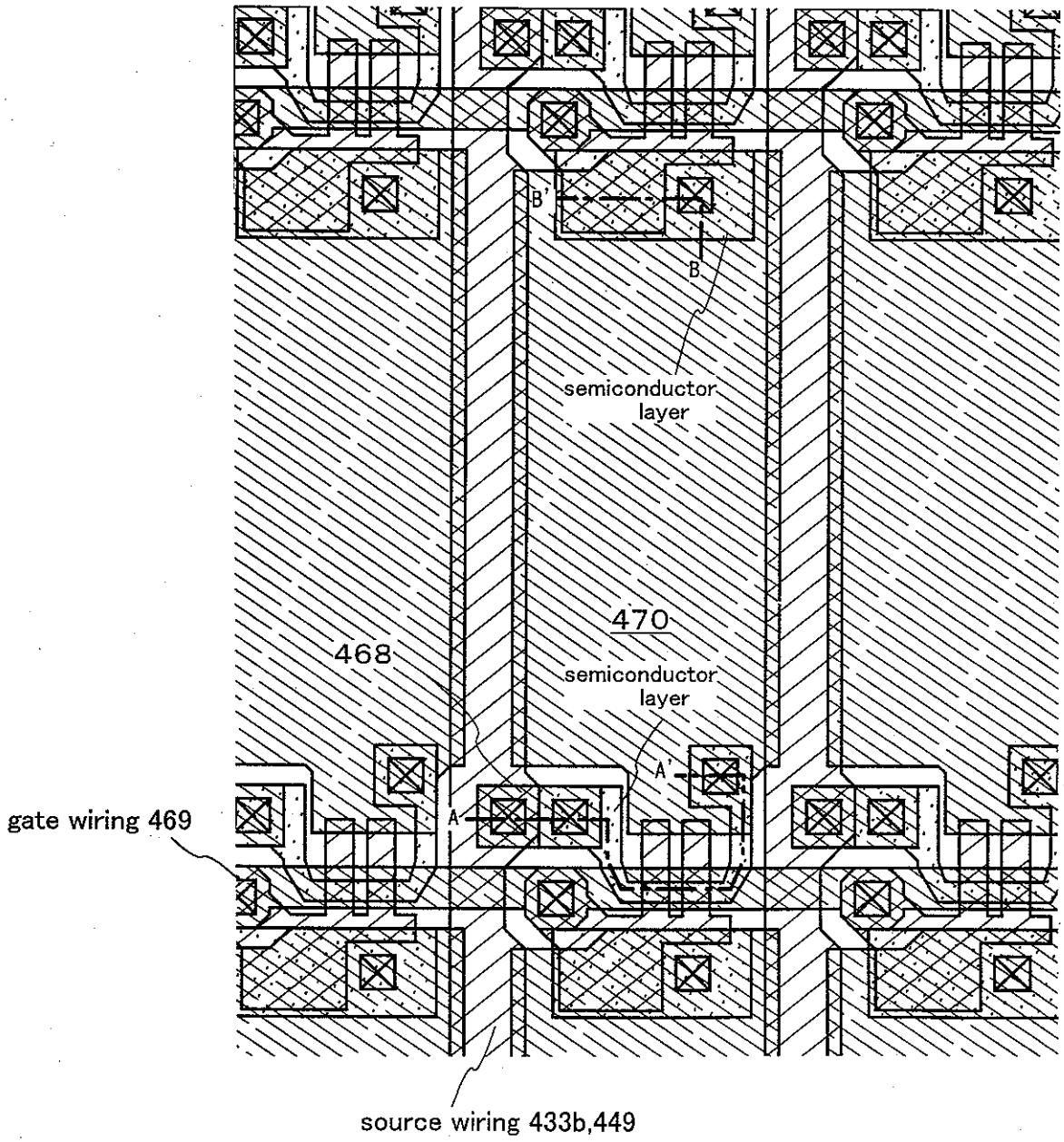
Fig.13C

Fig.14


470
451
B'
505
432b
460
448
459
458
B
A'
469
504
507
474
437b
437a
468
442
433b
449
A
467
473
436b
503
465
436a
441
464
472
457
502
456
455
466
471
501
CMOS circuit
506
462
434b
463
434a
439

31 AUG 2005

Fig.15



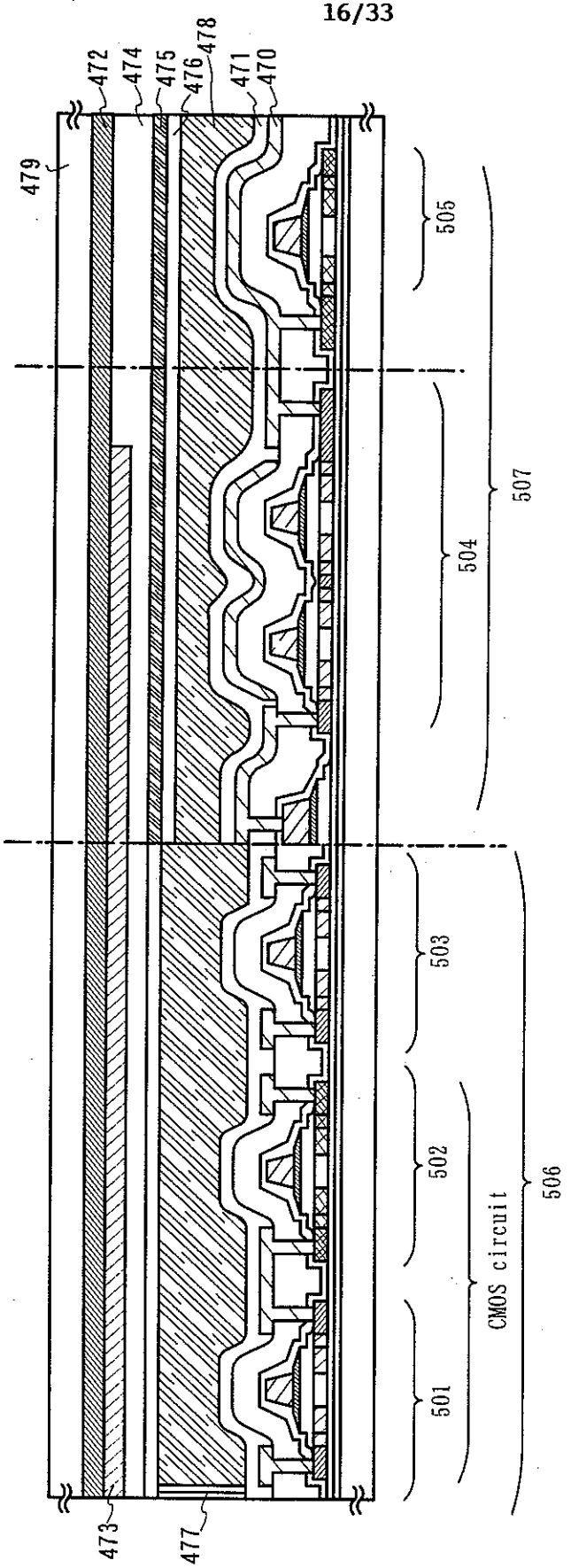
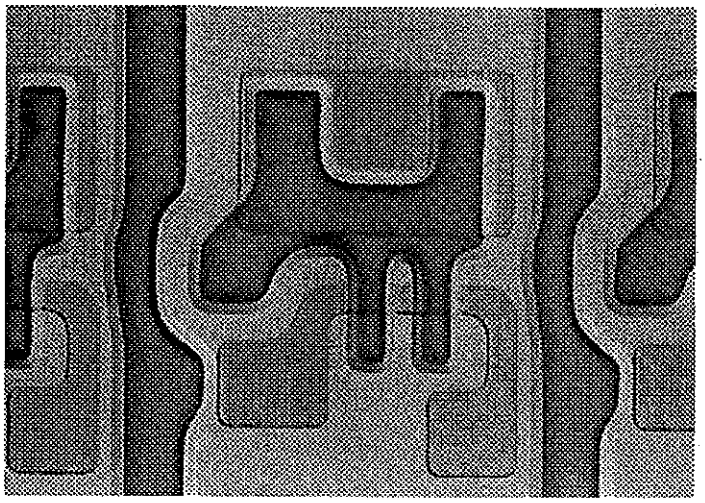

472
474
475
476
478
471
470
479
505
504
507
503
502
501
CMOS circuit
506
473
477

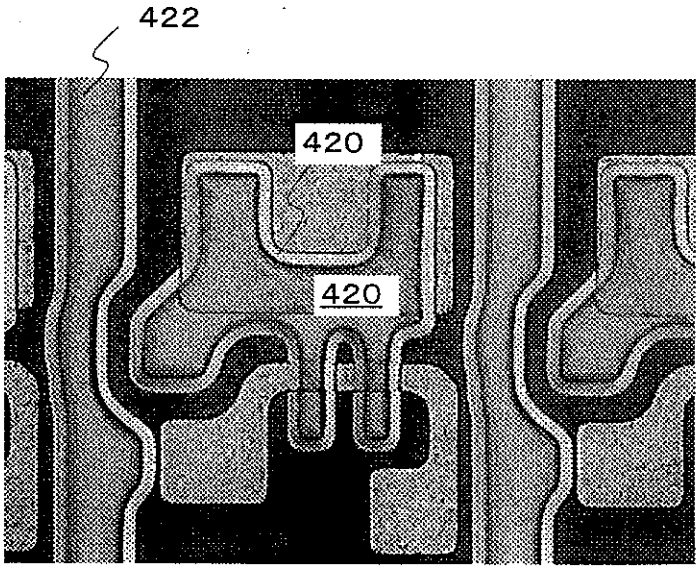
Fig.16

Fig. 17A



ICP power: 500W, RF power: 150W, chamber pressure: 1.0Pa
Cl_2=25sccm、CF_4=25sccm、O_2=10sccm

Fig.17B



ICP power: 500W, RF power: 20W, chamber pressure: 1.0Pa
Cl_2=30sccm、CF_4=30sccm

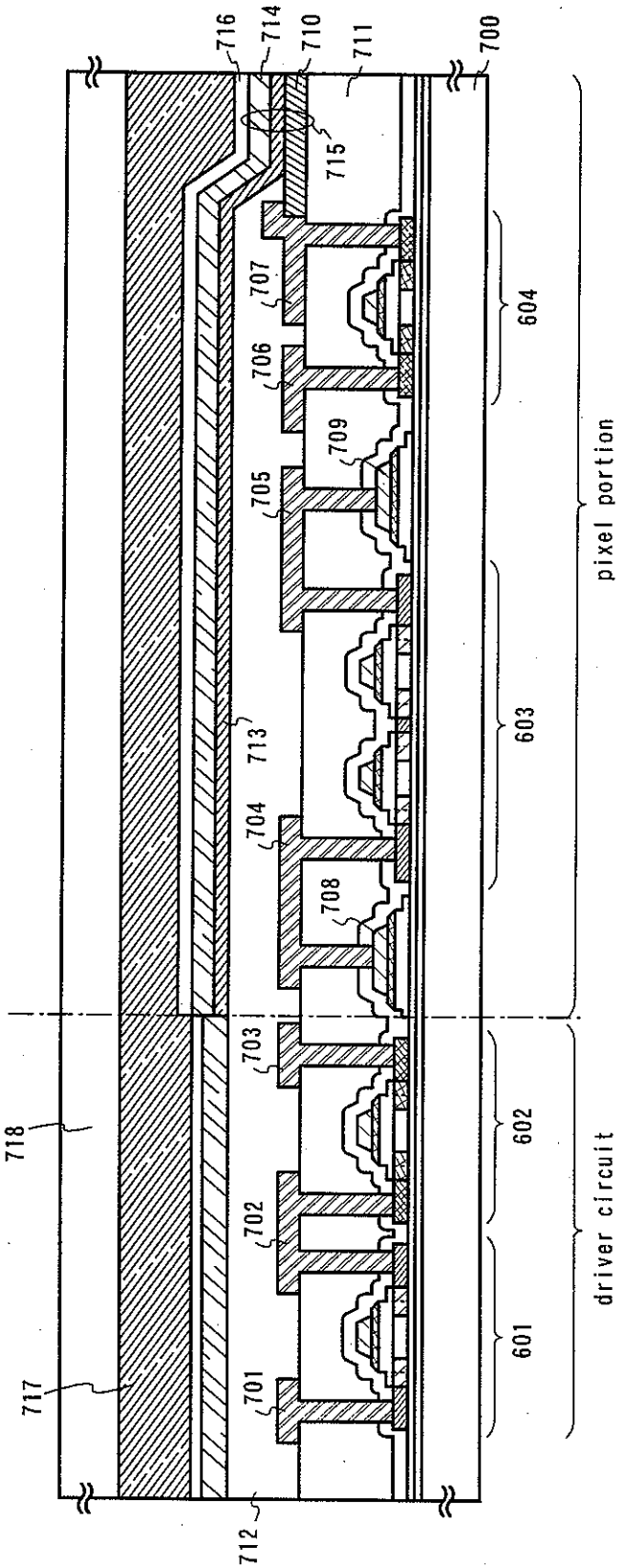

716
714
710
711
700
715
707
706
709
705
713
704
708
703
718
702
717
701
712
604
603
602
601
pixel portion
driver circuit

Fig.18

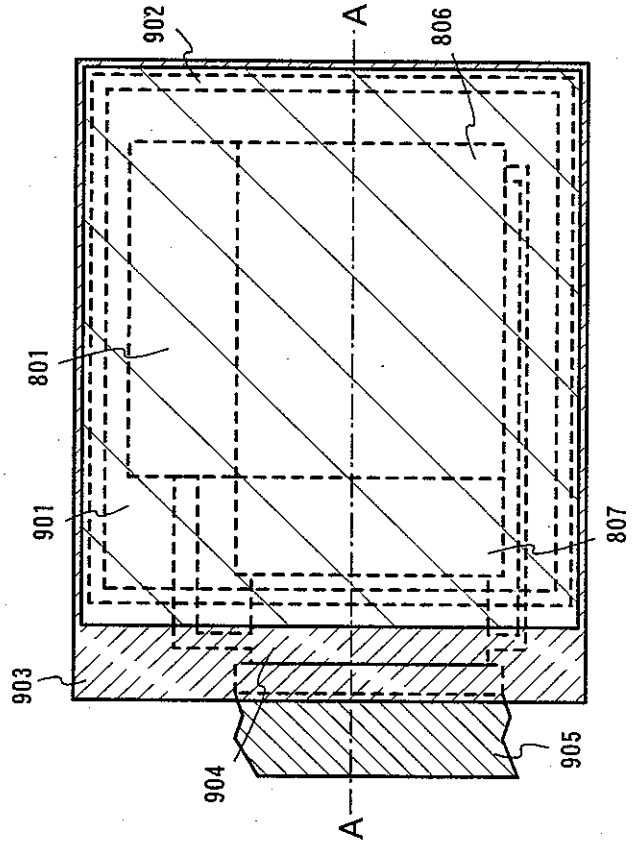


Fig.19A

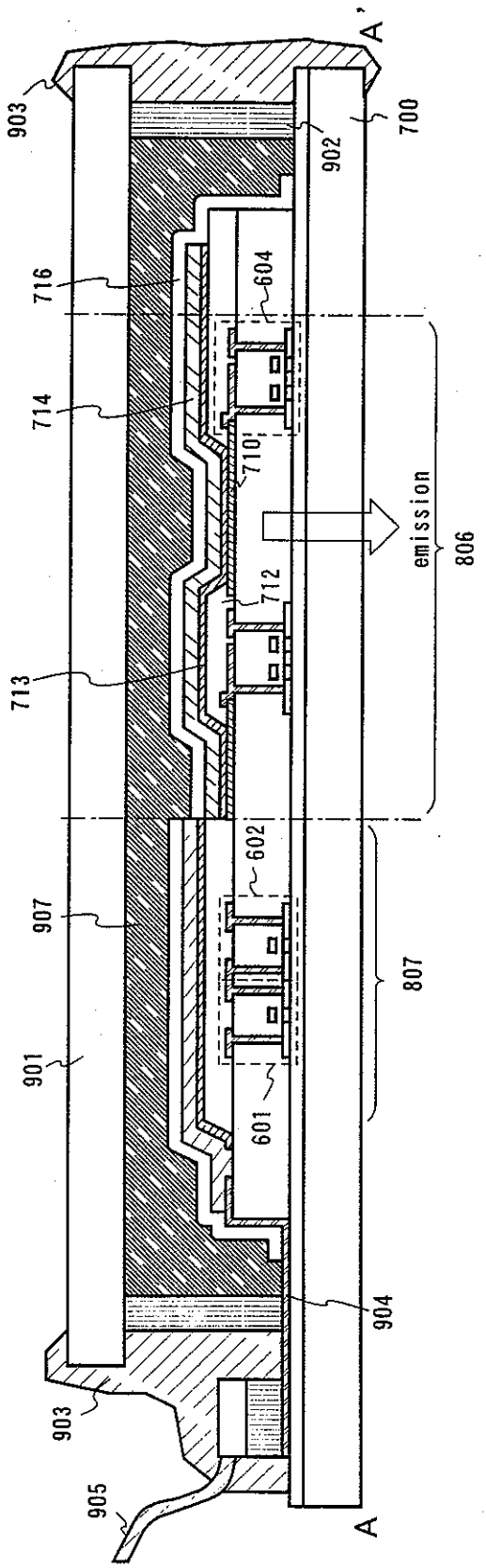


Fig.19B

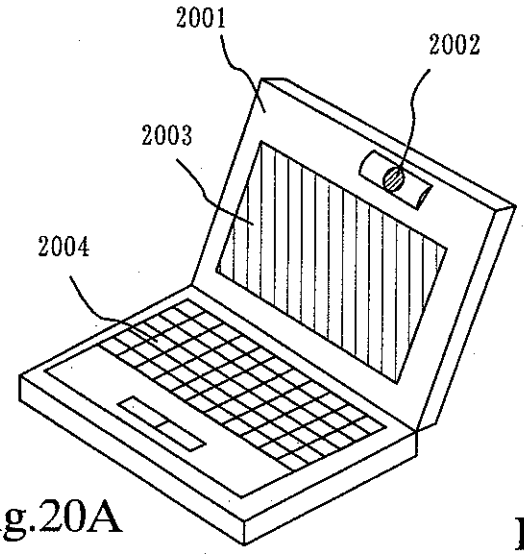


Fig.20A

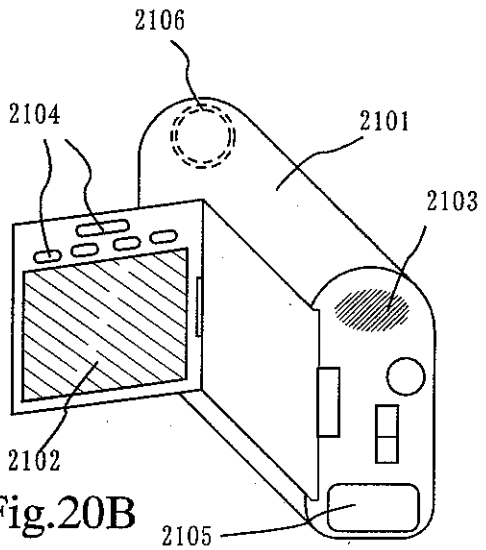


Fig.20B

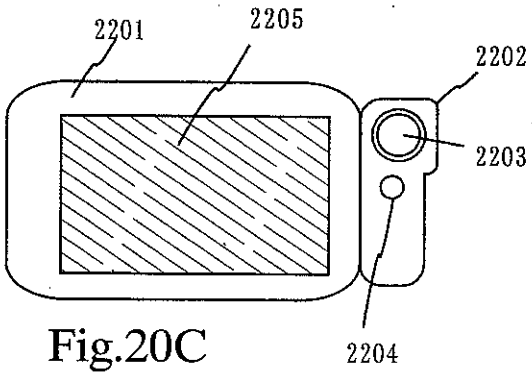


Fig.20C

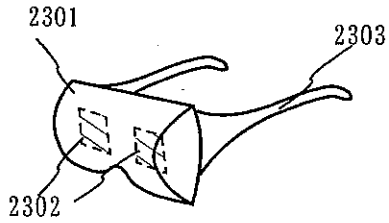


Fig.20D

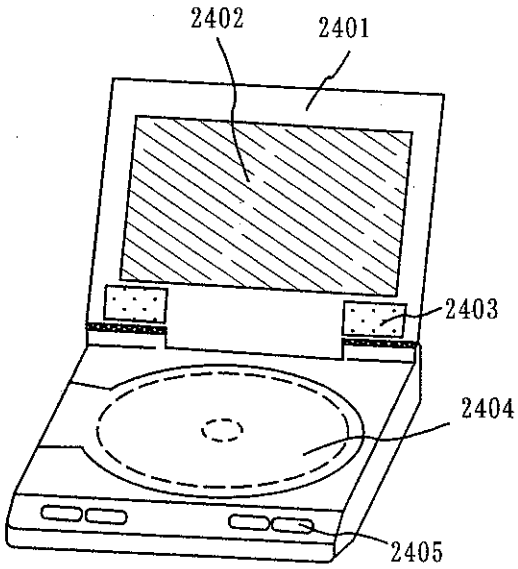


Fig.20E

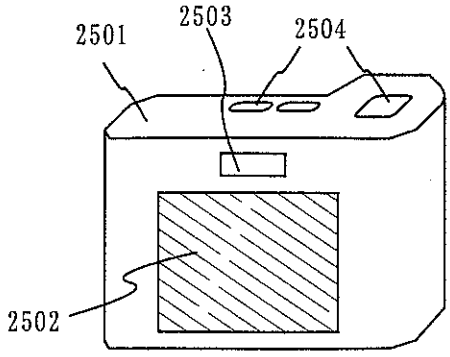


Fig.20F

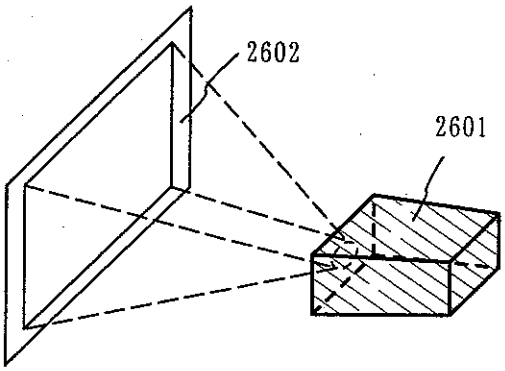


Fig.21A

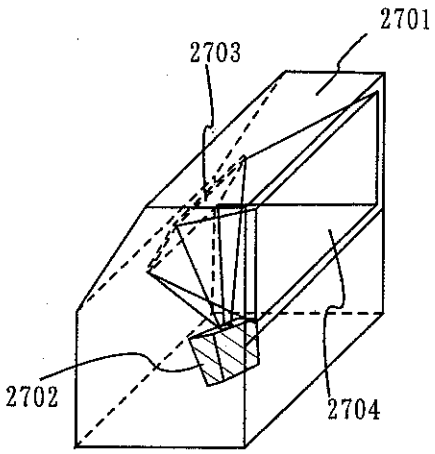


Fig.21B

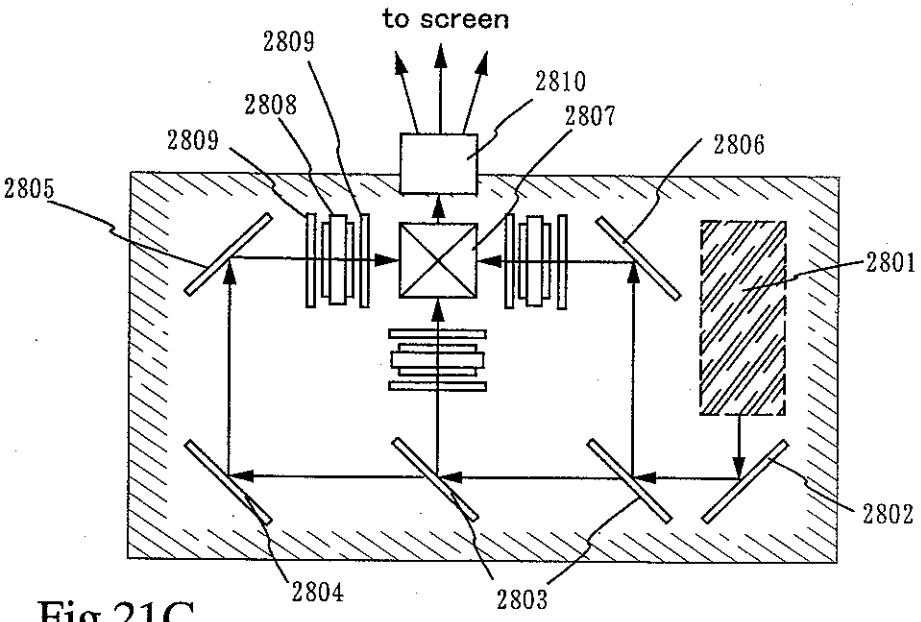


Fig.21C

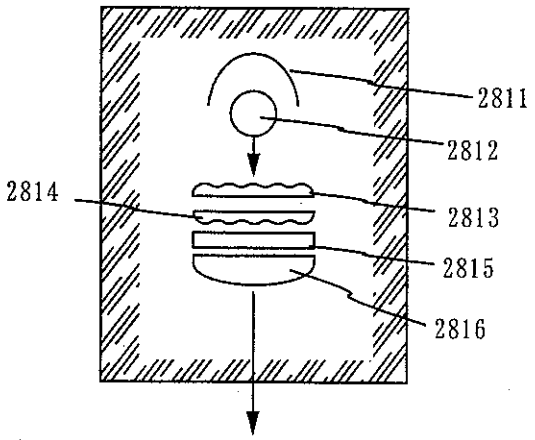


Fig.21D

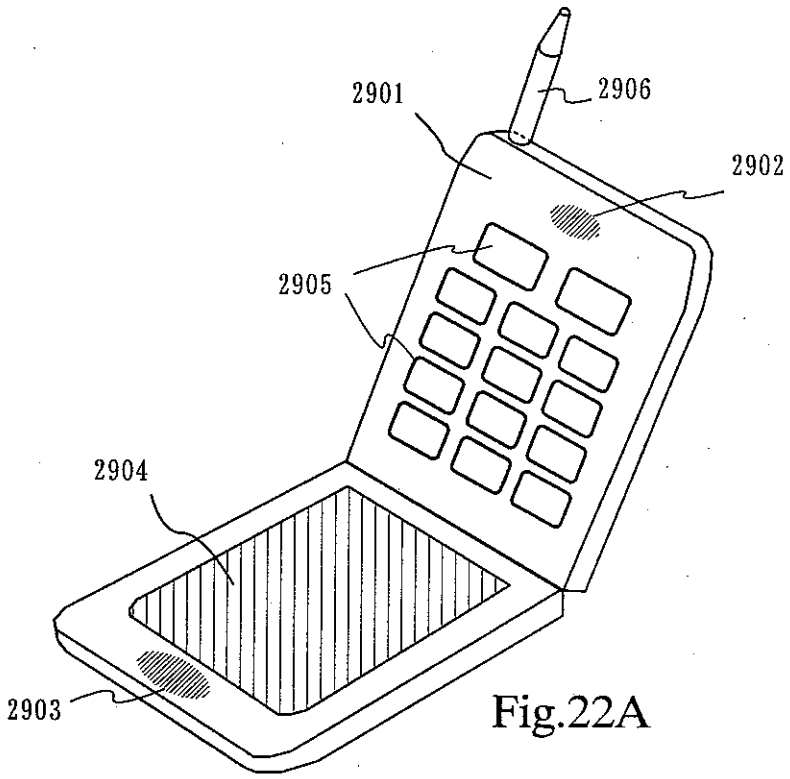


Fig.22A

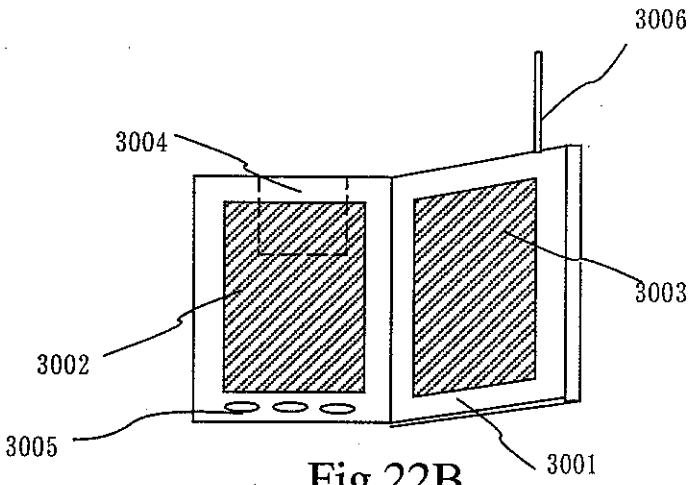


Fig.22B

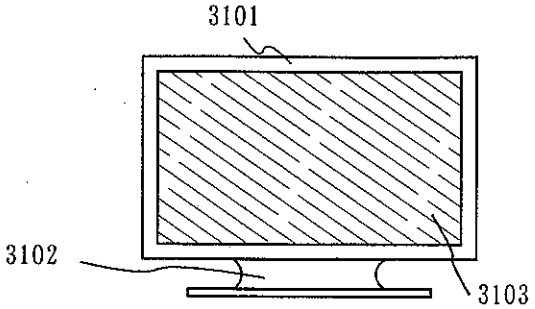


Fig.22C

31 AUG 2005

Fig.23A

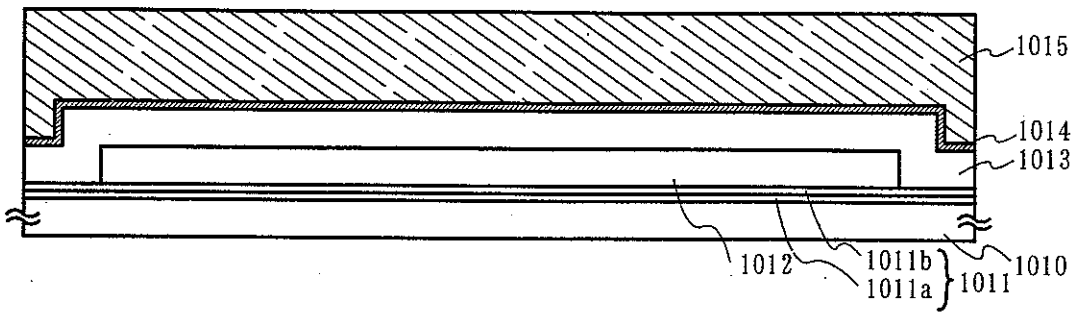


Fig.23B

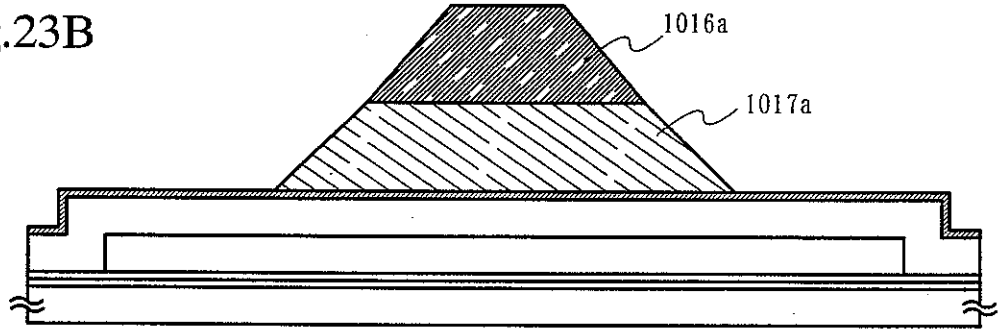


Fig.23C

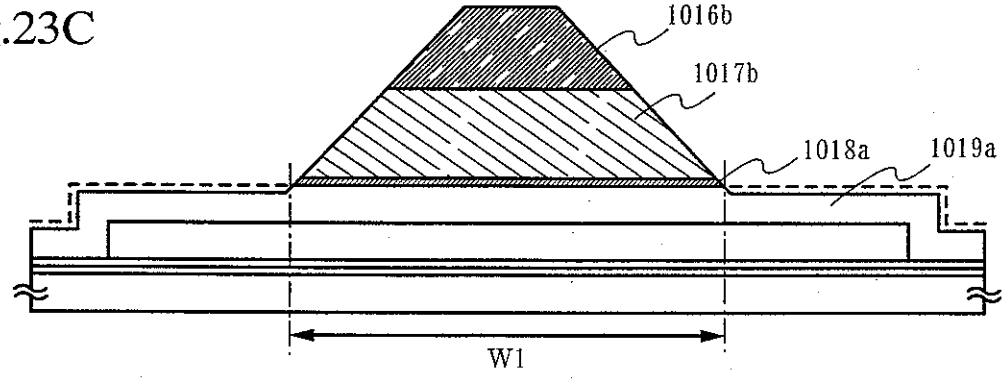
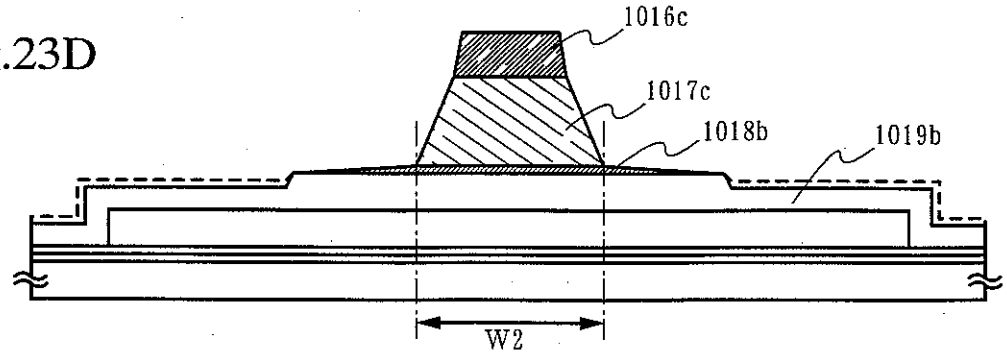


Fig.23D



31 AUG 2005

Fig.24A

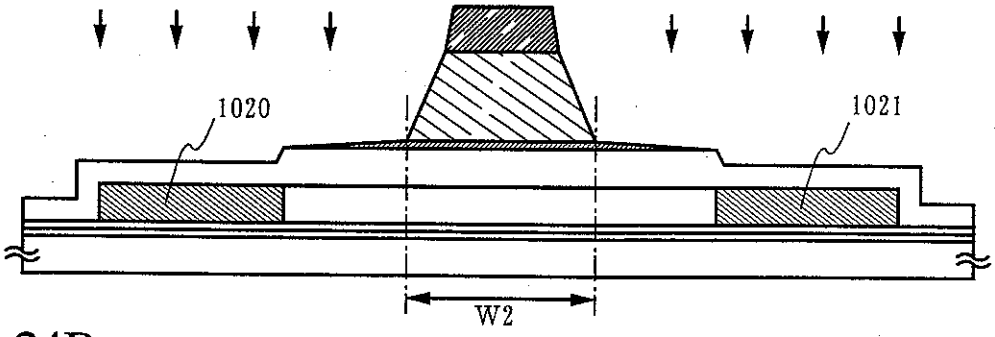


Fig.24B

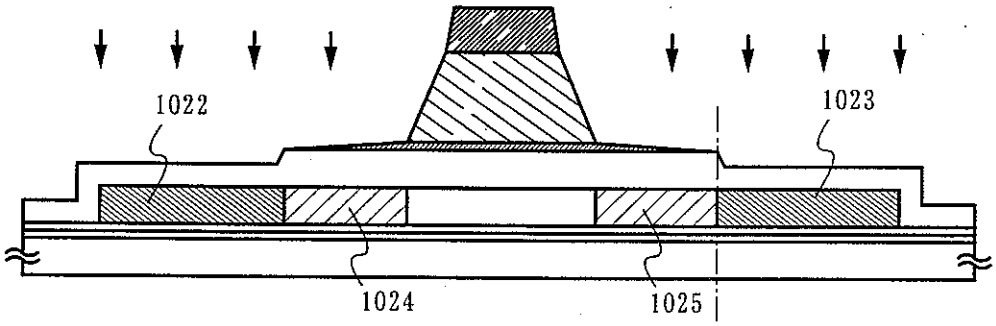


Fig.24C

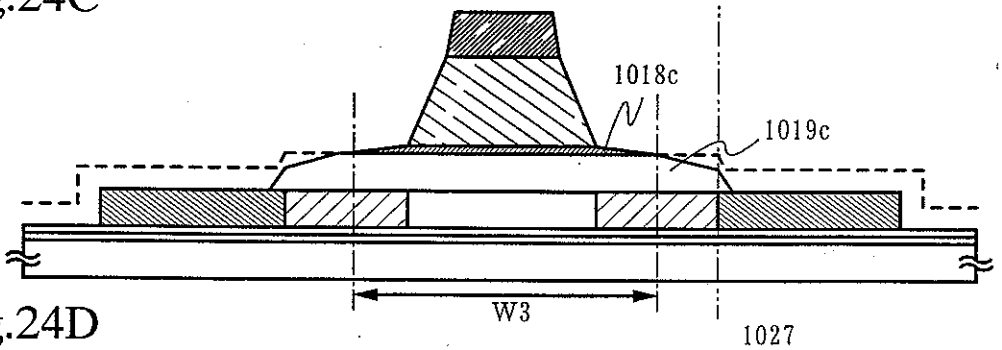
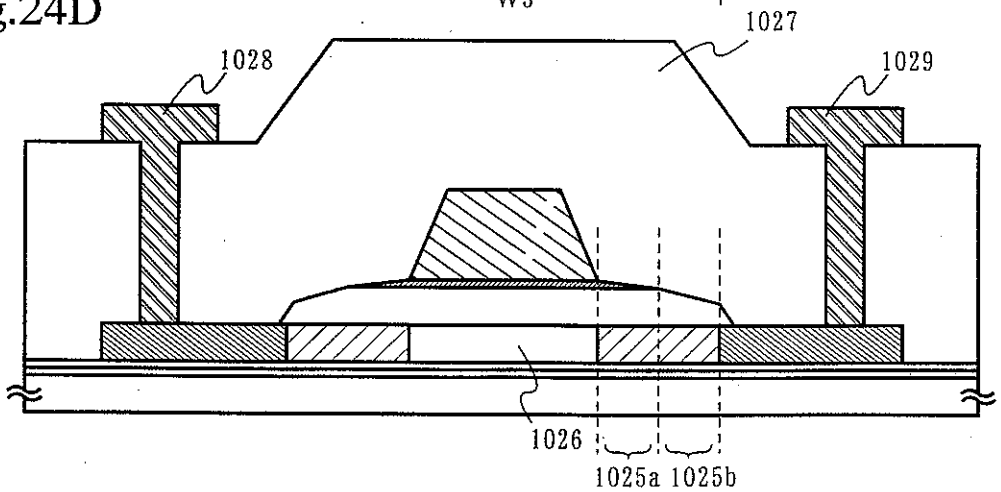


Fig.24D



31 AUG 2005

Fig.25A

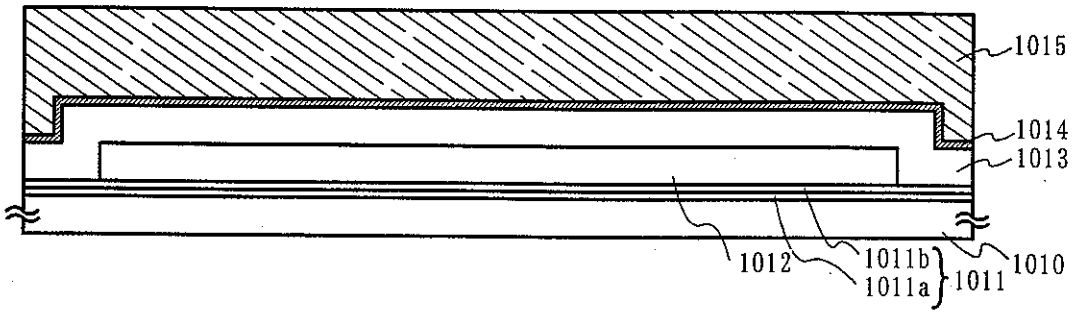


Fig.25B

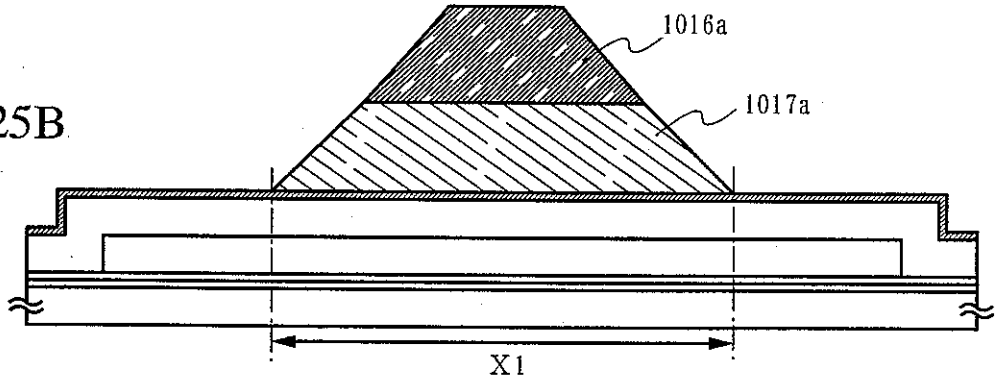


Fig.25C

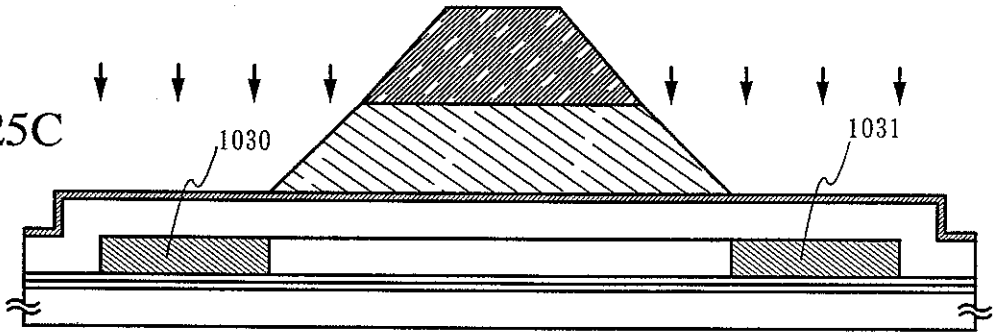


Fig.25D

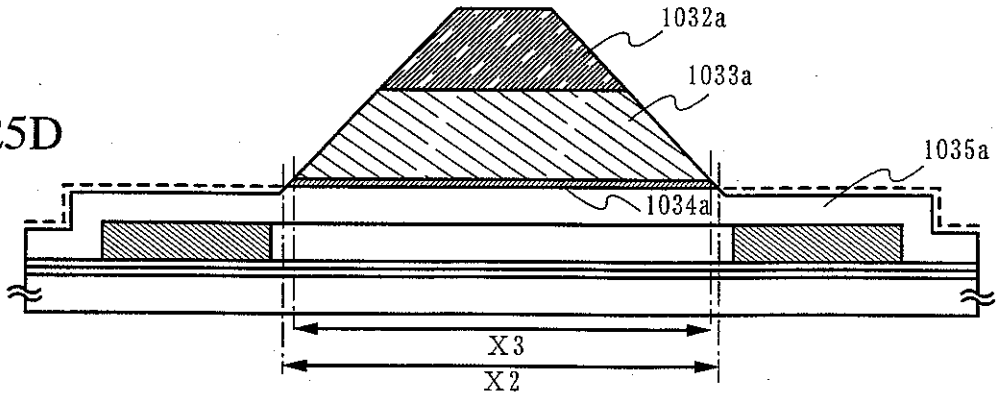


Fig.26A

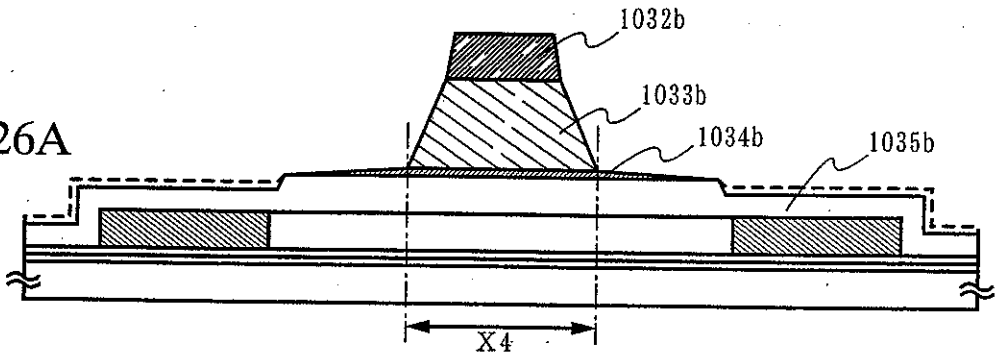


Fig.26B

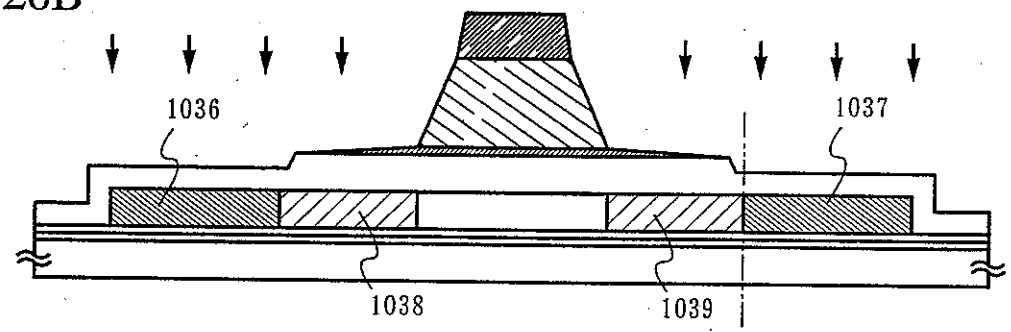


Fig.26C

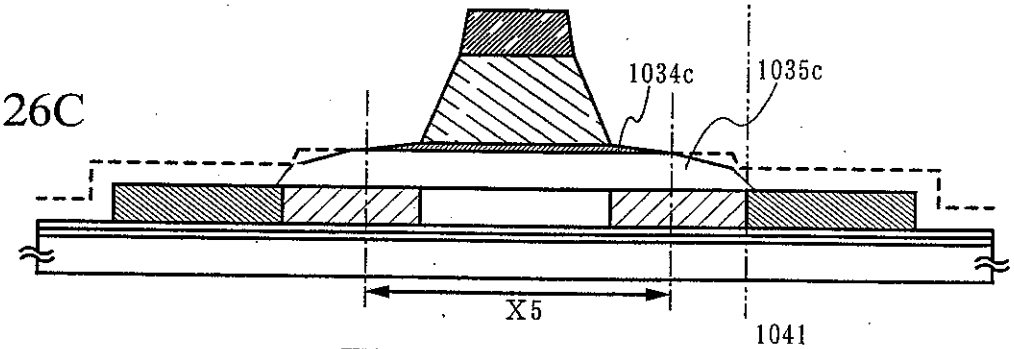
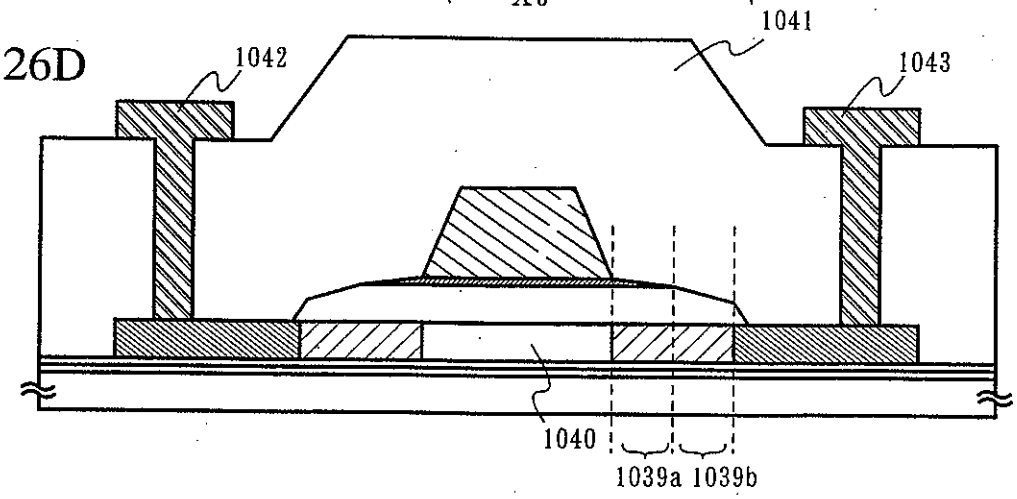


Fig.26D



31 AUG 2005

Fig.27A

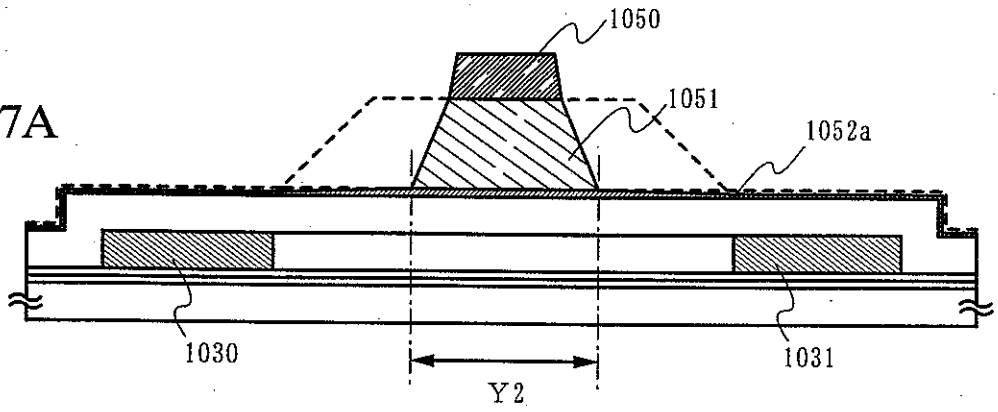


Fig.27B

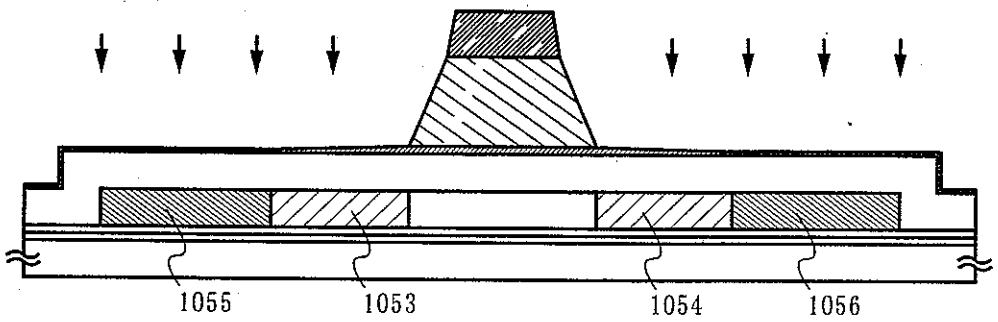


Fig.27C

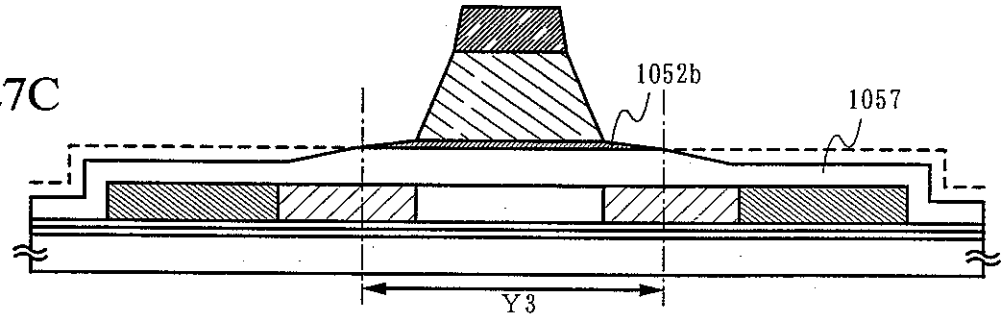
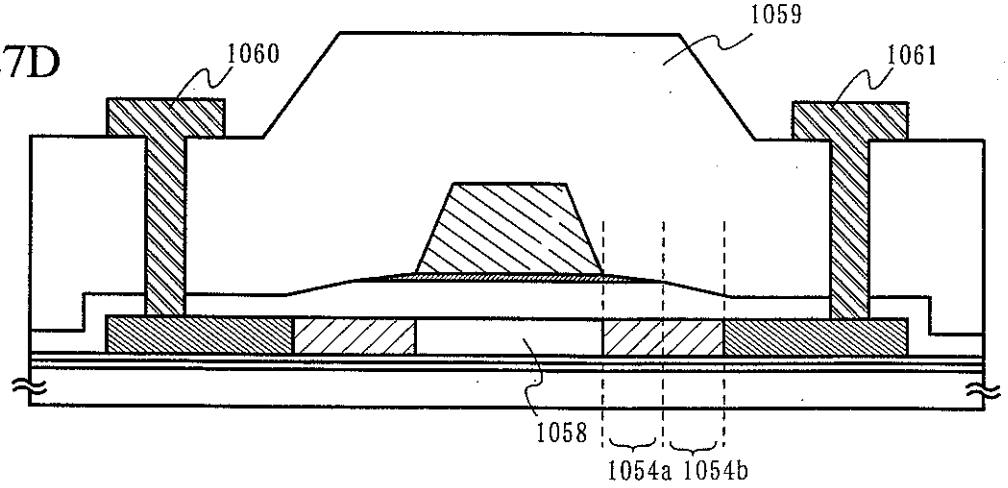


Fig.27D



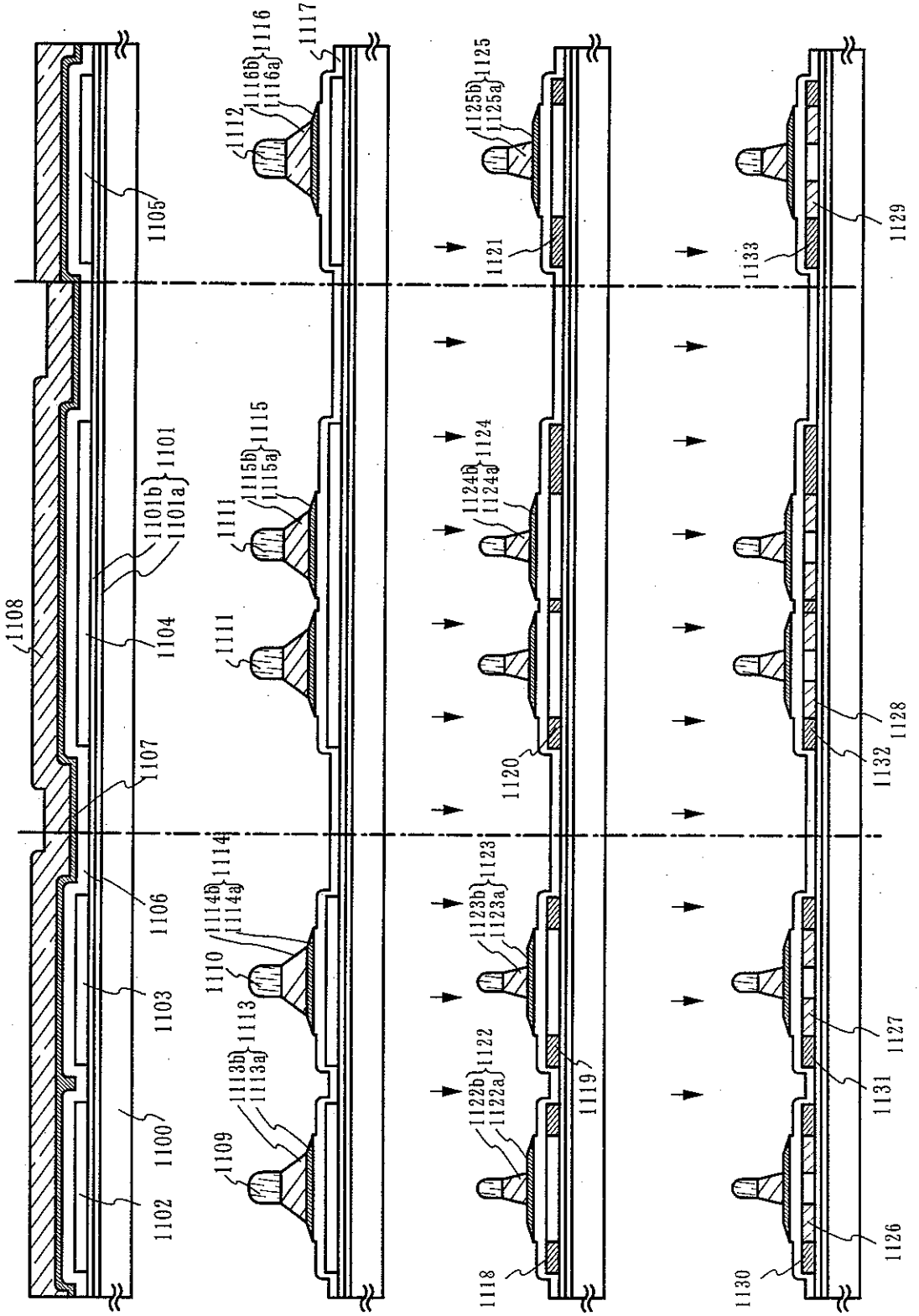


Fig.28A Fig.28B Fig.28C Fig.28D

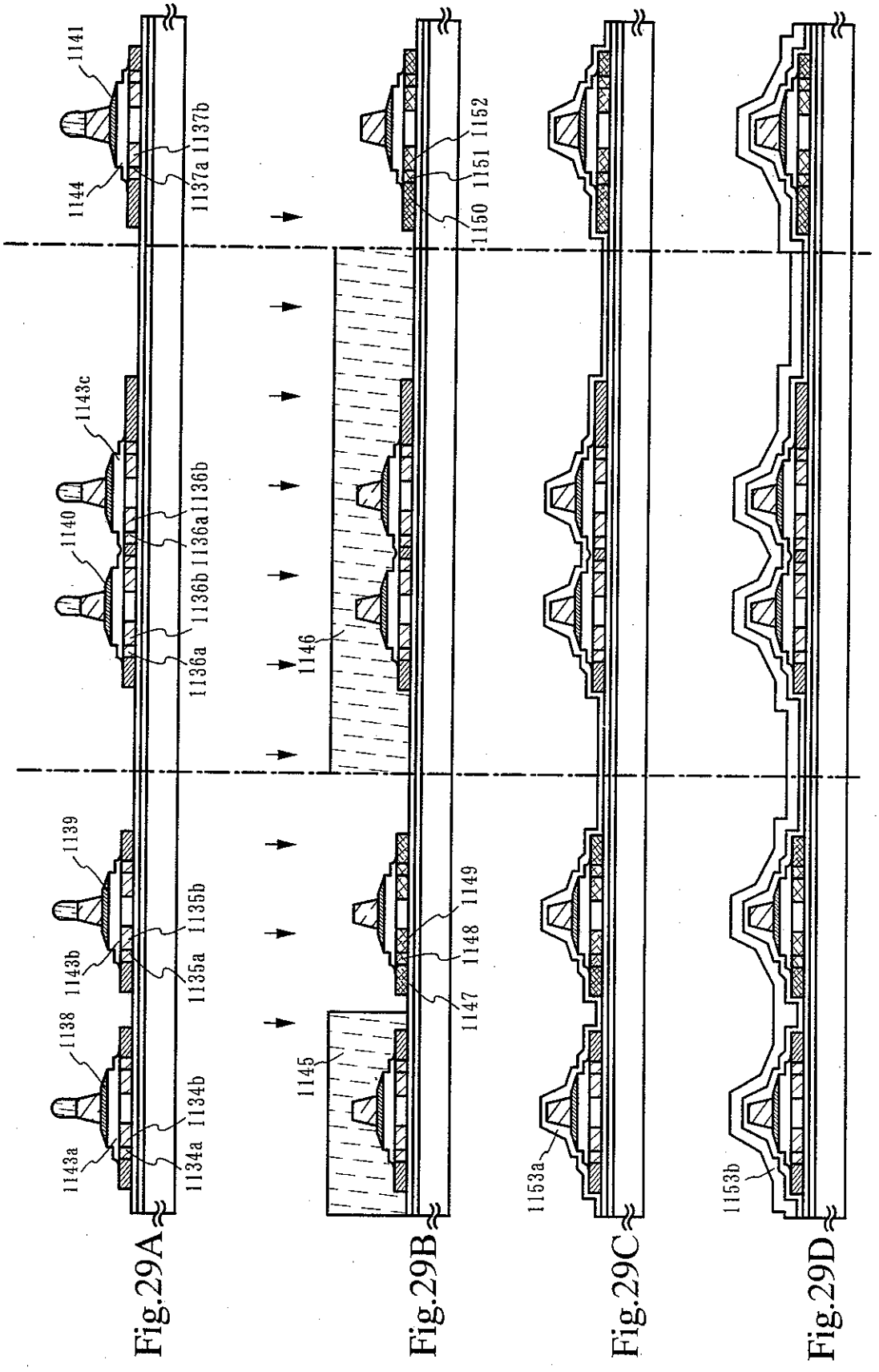
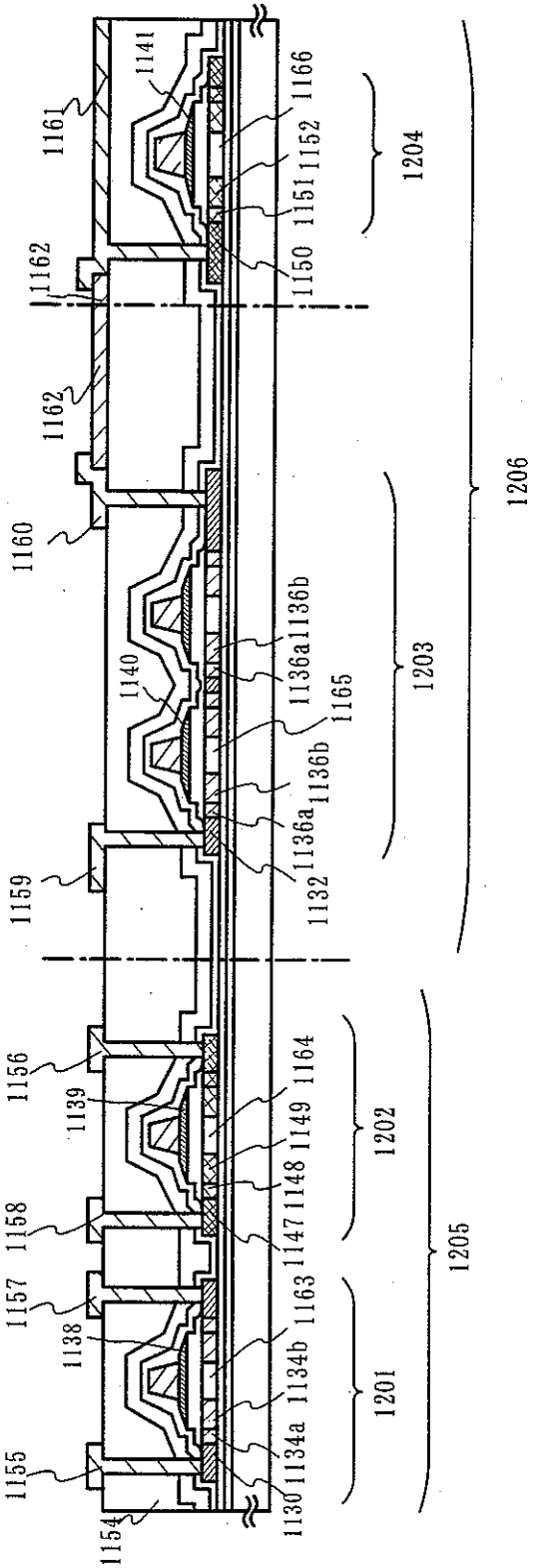


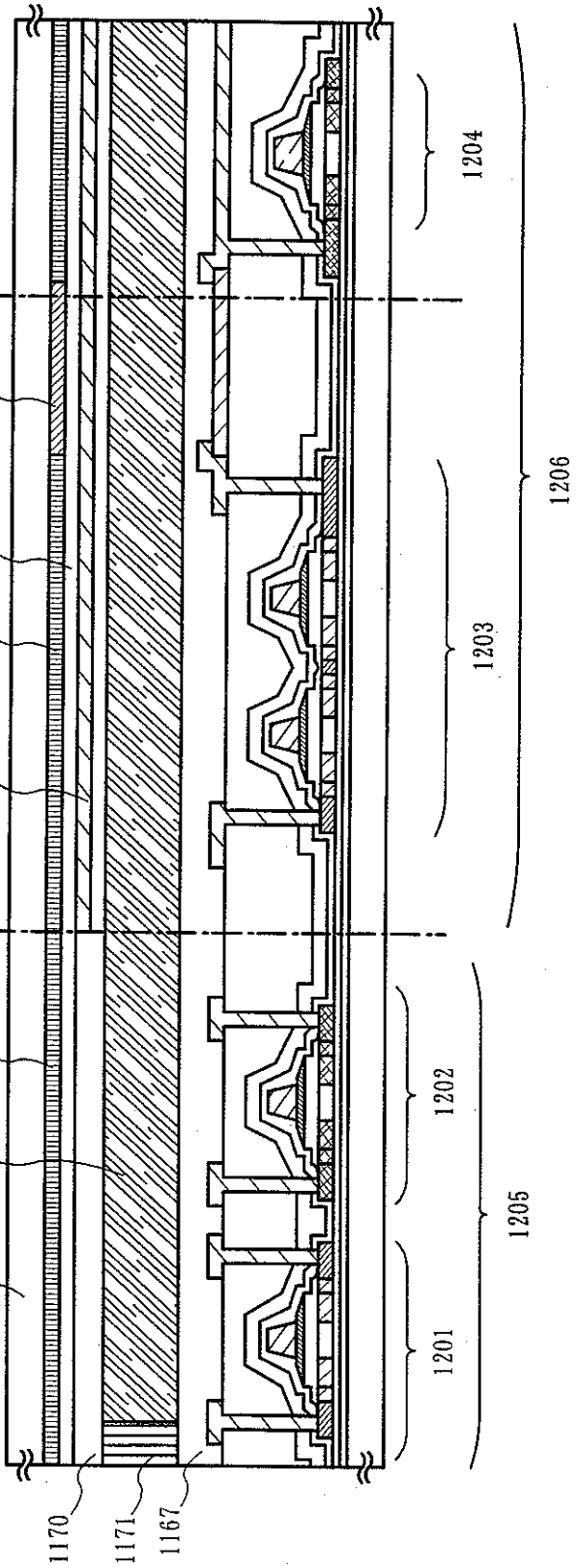
Fig.29A Fig.29B Fig.29C Fig.29D

Fig.30


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31 AUG 2005

Fig.31



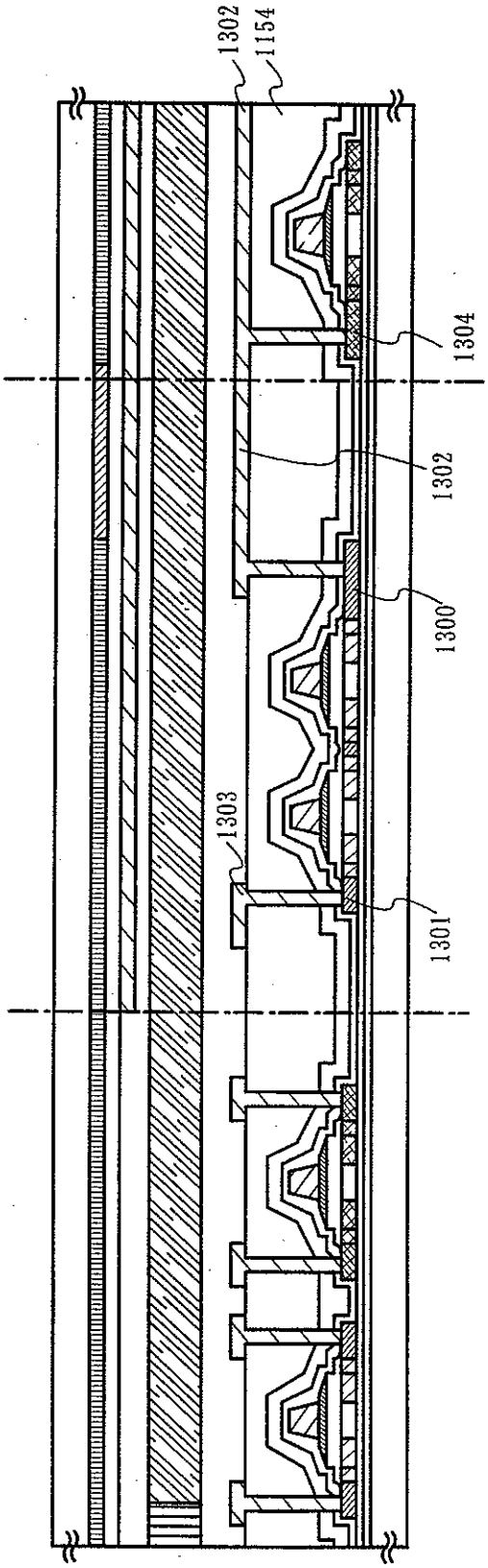


Fig.32

31 AUG 2005

Fig.33

