

METHOD AND SYSTEM FOR ADJUSTING MEMORY, AND SEMICONDUCTOR DEVICE

ABSTRACT

Embodiments of the disclosure, there is provided a method, a system for adjusting the memory, and a semiconductor device. The method for adjusting the memory includes: acquiring a mapping relationship between a temperature of a transistor, an equivalent width-length ratio of a sense amplifier transistor in a sense amplifier and an actual time at which the data is written into the memory; acquiring a current temperature of the transistor; and adjusting the equivalent width-length ratio, based on the current temperature and the mapping relationship, so that the actual time at which the data is written into the memory corresponding to the adjusted equivalent width-length ratio is within a preset writing time. Since the temperature may affect the actual time at which the data is written into the memory, the mapping relationship between the temperature of the transistor, the equivalent width-length ratio and the actual time at which the data is written into the memory is acquired firstly. And then, the actual time at which the data is written into the memory is acquired according to the current temperature of the transistor, and the actual time at which the data is written into the memory of the transistor at the current temperature is adjusted by adjusting the equivalent width-length ratio of the sense amplifier transistor in the sense amplifier.

[Fig. 3]