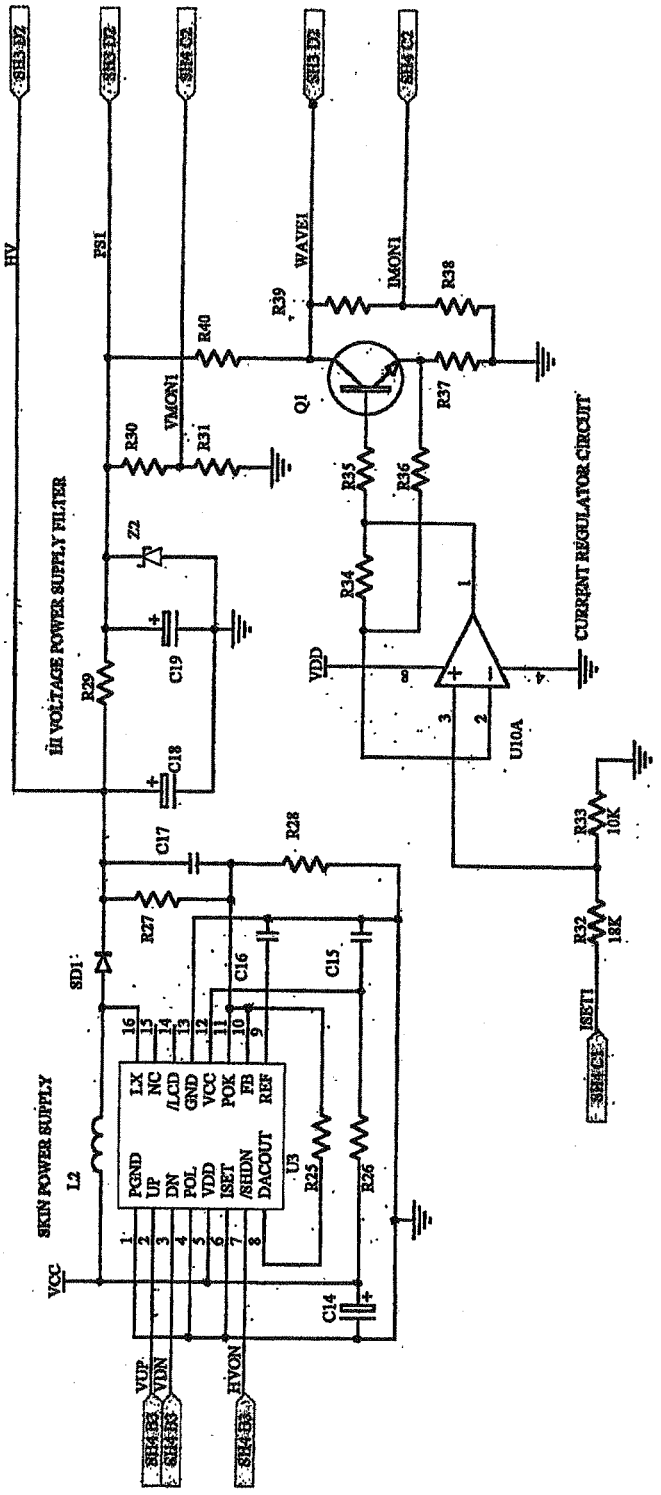

HV
FB1
WAVE1
IMON1
VMON1
Q1
R39
R38
R40
R37
R30
R31
R35
R36
R34
Z2
C19
R29
C18
C17
R28
R27
SD1
C16
C15
L2
U3
R25
R26
C14
VCC
VUP
VDN
HVON
VDD
U10A
R33
10K
R32
18K
ISET1
CURRENT REGULATOR CIRCUIT
HI VOLTAGE POWER SUPPLY FILTER
SKIN POWER SUPPLY
LX
NC
/LCD
GND
VCC
POK
FB
REF
PGND
UP
DN
POL
VDD
ISET
/SHDN
DACOUT

Figure 1

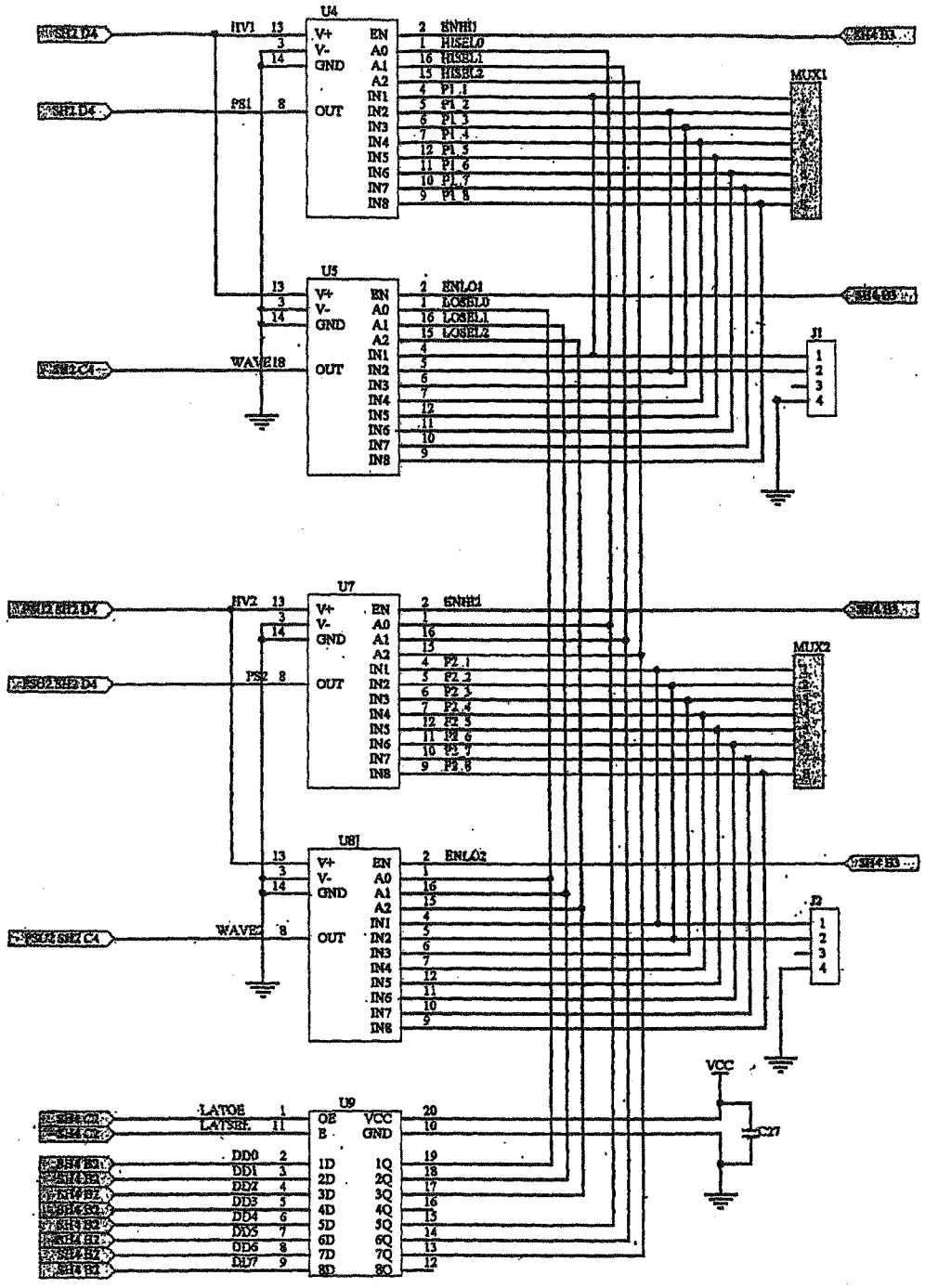

U4
U5
U7
U8
U9
MUX1
MUX2
J1
J2
HV1
HV2
PS1
PS2
WAVE1
WAVE2
VCC
C27
LATOE
LATSEL
DD0
DD1
DD2
DD3
DD4
DD5
DD6
DD7

Figure 2

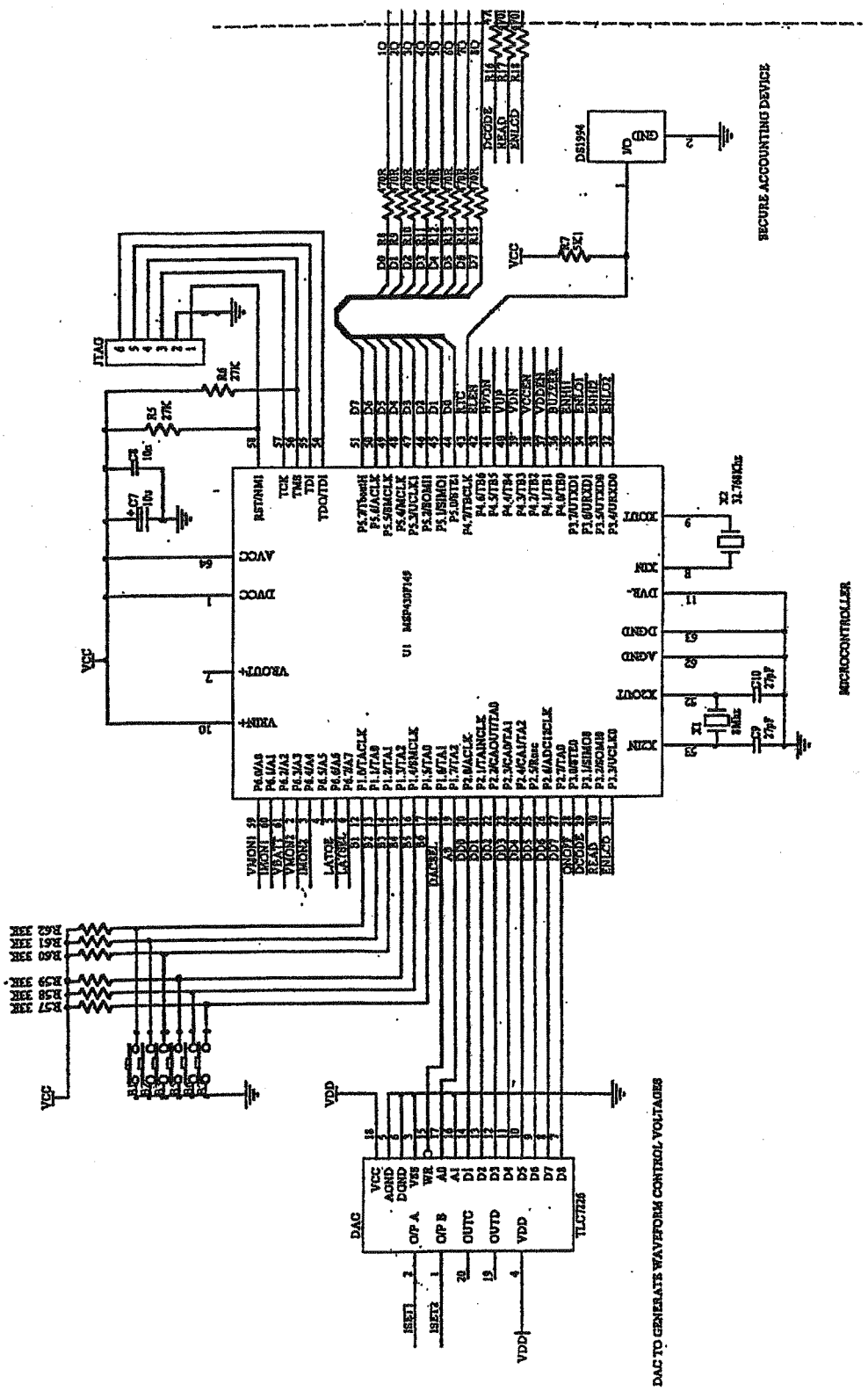

SECURE ACCOUNTING DEVICE
MICROCONTROLLER
DAC TO GENERATE WAVEFORM CONTROL VOLTAGES
DS1994
MSP430F149
TLC7226
DAC
JTAG
VCC
VDD

Figure 3

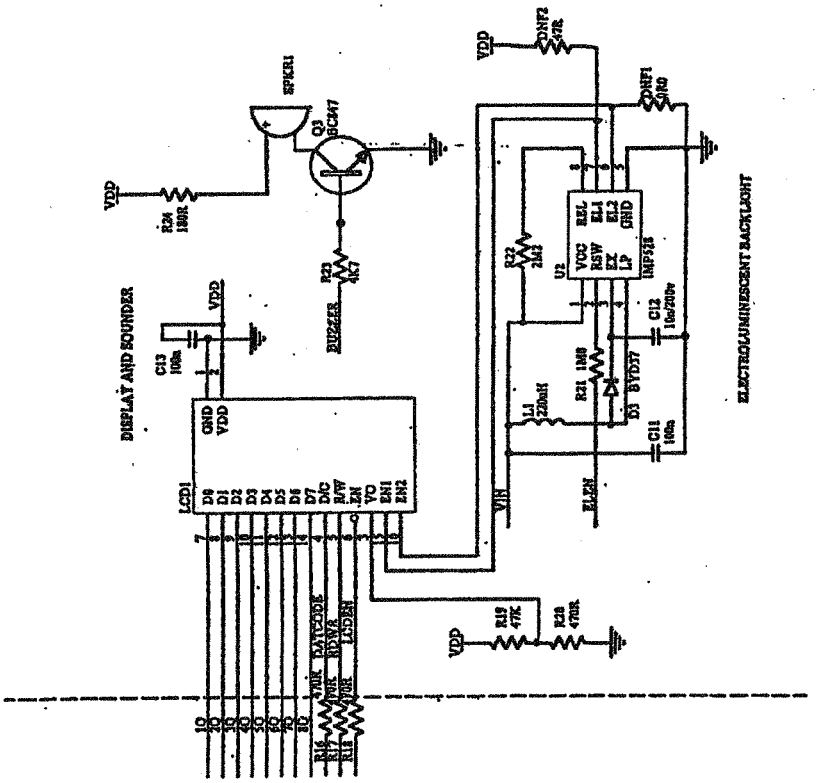

DISPLAY AND SOUNDER
ELECTROLUMINESCENT BACKLIGHT
VDD
SPKR1
Q3
BC847
R24
180R
R23
4K7
BUZZER
VDD2
C13
100n
LCD1
GND
VDD
D0
D1
D2
D3
D4
D5
D6
D7
D/C
R/W
EN
VO
EN1
EN2
R14
R15
R16
R17
R18
10K
LATCODE
RDWR
LCDEN
VDD
ONF2
47K
ONF1
0R0
U2
EL1
EL1
EL2
GND
VCC
R/W
E2
LP
IMP58
R22
2K2
C12
10n/50v
R31
180
D3
BYD37
L1
220mH
C11
100n
VIH
EL2N
R19
47K
R20
47K

Figure 4

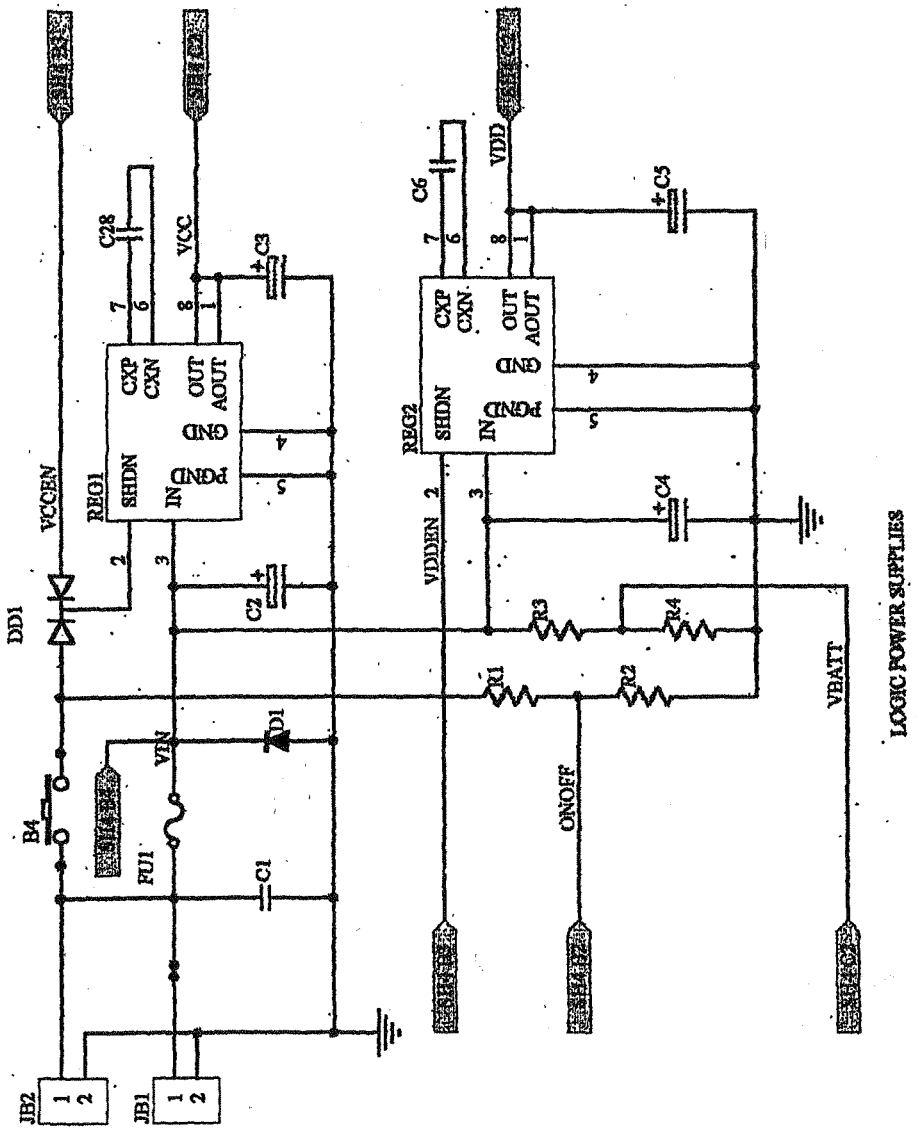

JB2
JB1
B4
FU1
VIN
VCCEN
DD1
D1
C1
C2
C28
C3
C4
C5
C6
R1
R2
R3
R4
REG1
REG2
SHDN
IN
PGND
GND
OUT
AOUT
CXP
CXN
VCC
VDD
VIDDEN
ONOFF
VBATT
LOGIC POWER SUPPLIES

Figure 5

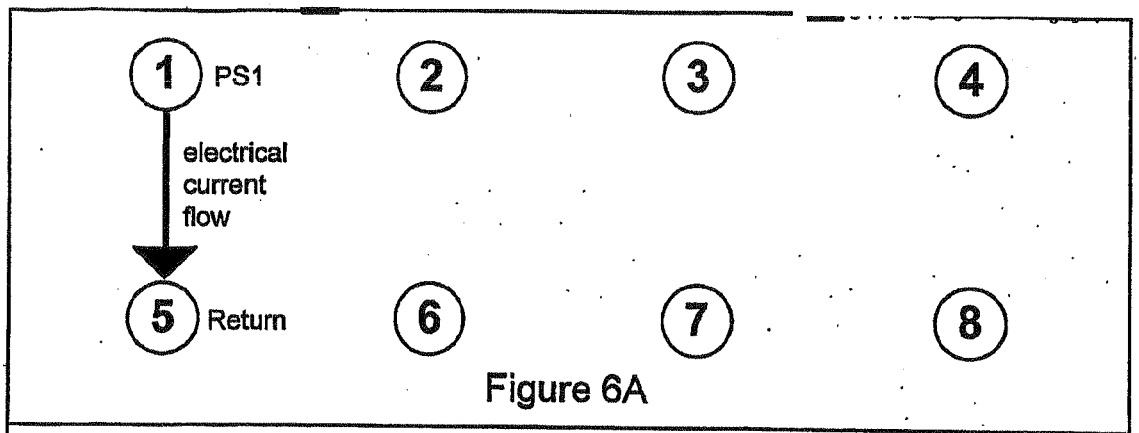


Figure 6A

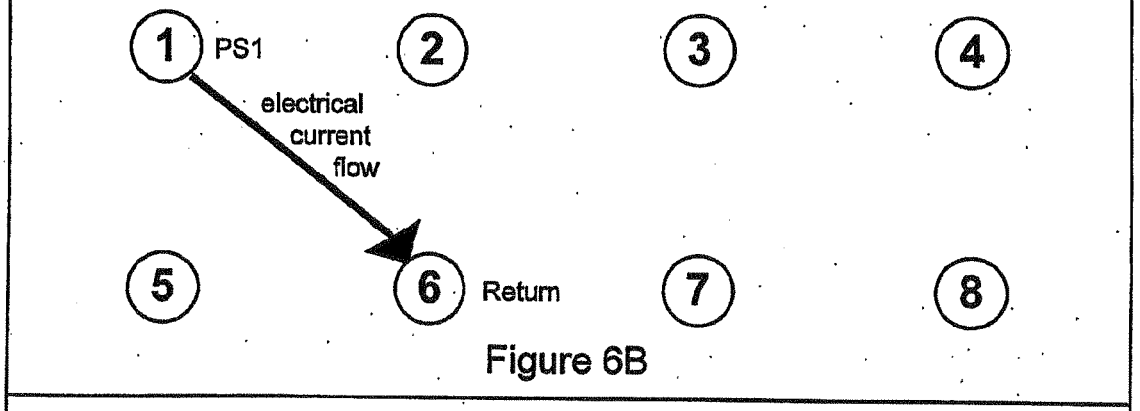


Figure 6B

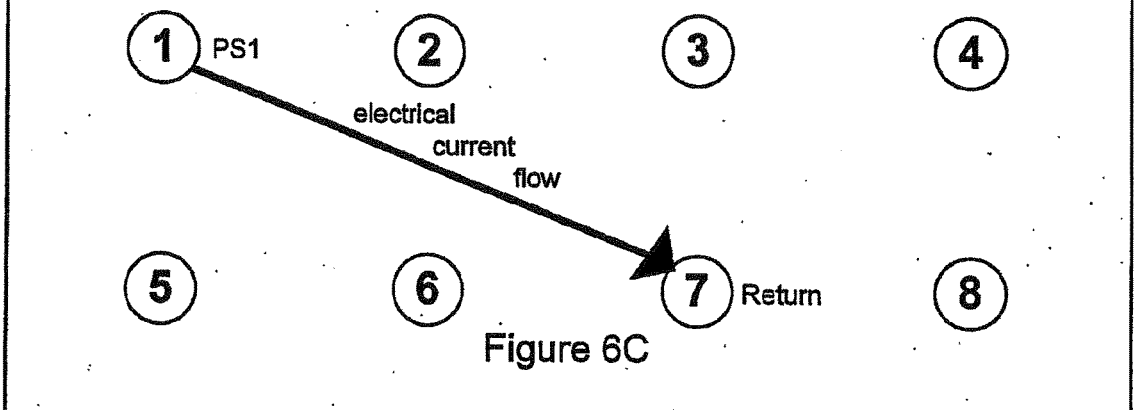


Figure 6C

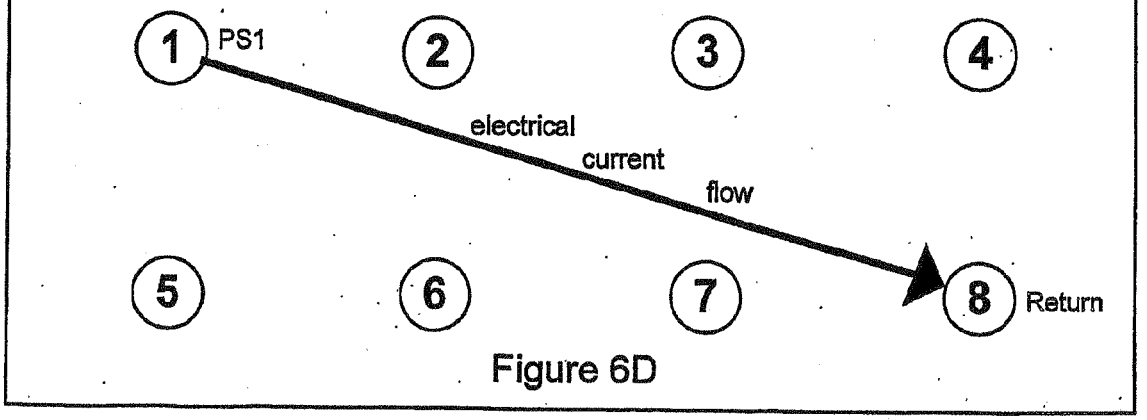


Figure 6D

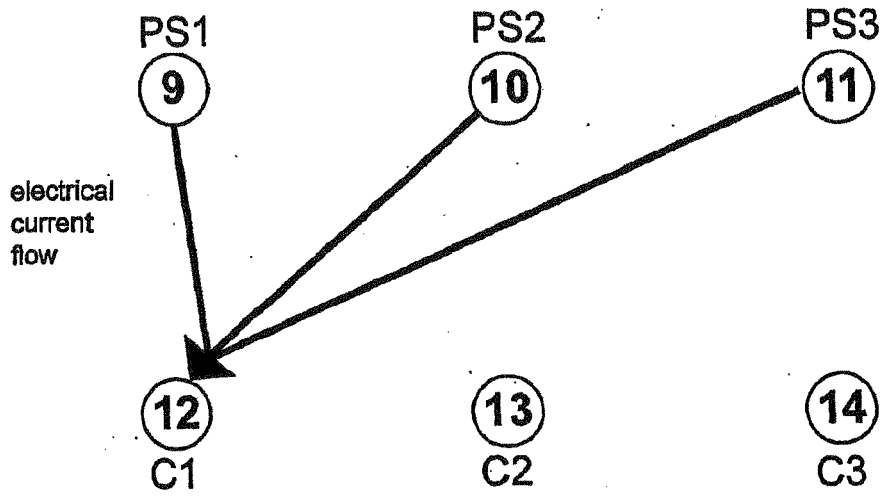


Figure 7A

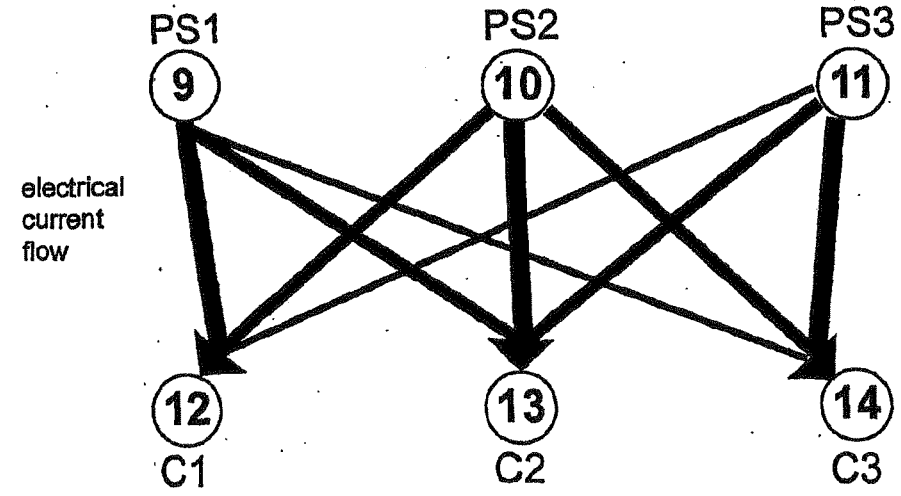


Figure 7B

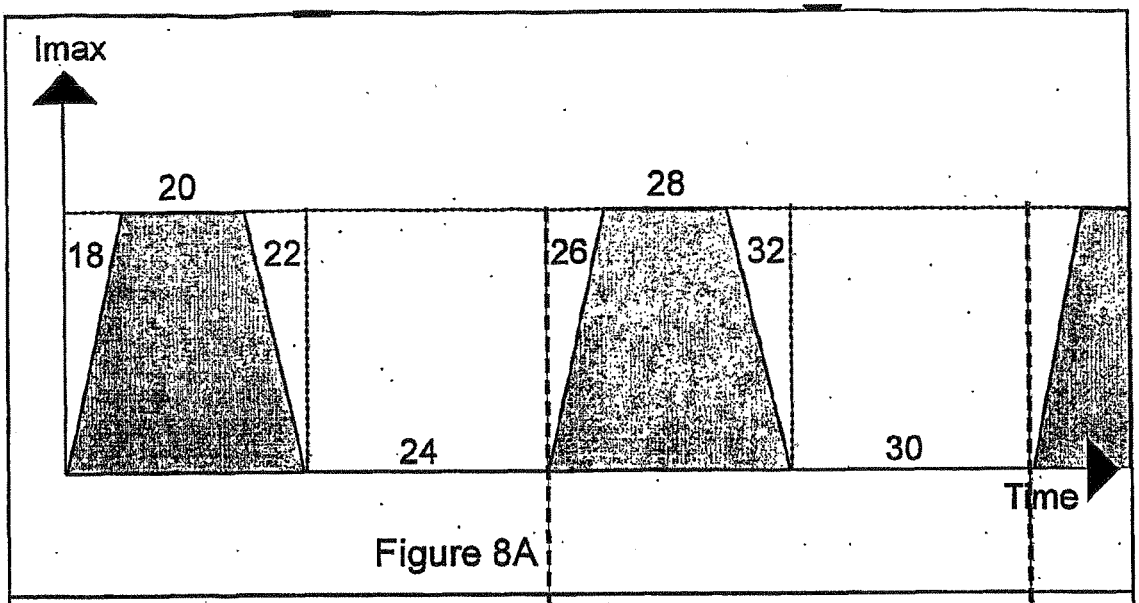


Figure 8A

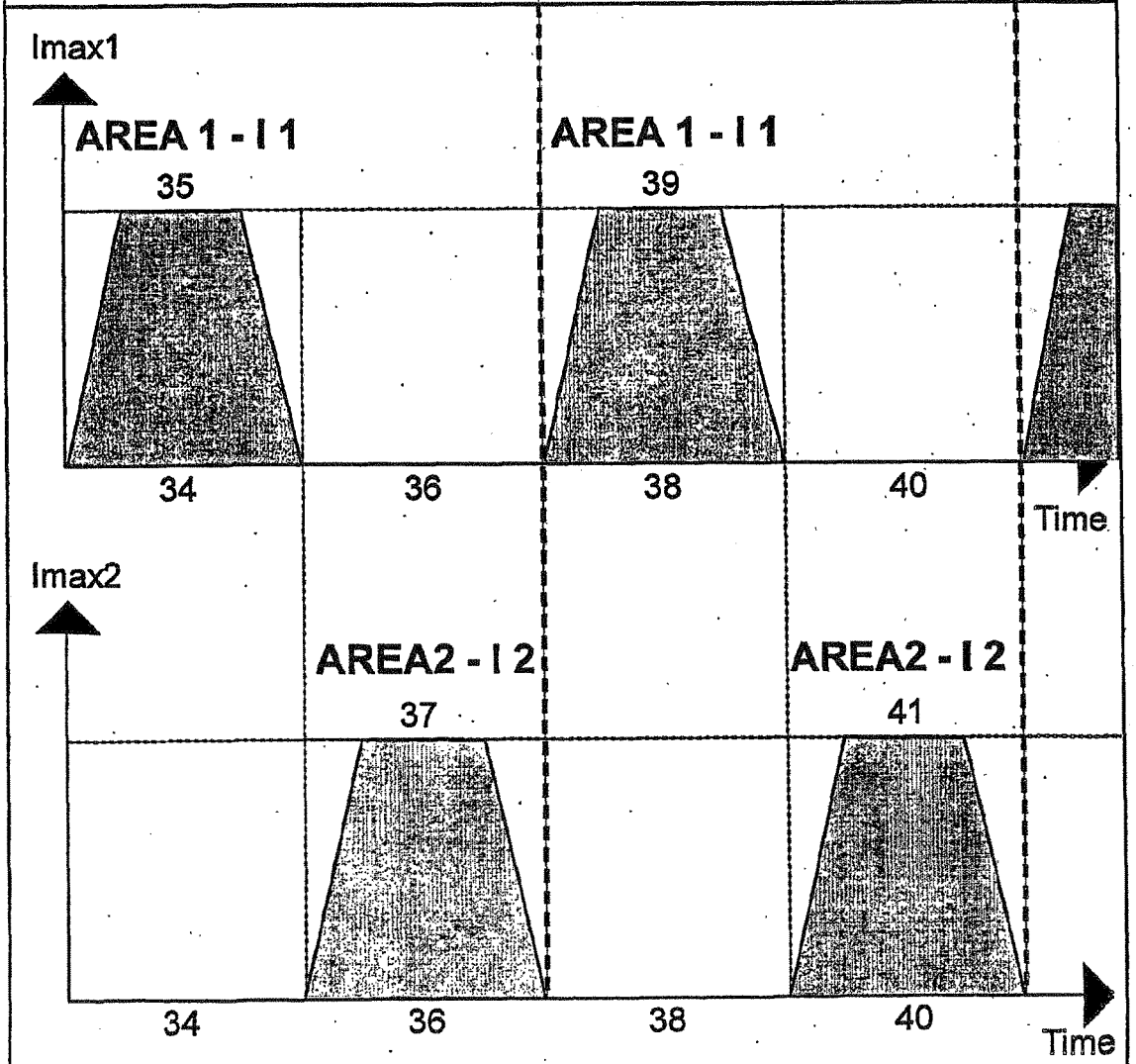


Figure 8B

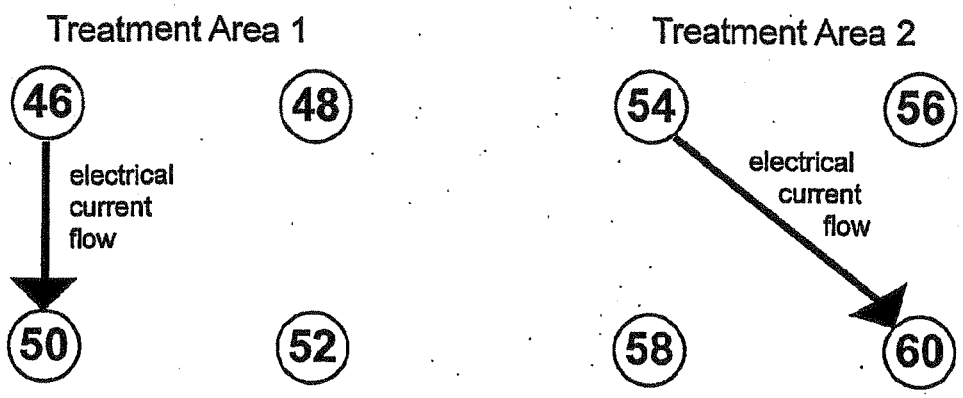


Figure 9A

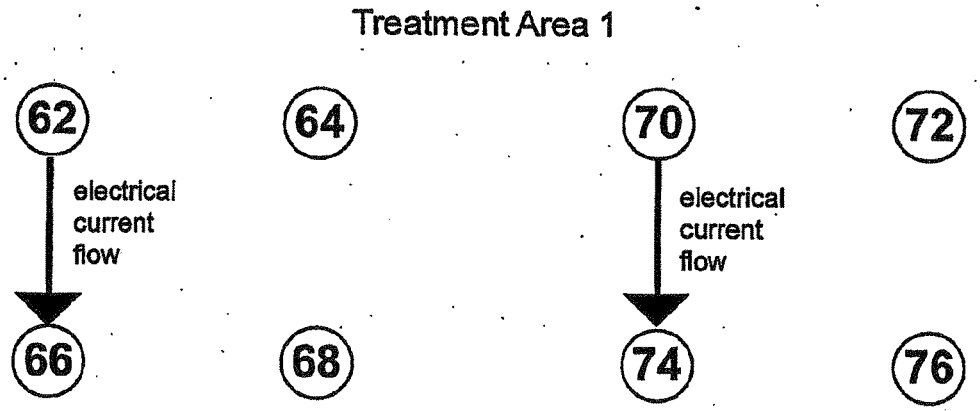


Figure 9B