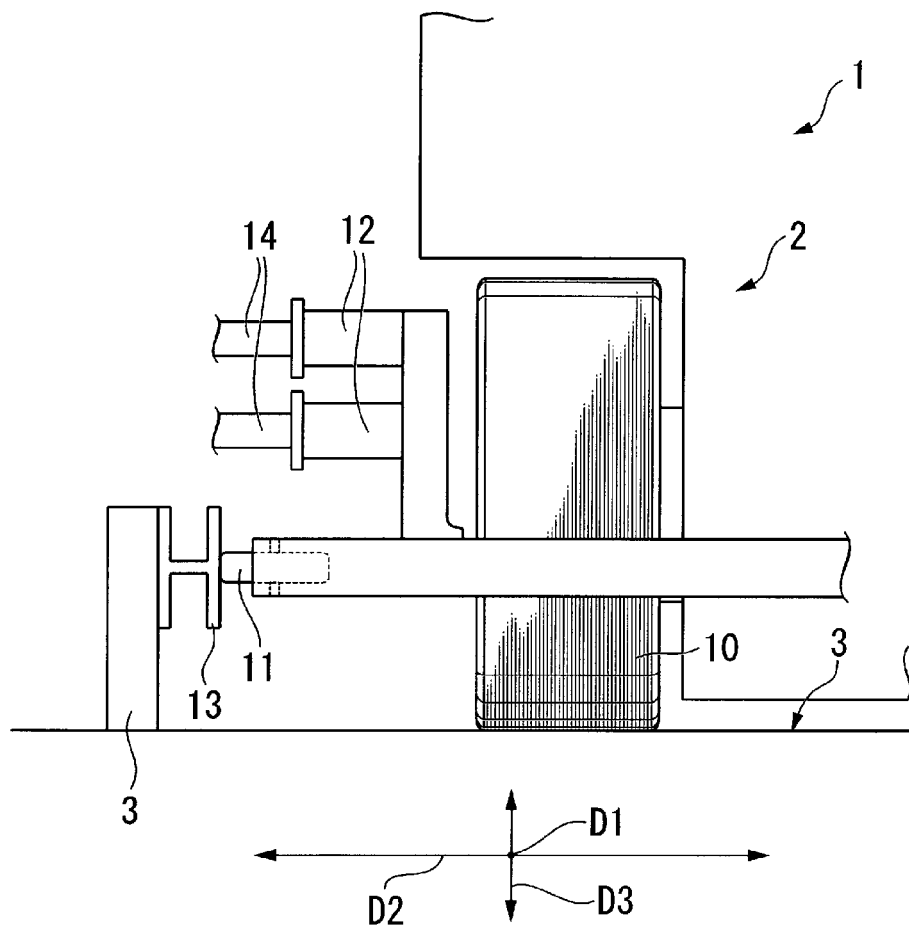
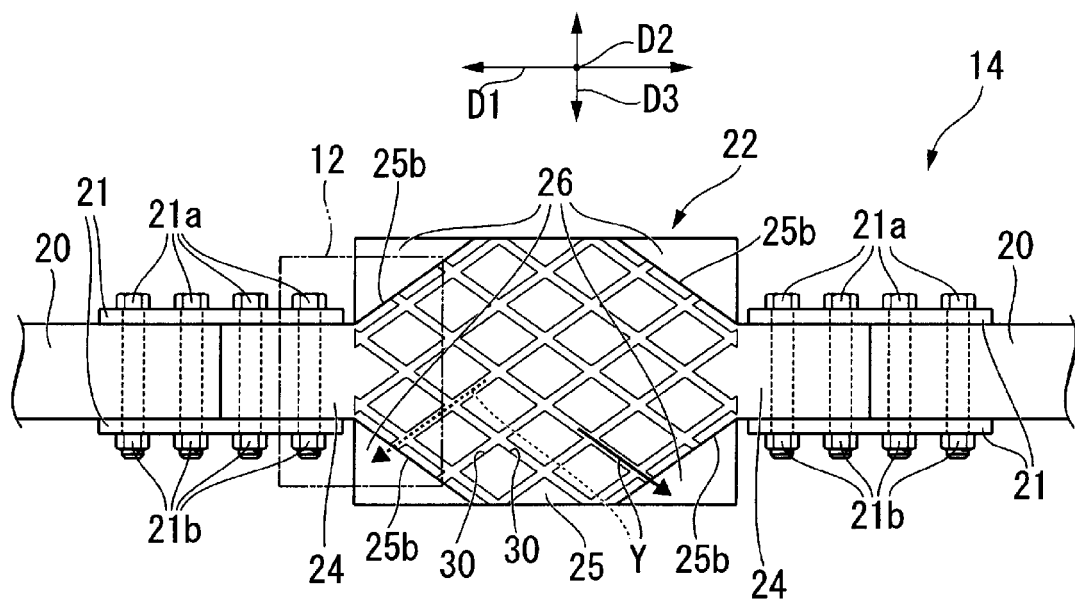


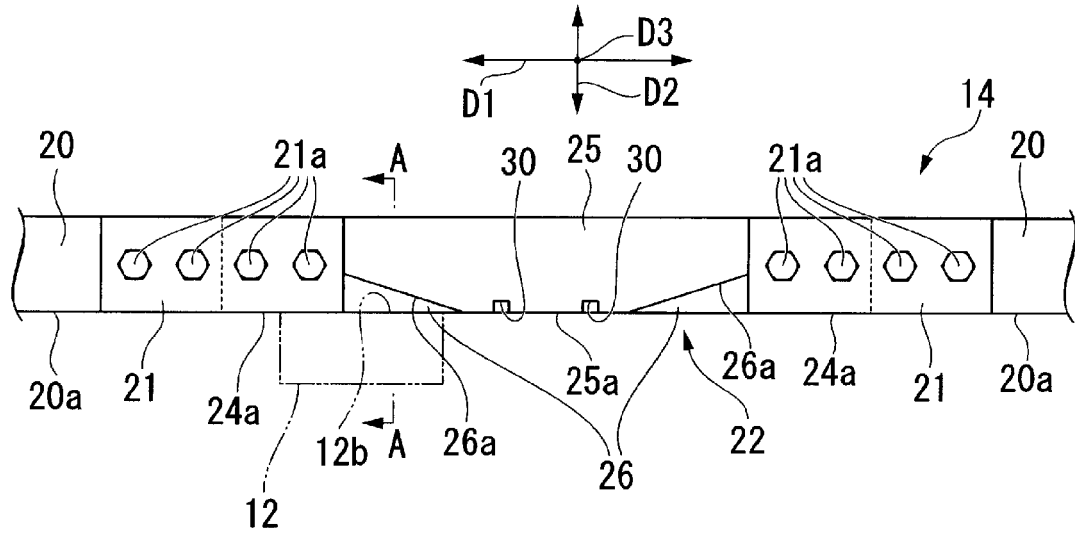
[図1]



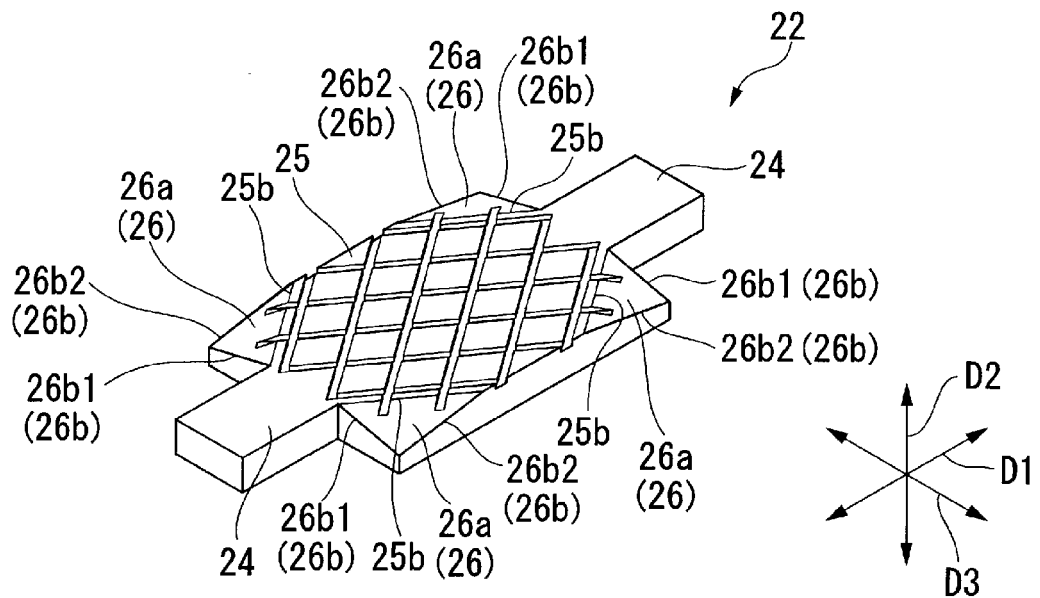
[図2A]



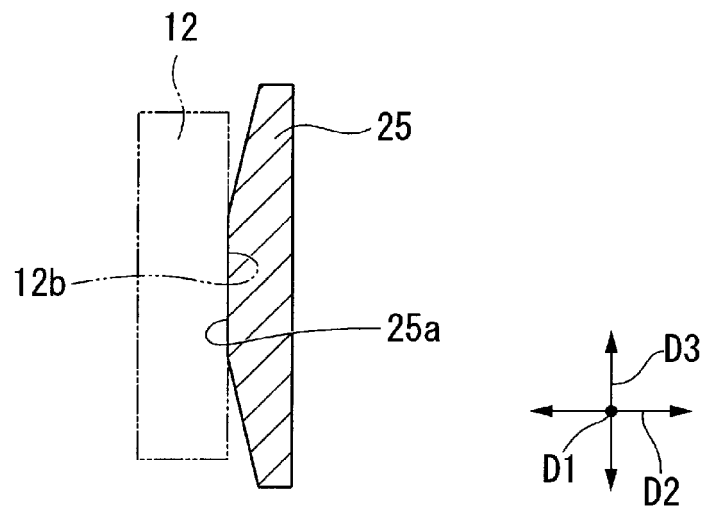
[図2B]



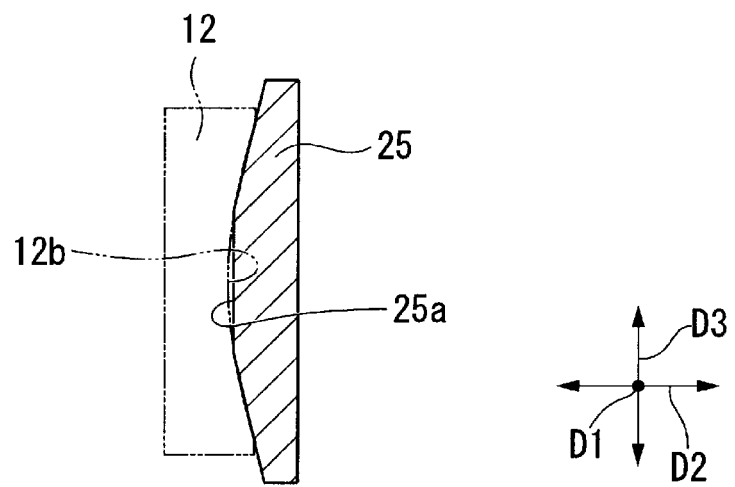
[図2C]



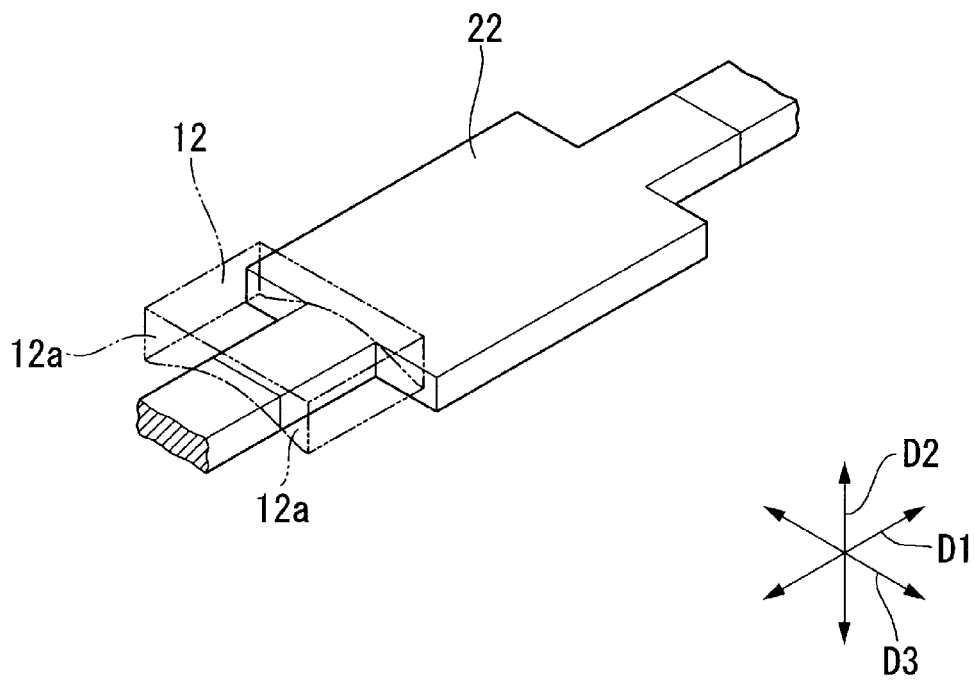
[図3A]



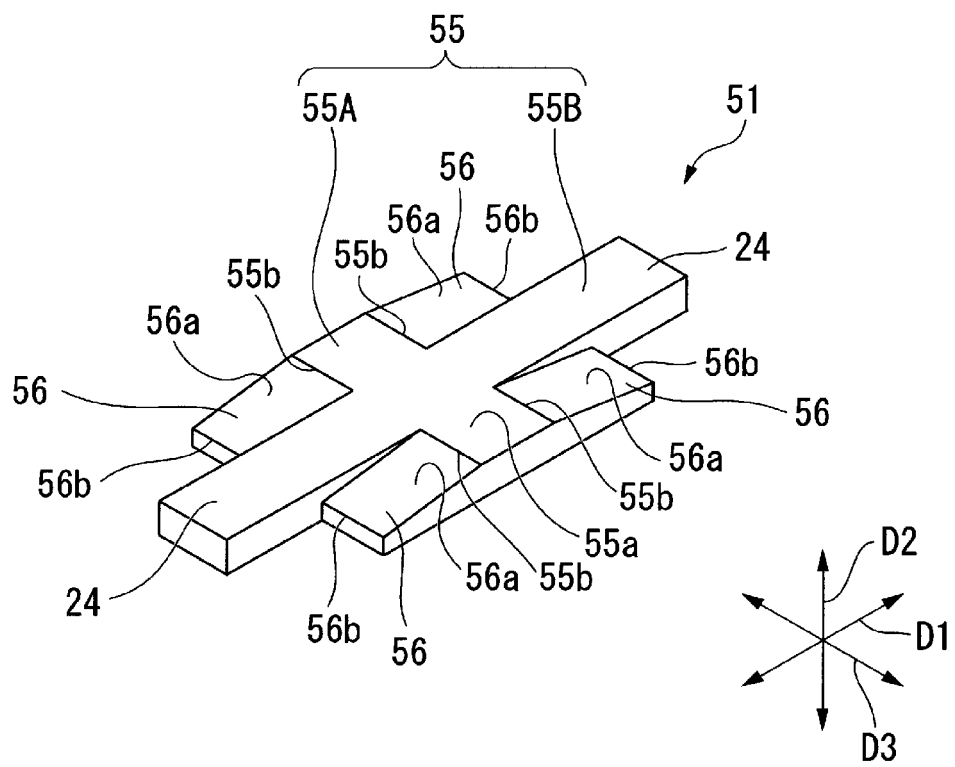
[図3B]



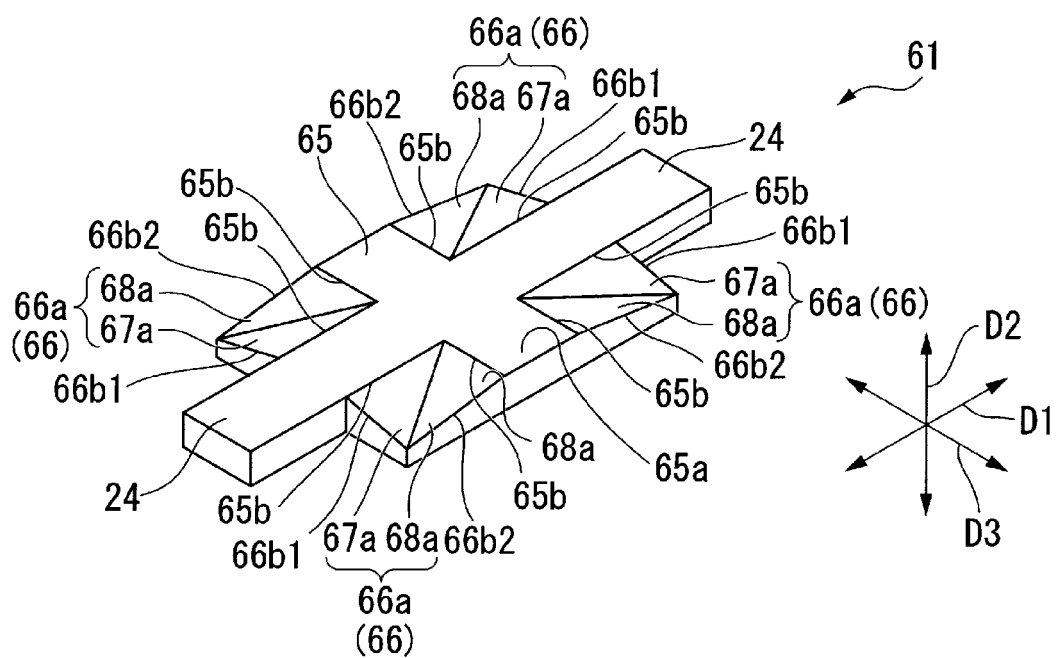
[図4]



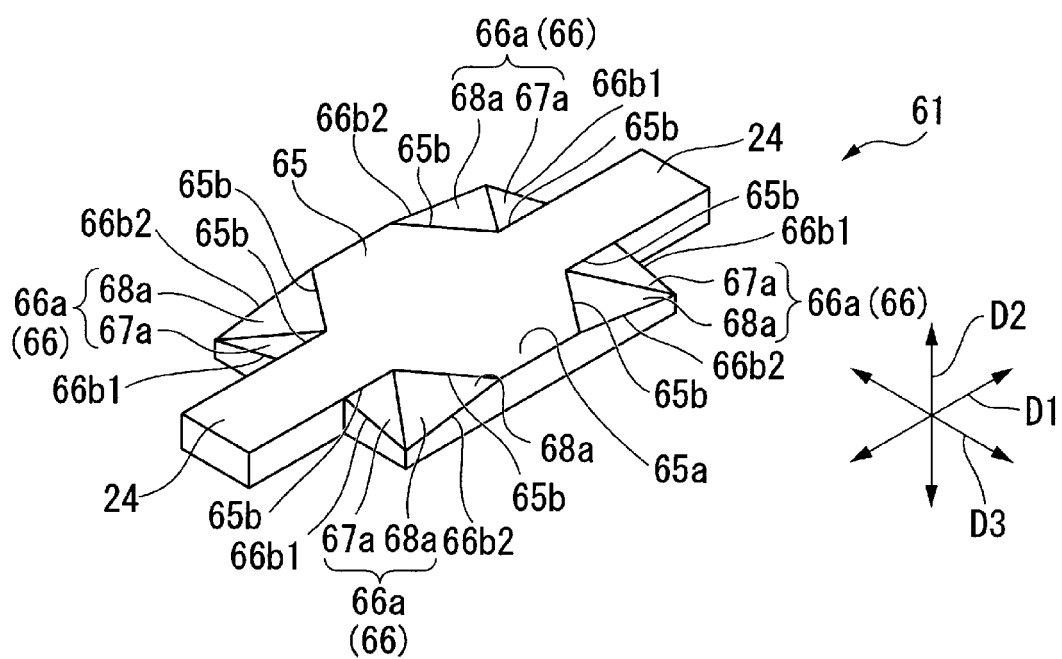
[図5]



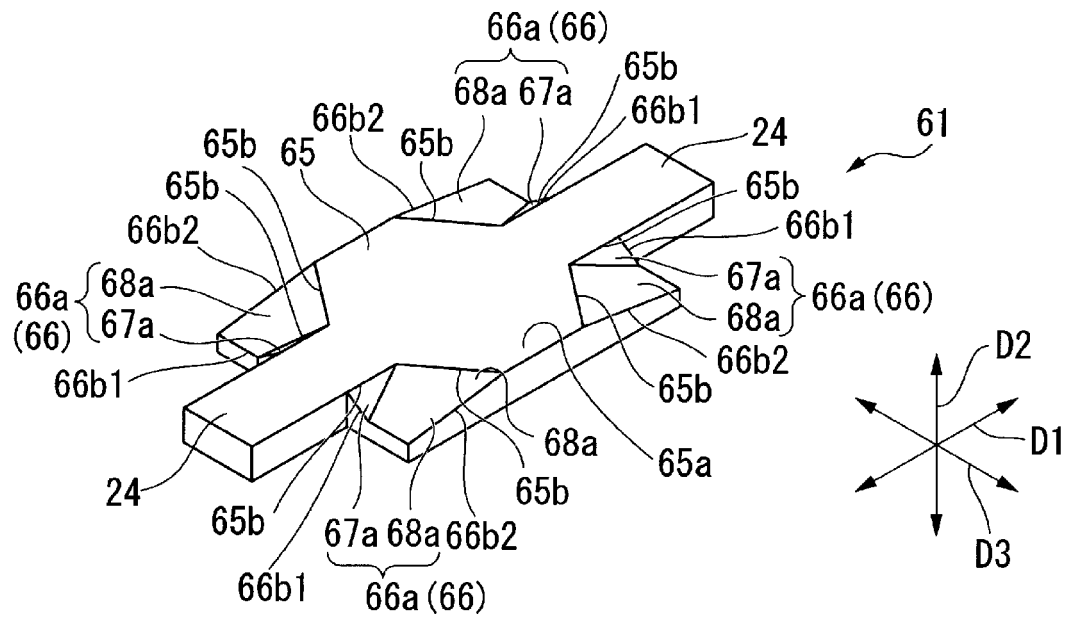
[図6A]



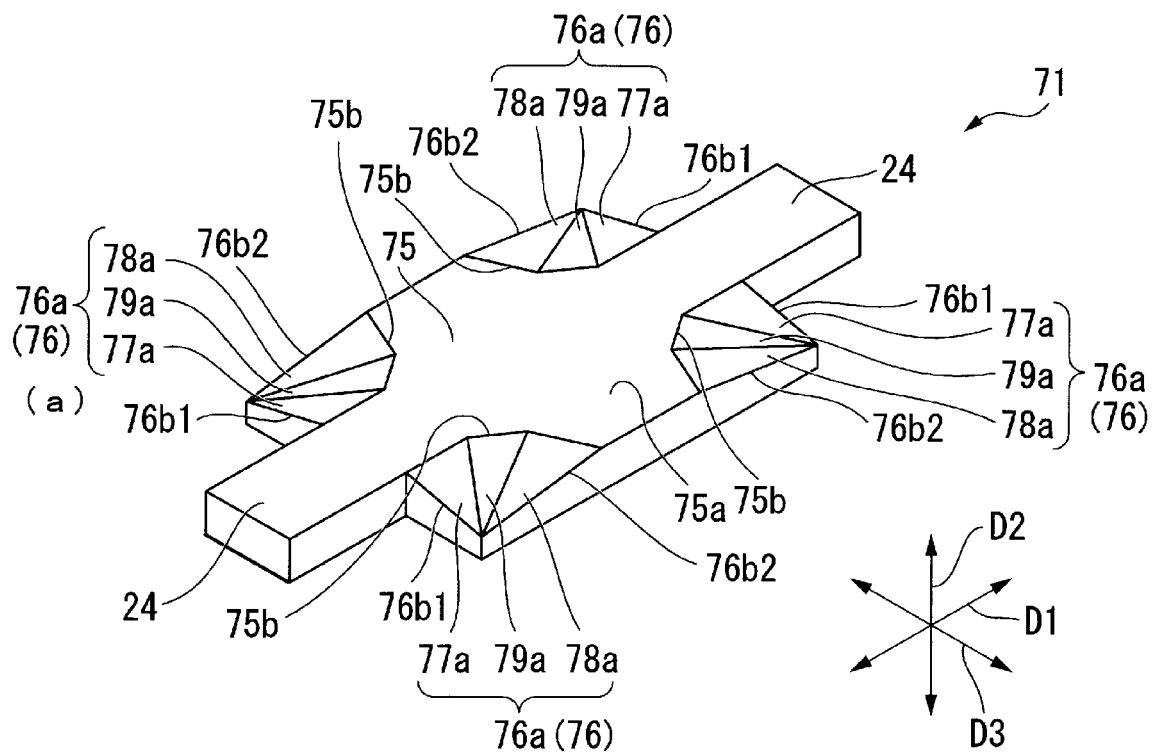
[図6B]



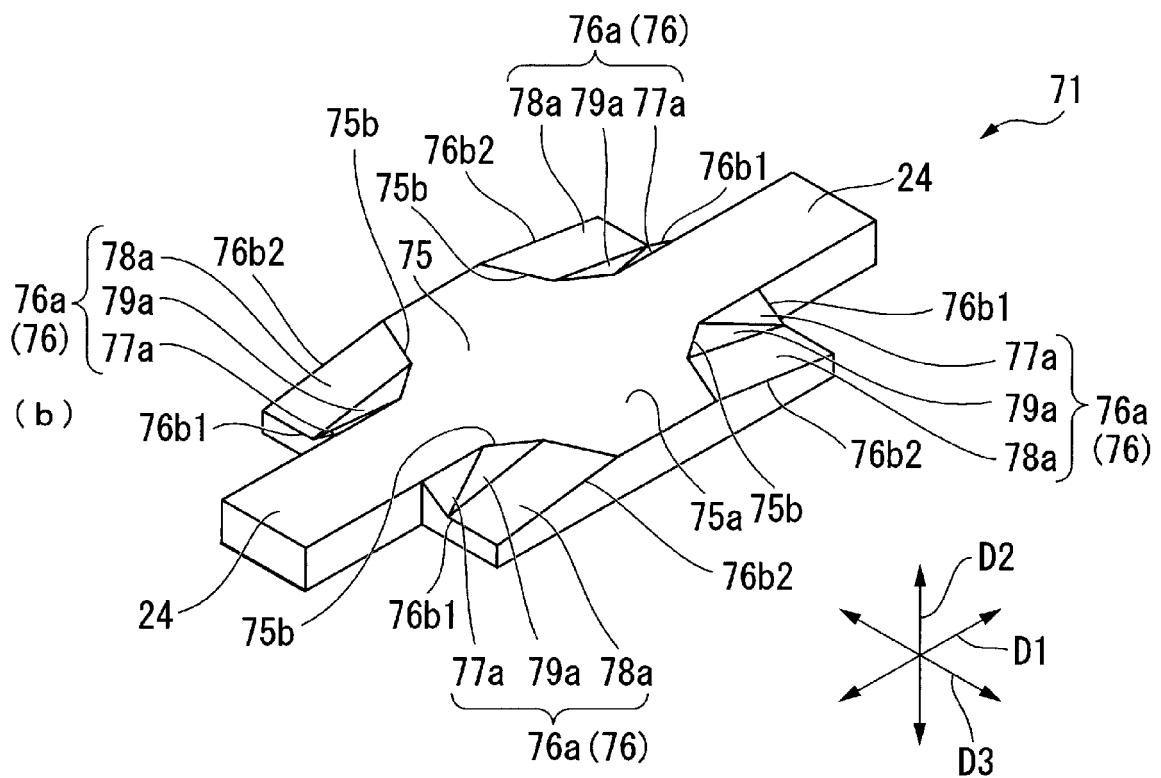
[図6C]



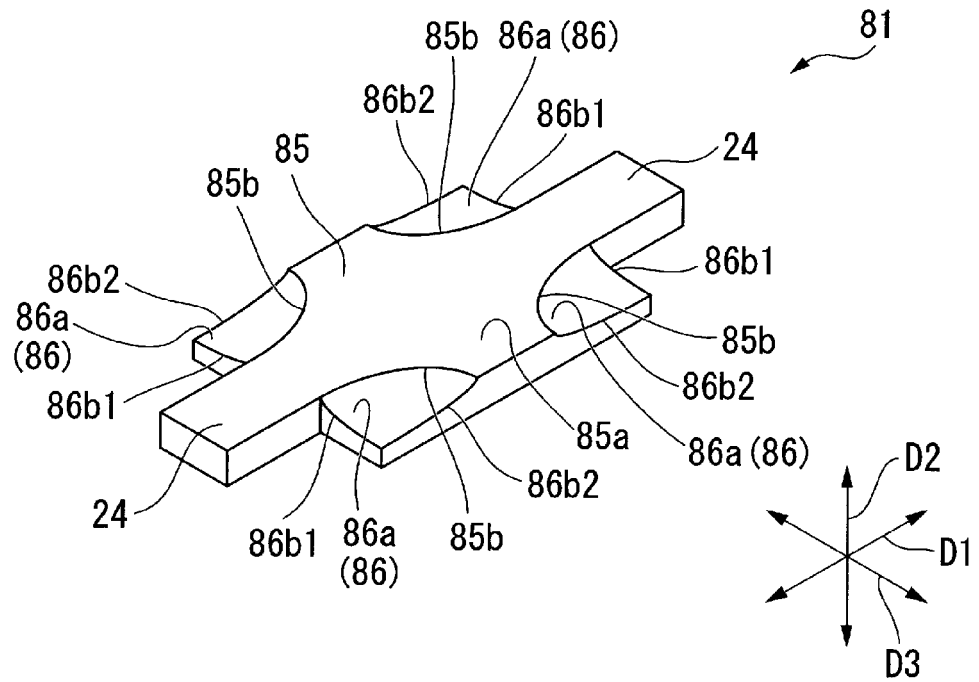
[図7A]



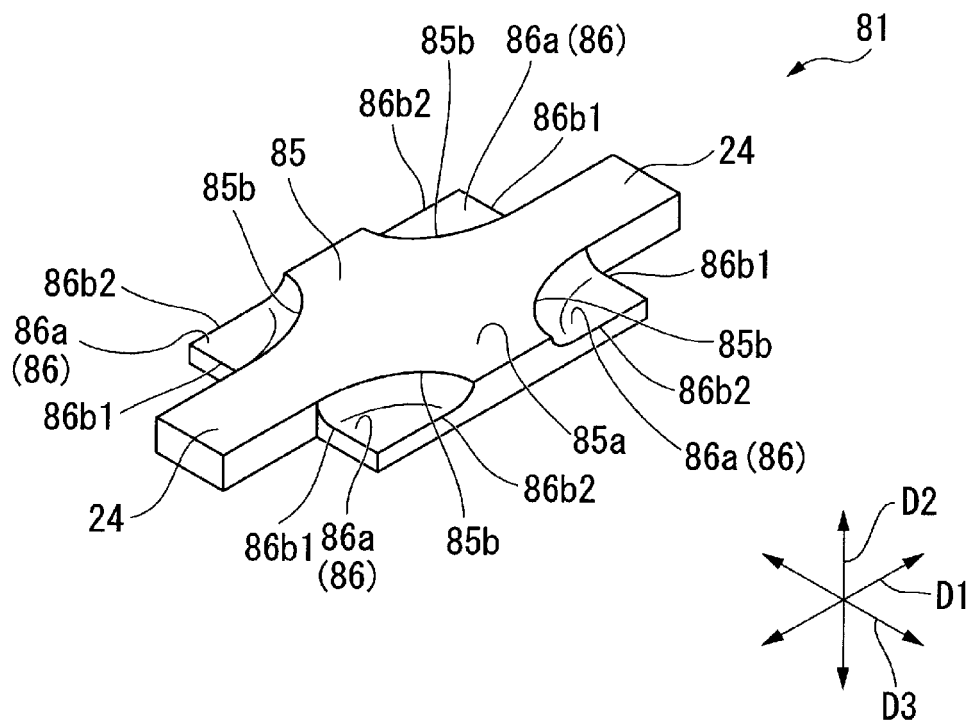
[図7B]



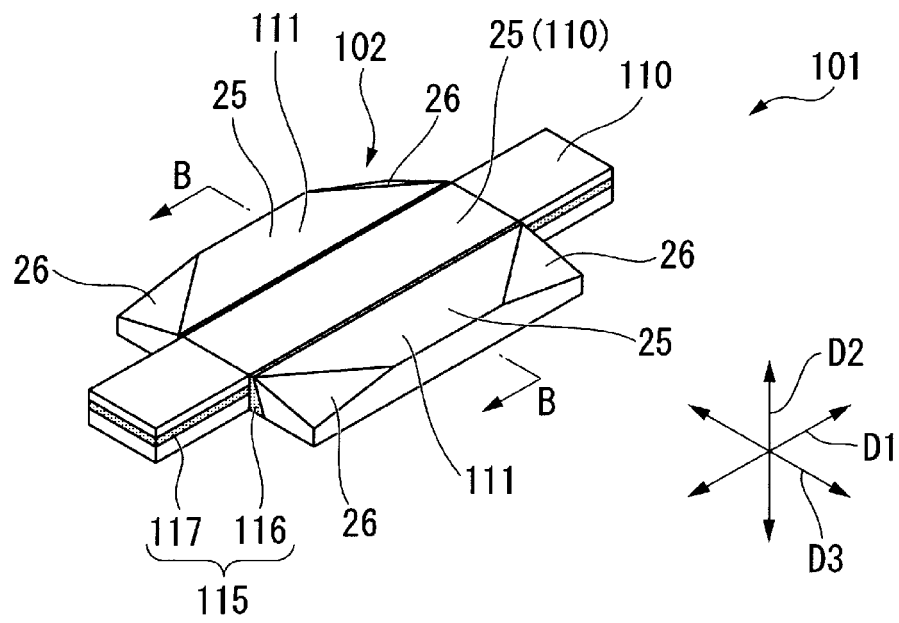
[図8A]



[図8B]



[図9]



[illegible]

The diagram illustrates a cross-section of a device. A substrate 101 has a top surface 102. A layer 110 is disposed on the top surface 102. Within this layer, there is a vertical structure 115. This structure consists of a central core 117 and two side regions 116. A dashed line 12 is positioned to the left of the structure 115. An arrow points from the label Y2 towards the structure 115. At the bottom right, a three-dimensional coordinate system is depicted with axes labeled D1, D2, and D3.