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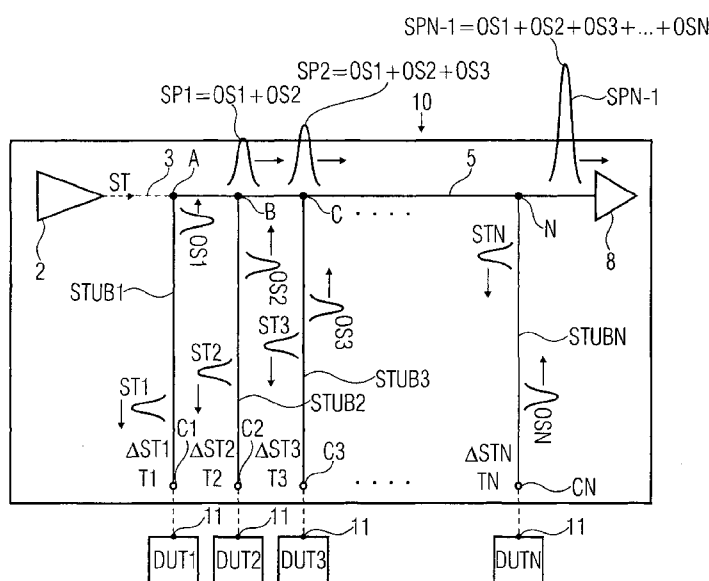
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(54) Title: APPARATUS AND METHOD FOR TESTING A PLURALITY OF DEVICES UNDER TEST



(57) Abstract: Embodiments of the invention relate to an apparatus (10) and a method for testing a plurality of devices under test wherein the apparatus comprises a common device output line (5), a driver unit (2) configured to provide a stimulus (ST) to the DUTs. (DUT1, DUT2, DUT3, ..., DUTN), wherein the driver unit (2) is configured such that the stimulus (ST) reaches different DUTs, at different times (T1, T2, T3, ..., TN); thereby creating stimuli time shifts ($\Delta ST1$, $\Delta ST2$, $\Delta ST2$, ..., $\Delta STN-1$) at the DUTs, a receiver unit (8) electrically coupled to the common device output line (5); and a plurality of DUT connections (C1, C2, C3, ..., CN), electrically coupled to the common device output line (5), so that DUT terminals (11) of the plurality of DUTs are electrically coupleable via the common output device line (5) to the receiver unit (8), wherein output signal propagation delays for DUT output signals (OS1, OS2, OS3, ..., OSN), propagating from the DUT connections (C1, C2, C3, ..., CN) to the receiver unit (8), are adapted to the stimuli time shifts ($\Delta ST1$, $\Delta ST2$, ..., $\Delta STN-1$), such that a test of DUTs, having identical stimulus response delays, results in a temporally aligned superposition

tion (SPN-1) of the DUT output signals (OS1, OS2, OS3, ..., OSN) at the receiver unit (8).