

[Fig.1]

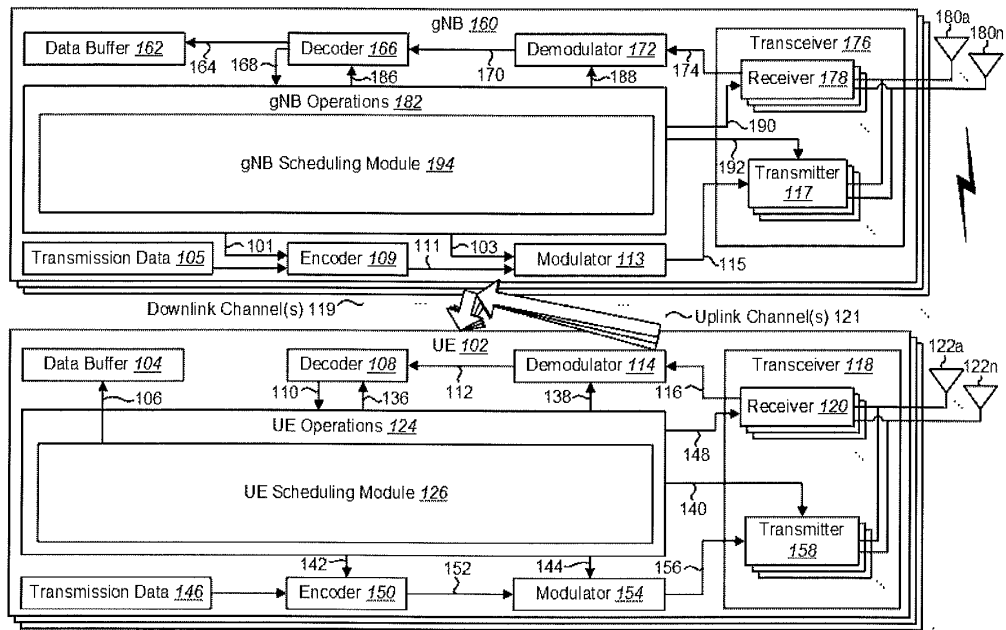


FIG. 1

[Fig.2]

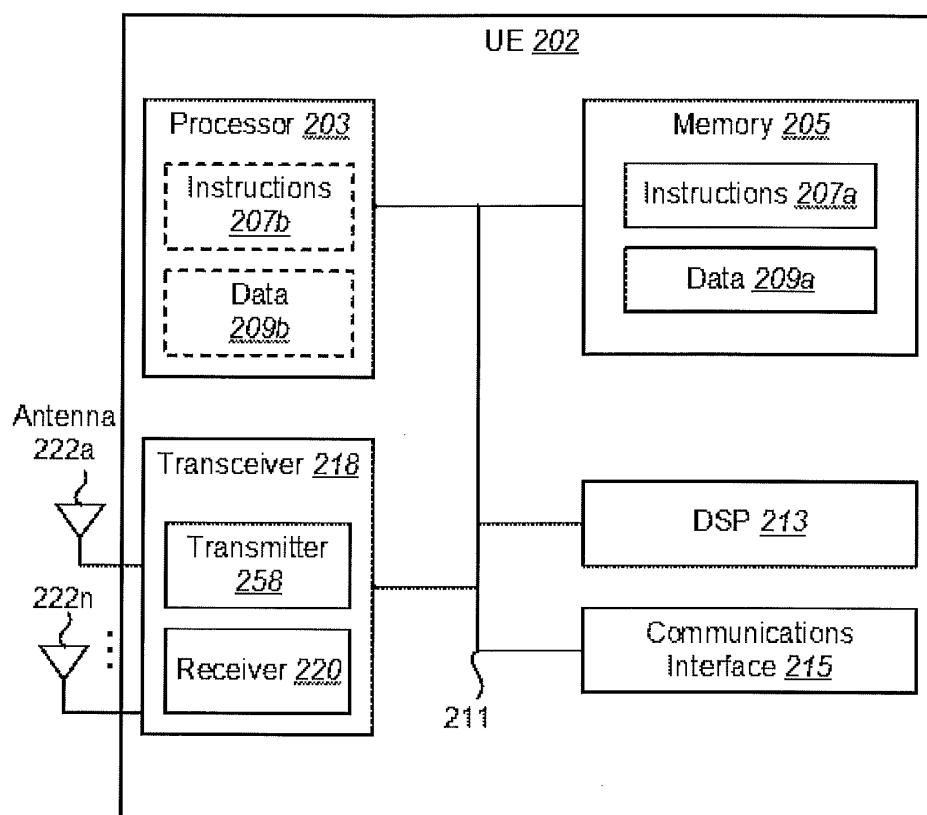


FIG. 2

[Fig.3]

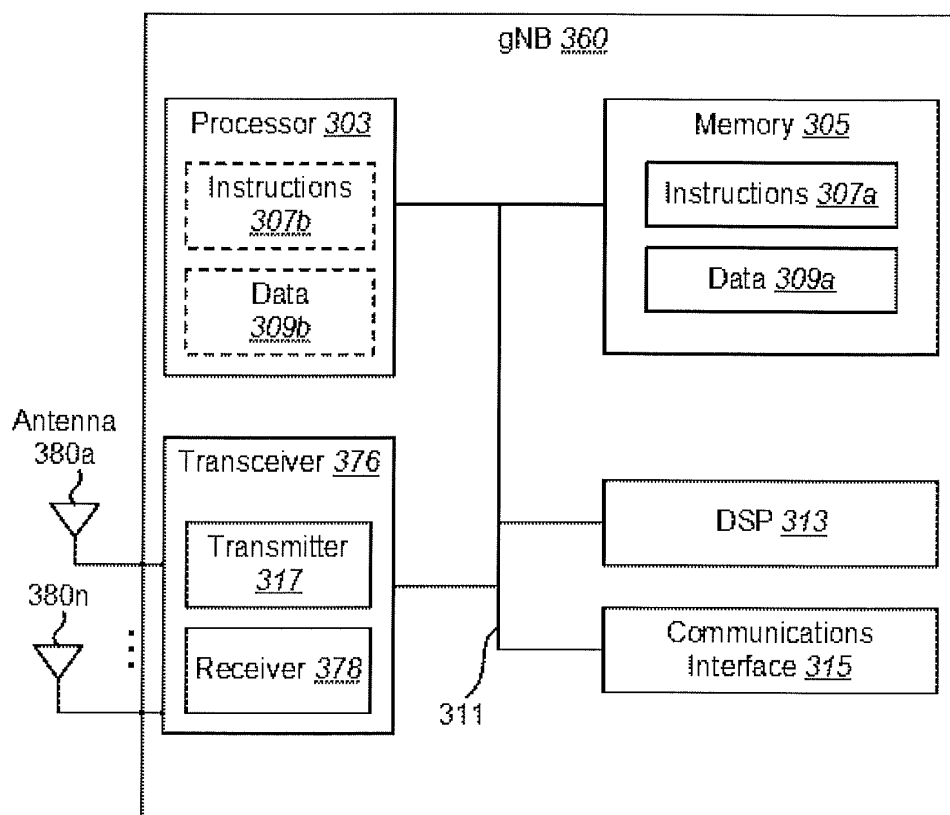
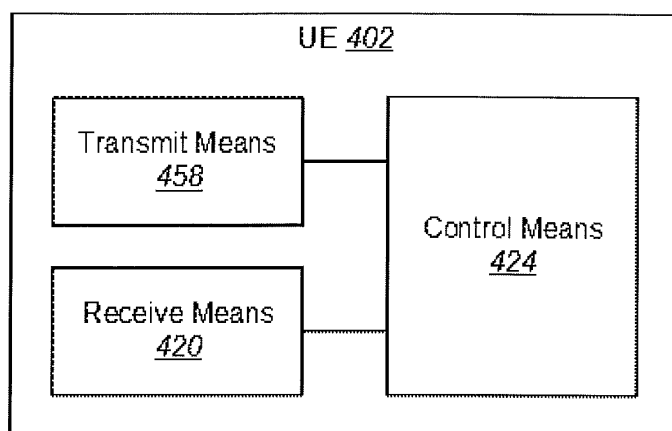
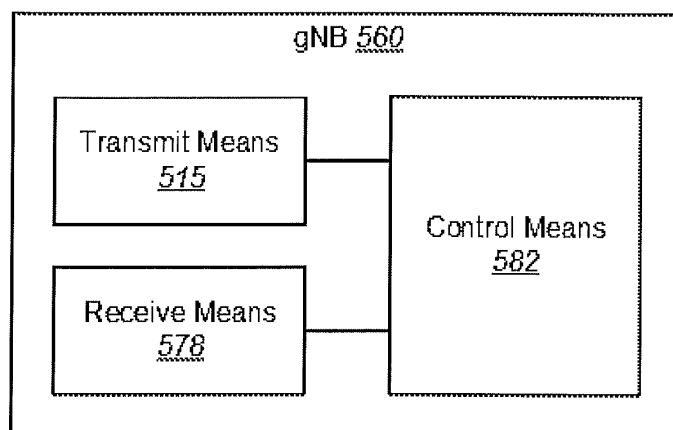


FIG. 3

[Fig.4]

**FIG. 4**

[Fig.5]

**FIG. 5**

[Fig.6]

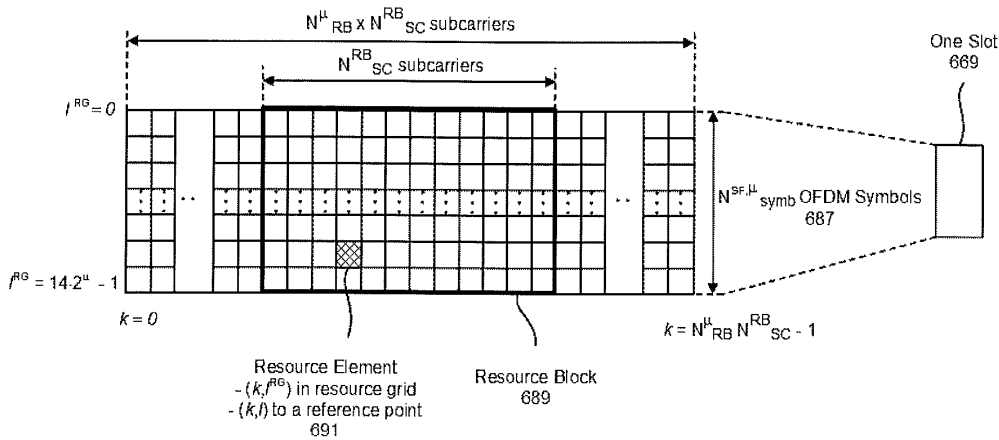


FIG. 6

[Fig.7]

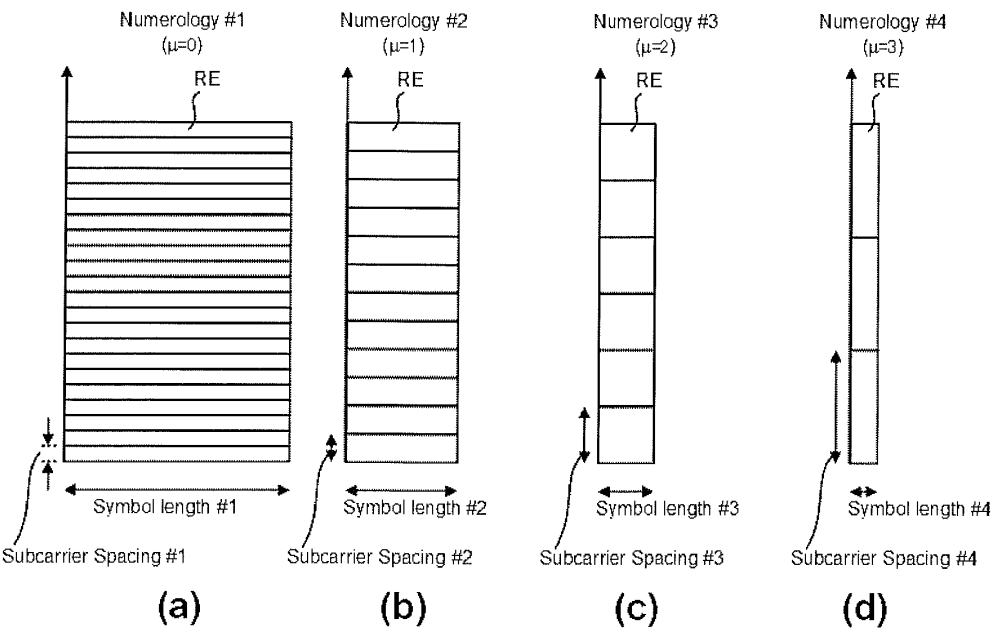


FIG. 7

[Fig.8]

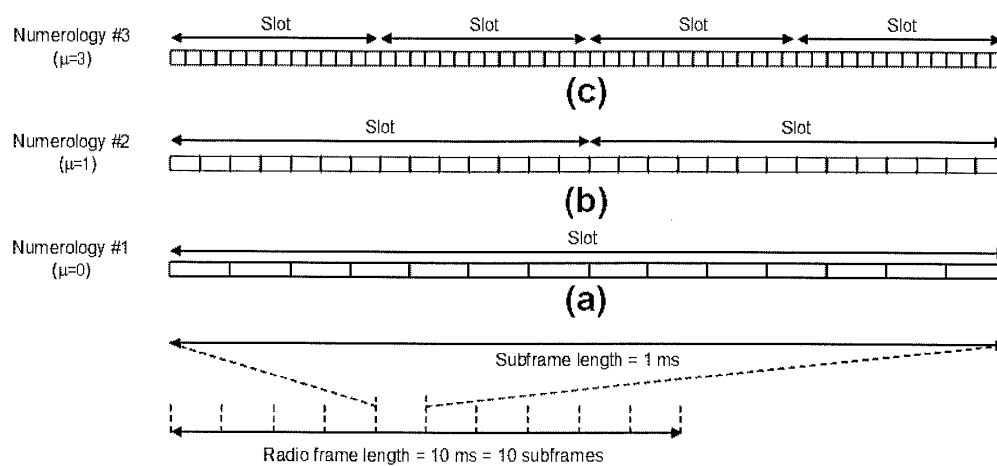


FIG. 8

[Fig.9]

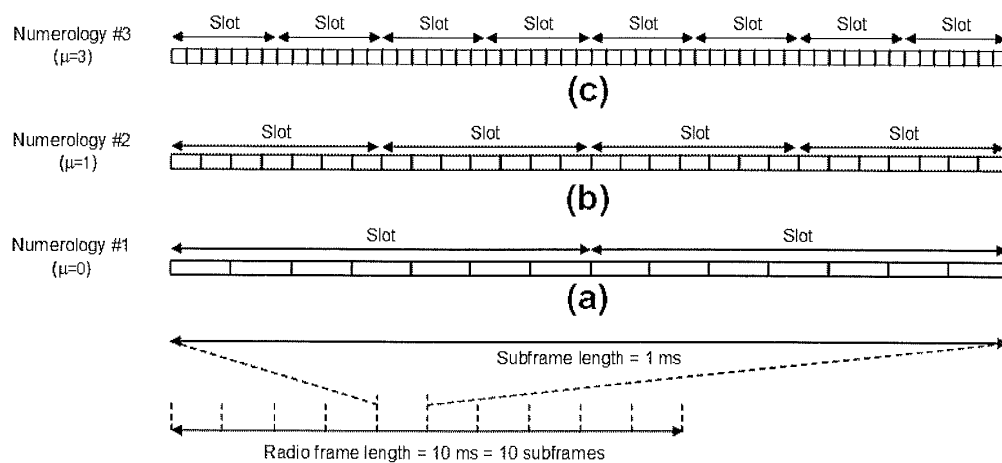


FIG. 9

[Fig.10]

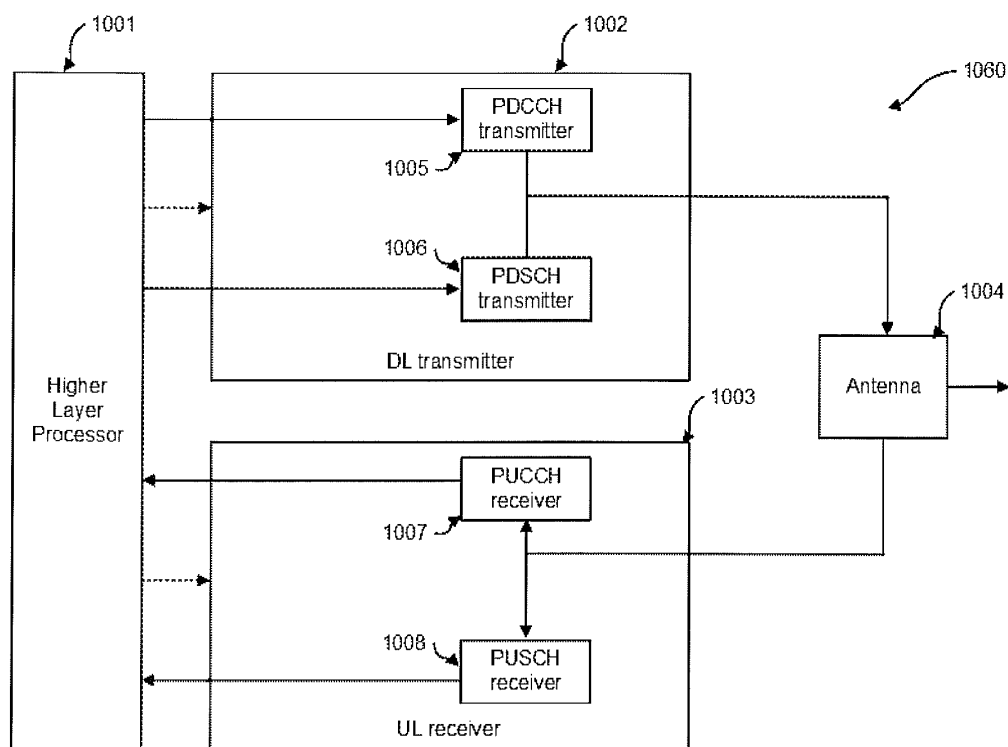
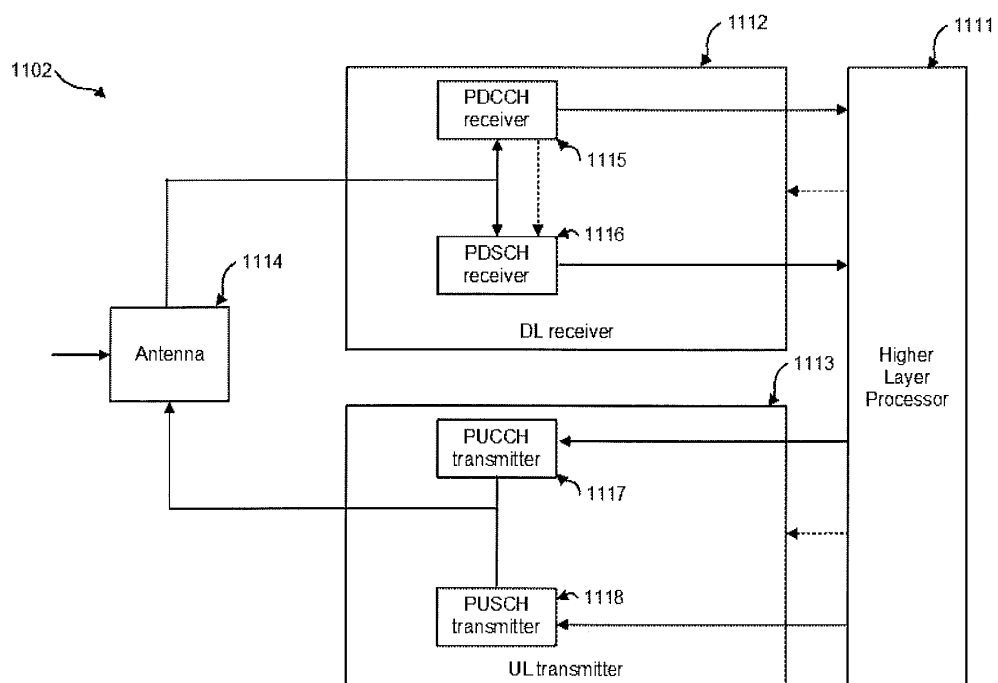


FIG. 10

[Fig.11]

**FIG. 11**

[Fig.12]

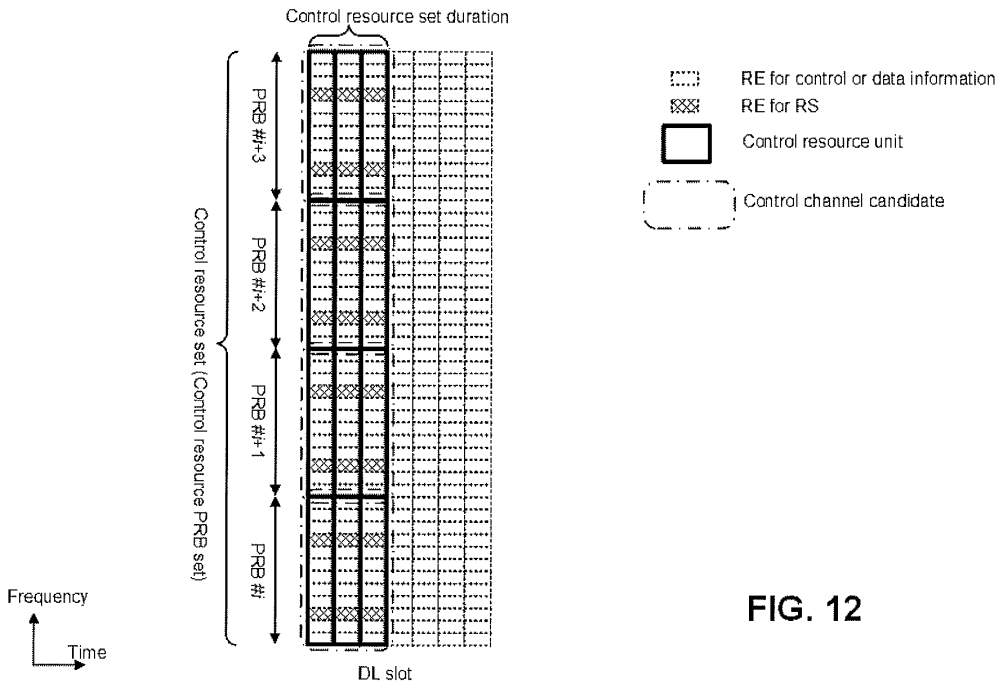


FIG. 12

[Fig.13]

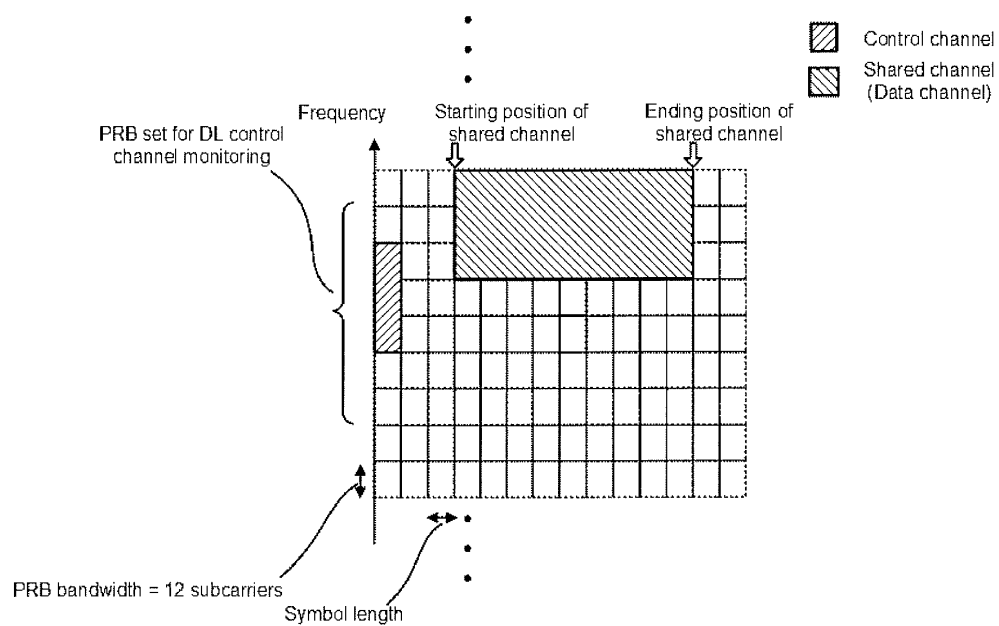


FIG. 13

[Fig.14]

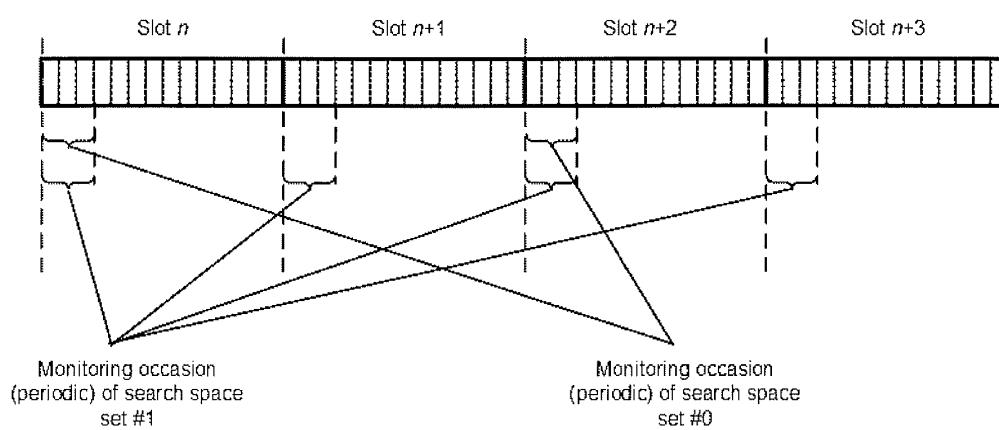


FIG. 14

[Fig.15]

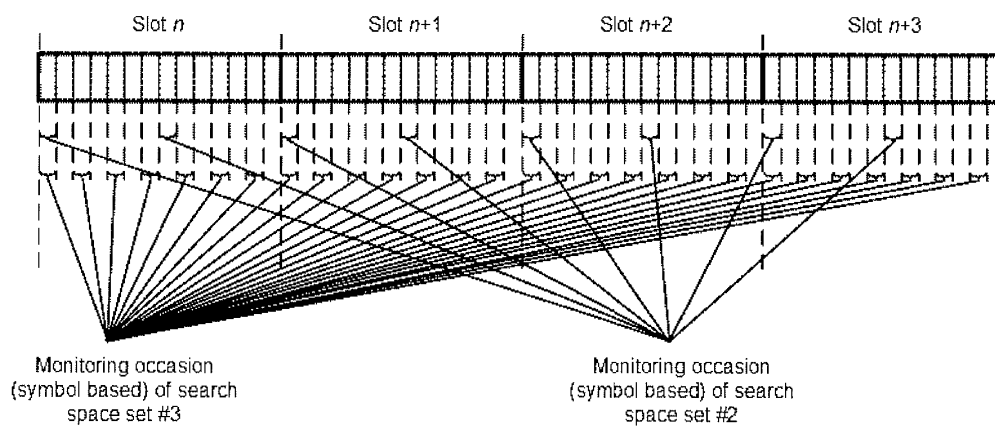


FIG. 15

[Fig.16]

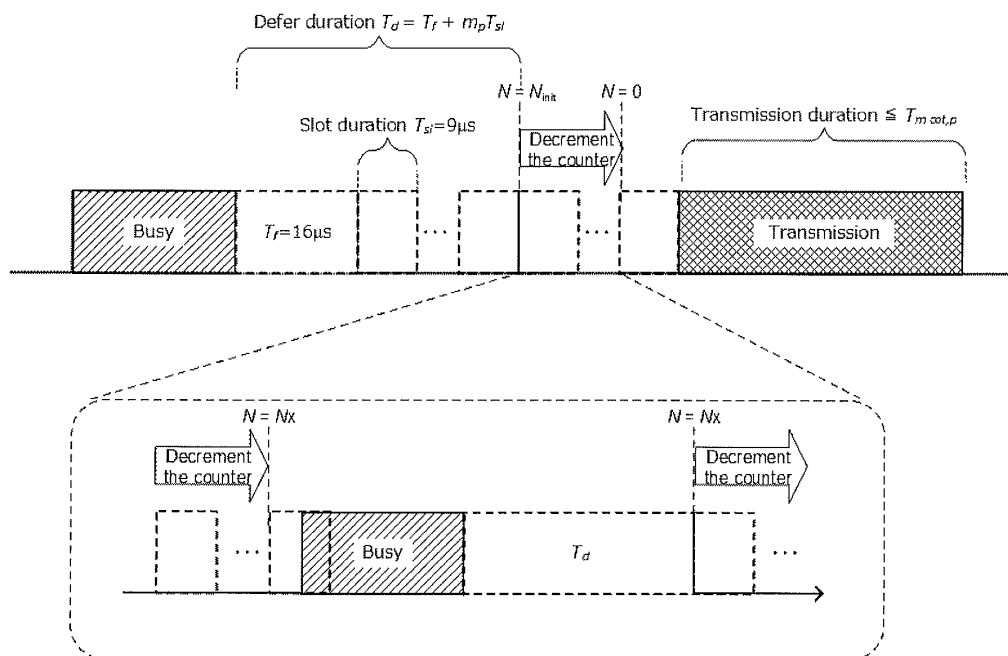


FIG. 16

[Fig.17]

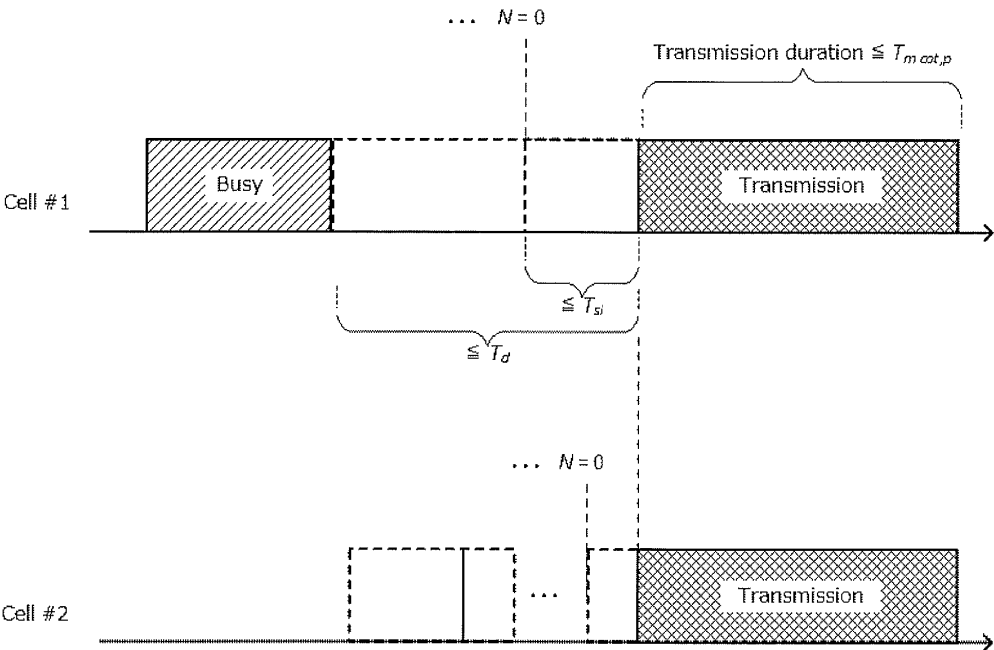


FIG. 17

[Fig.18]

| Channel Access<br>Priority Class<br>( <i>p</i> ) | <i>m<sub>p</sub></i> | <i>CW<sub>min,p</sub></i> | <i>CW<sub>max,p</sub></i> | <i>T<sub>m cot,p</sub></i> | Allowed <i>CW<sub>p</sub></i><br>sizes |
|--------------------------------------------------|----------------------|---------------------------|---------------------------|----------------------------|----------------------------------------|
| 1                                                | 1                    | 3                         | 7                         | 2 ms                       | {3, 7}                                 |
| 2                                                | 1                    | 7                         | 15                        | 3 ms                       | {7, 15}                                |
| 3                                                | 3                    | 15                        | 63                        | 8 or 10 ms                 | {15, 31, 63}                           |
| 4                                                | 7                    | 15                        | 1023                      | 8 or 10 ms                 | {15, 31, 63, 127,<br>255, 511, 1023}   |

FIG. 18

[Fig.19]

| Channel Access<br>Priority Class<br>( <i>p</i> ) | <i>m<sub>p</sub></i> | <i>CW<sub>min,p</sub></i> | <i>CW<sub>max,p</sub></i> | <i>T<sub>maxot,p</sub></i> | Allowed <i>CW<sub>p</sub></i><br>sizes |
|--------------------------------------------------|----------------------|---------------------------|---------------------------|----------------------------|----------------------------------------|
| 1                                                | 2                    | 3                         | 7                         | 2 ms                       | {3, 7}                                 |
| 2                                                | 2                    | 7                         | 15                        | 4 ms                       | {7, 15}                                |
| 3                                                | 3                    | 15                        | 1023                      | 6 or 10 ms                 | {15, 31, 63, 127,<br>255, 511, 1023}   |
| 4                                                | 7                    | 15                        | 1023                      | 6 or 10 ms                 | {15, 31, 63, 127,<br>255, 511, 1023}   |

FIG. 19

[Fig.20]

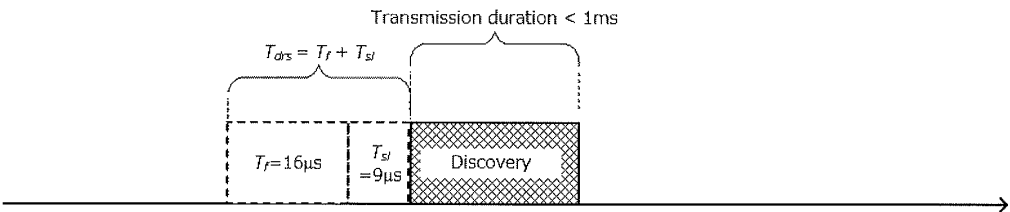


FIG. 20

[Fig.21]

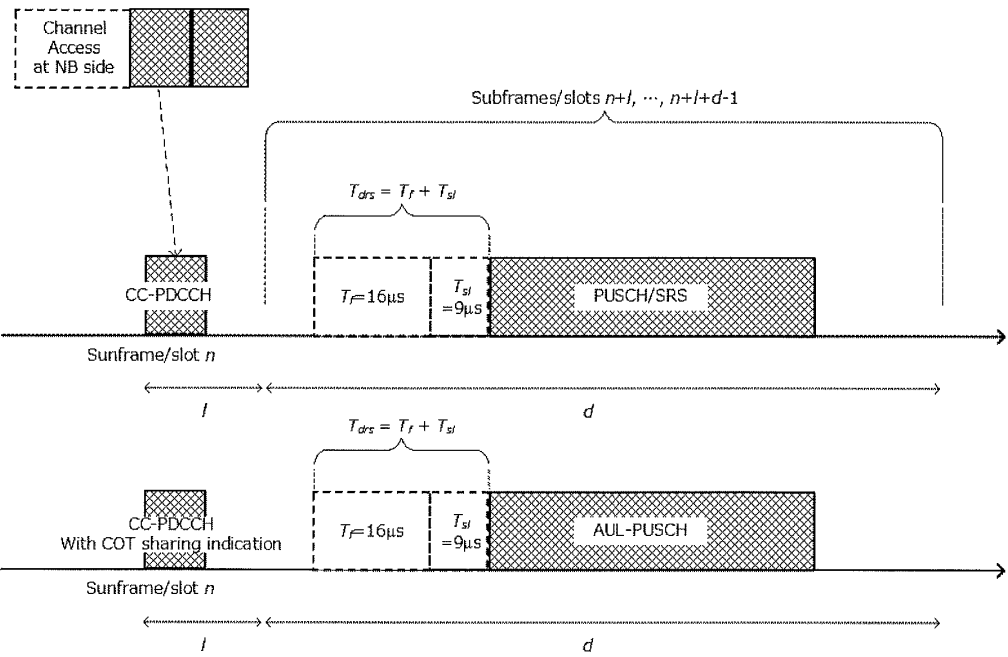


FIG. 21

[Fig.22]

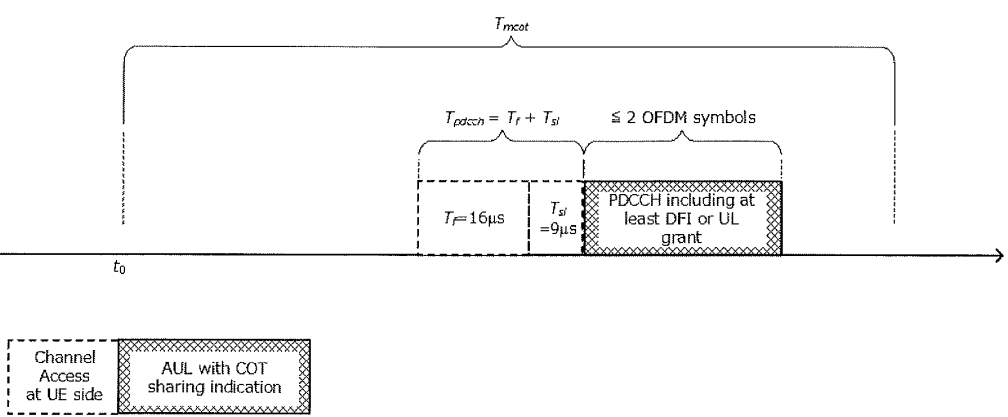


FIG. 22

[Fig.23]

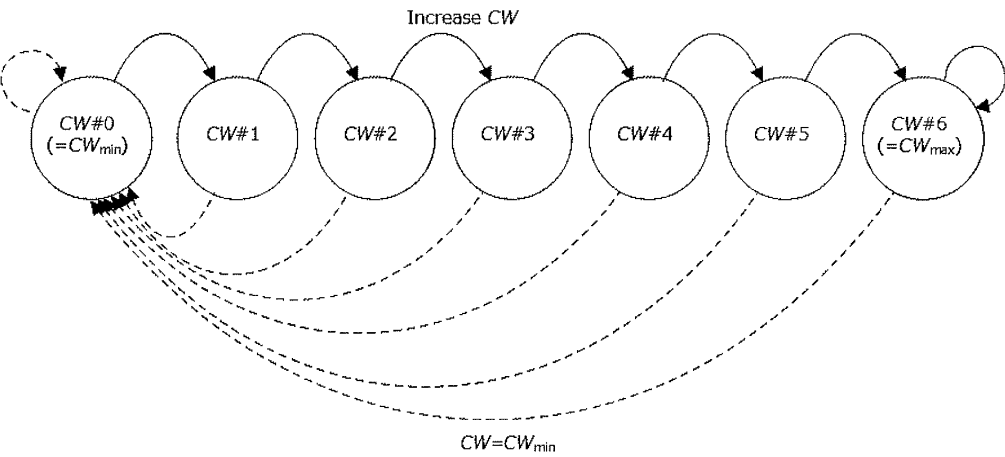


FIG. 23

[Fig.24]

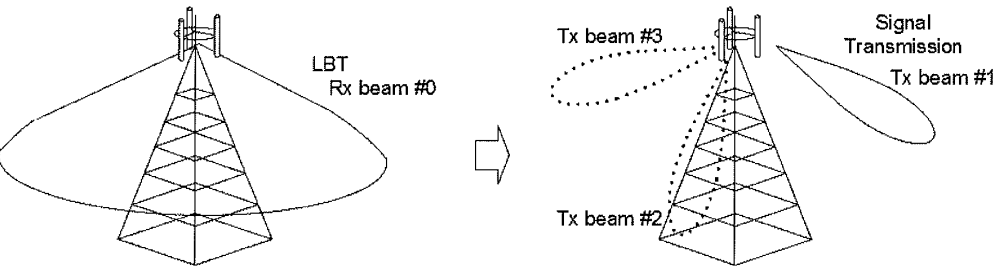


FIG. 24

[Fig.25]

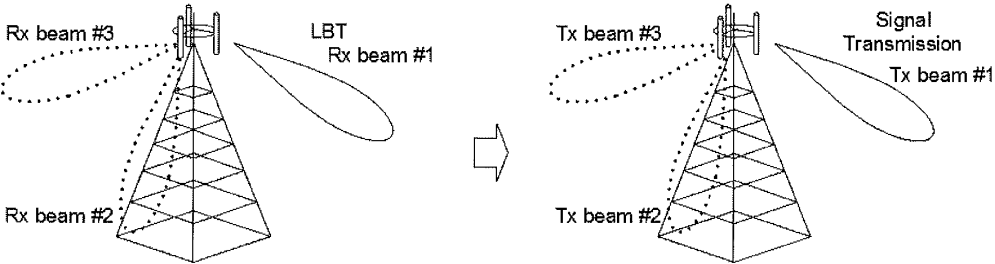


FIG. 25

[Fig.26]

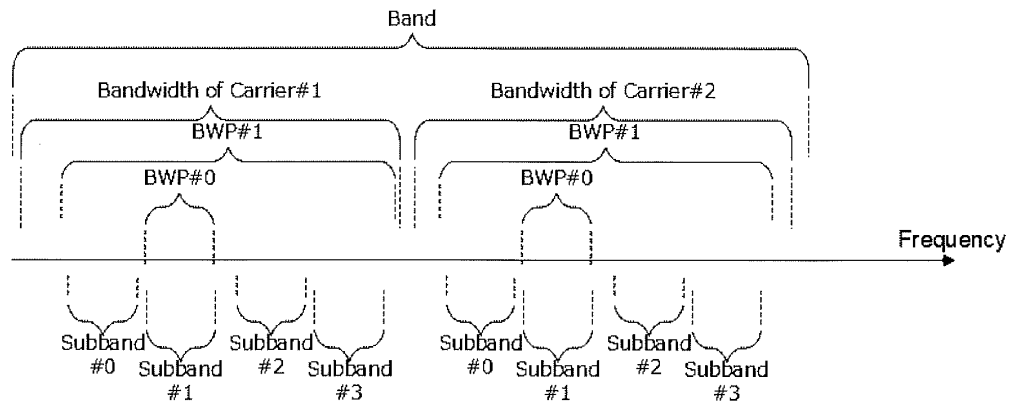


FIG. 26

[Fig.27]

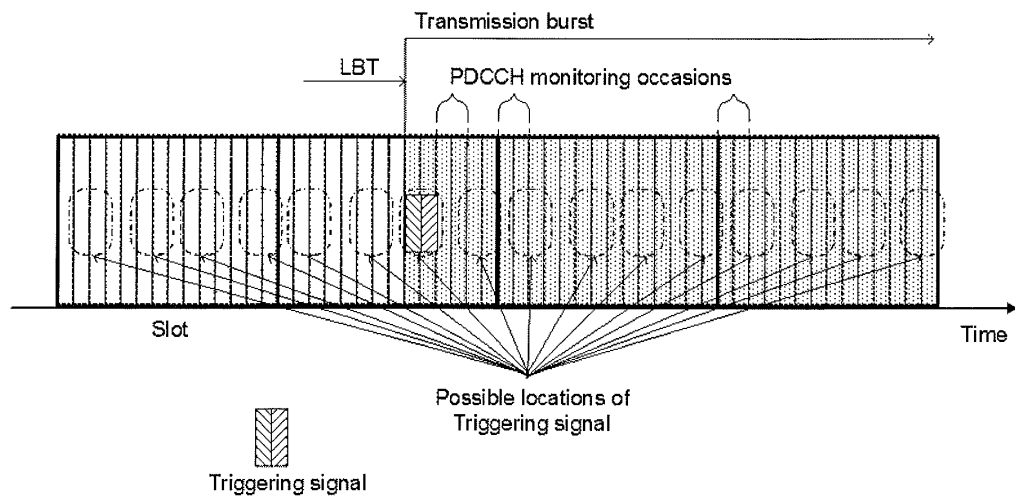


FIG. 27

[Fig.28]

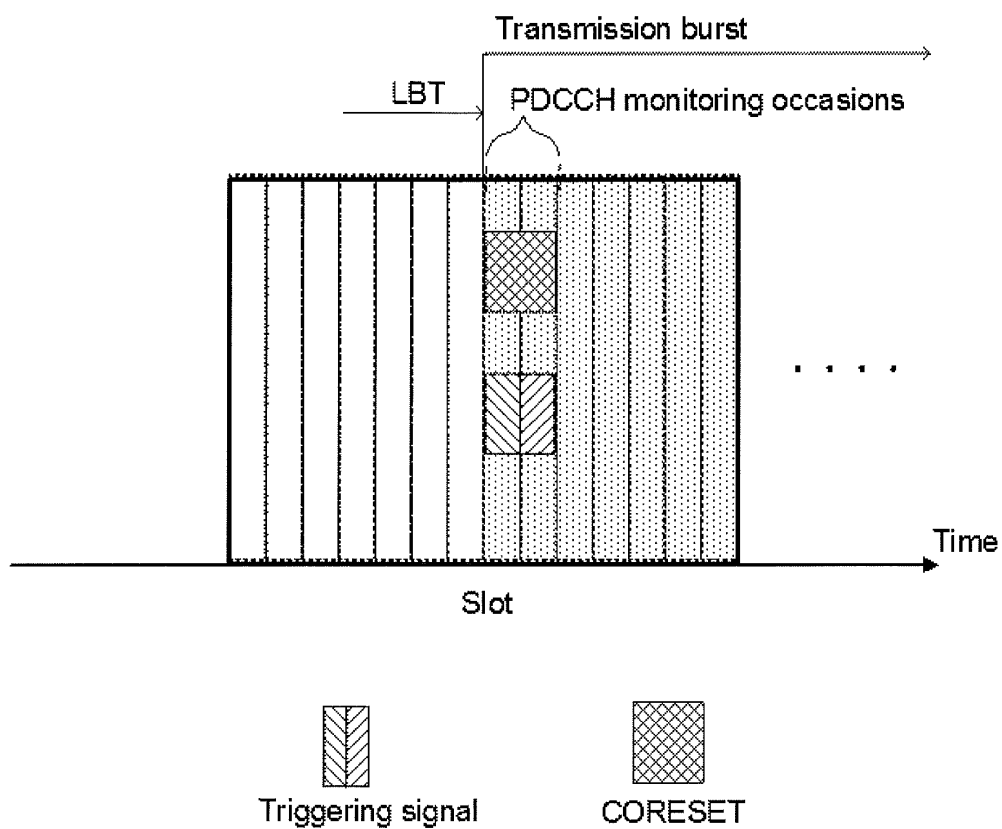


FIG. 28

[Fig.29]

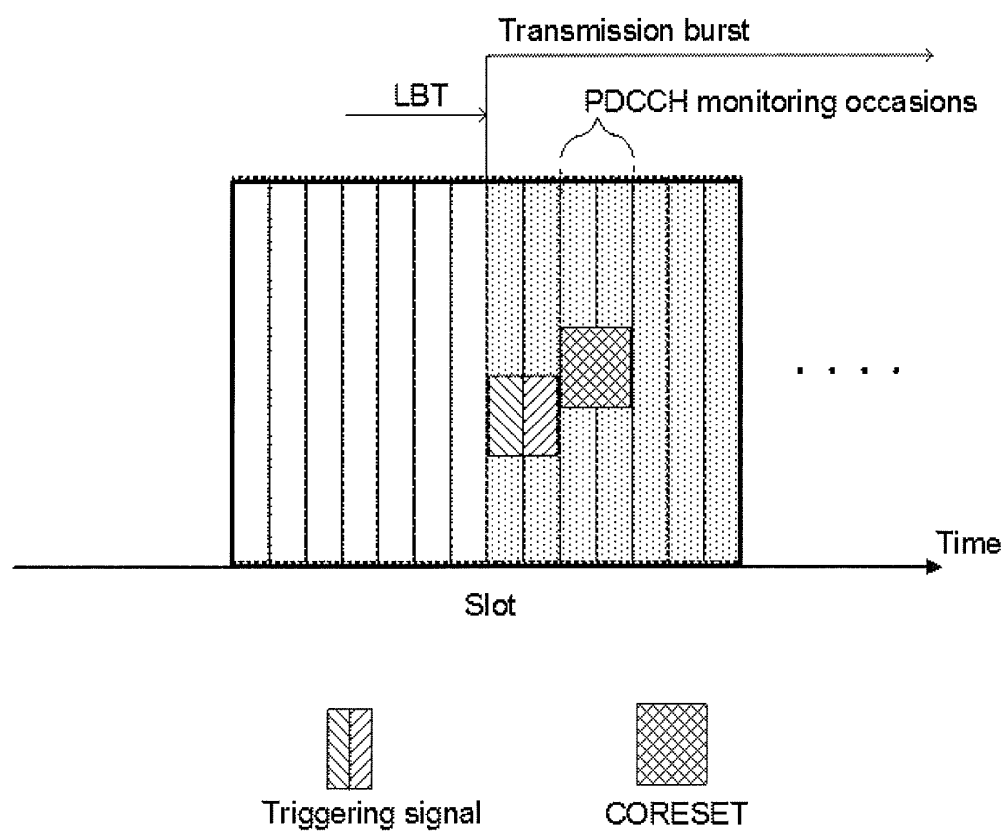


FIG. 29

[Fig.30]

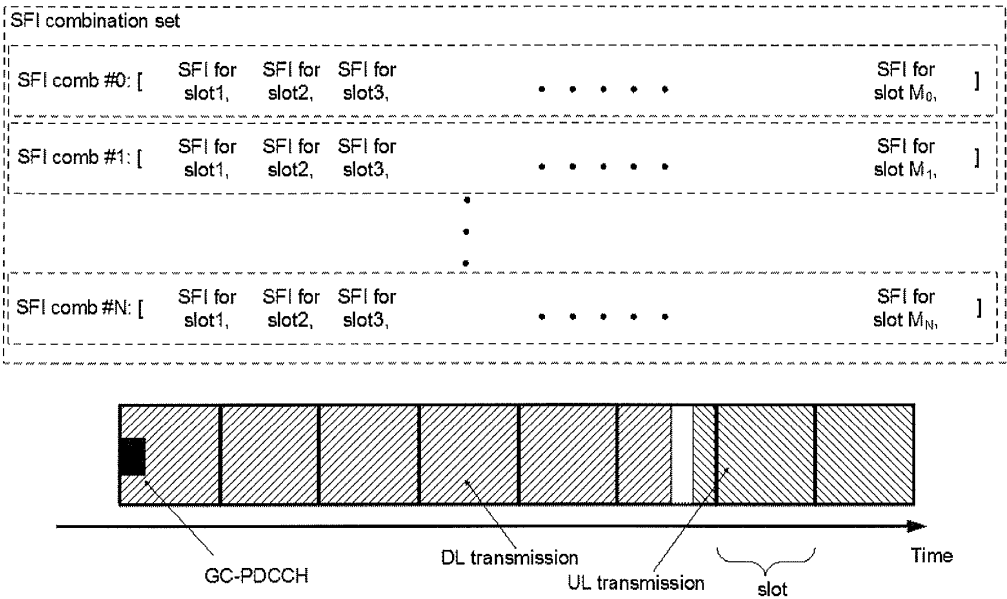


FIG. 30

[Fig.31]

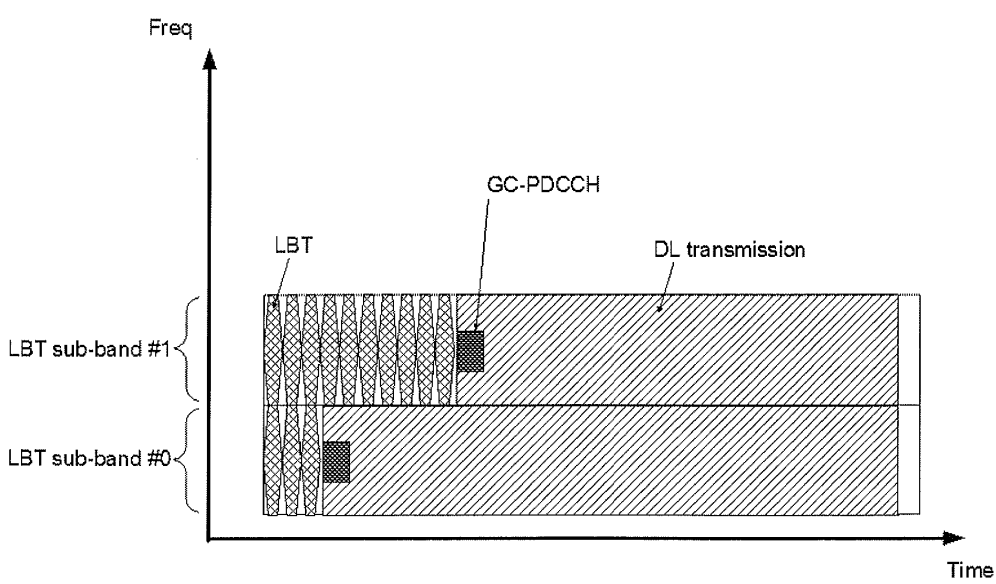


FIG. 31

[Fig.32]

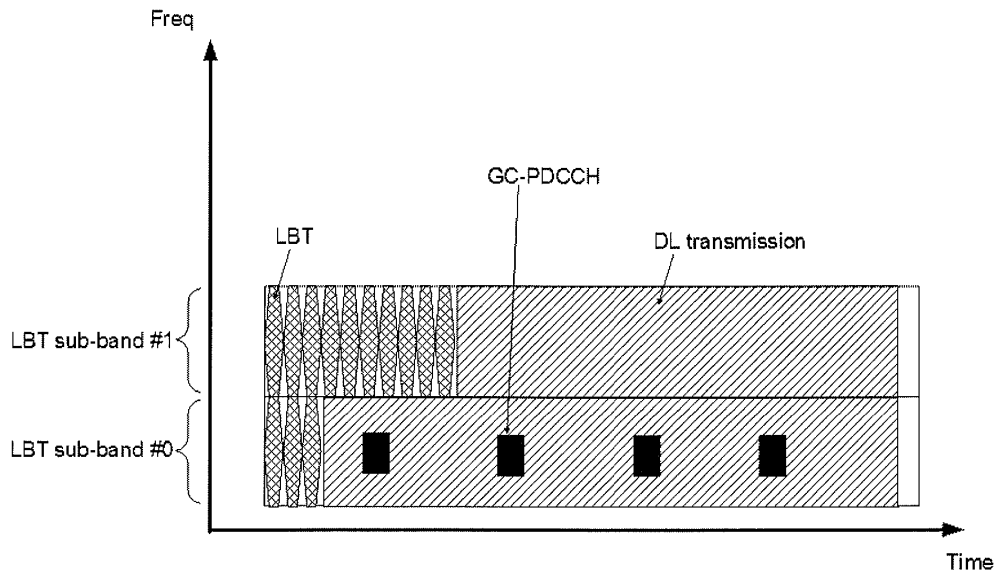


FIG. 32

[Fig.33]

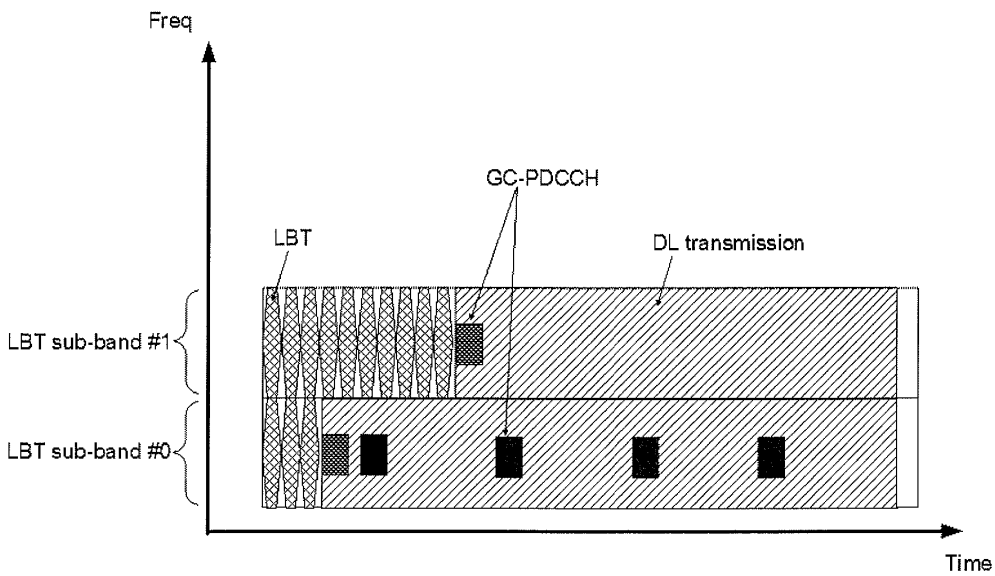


FIG. 33

[Fig.34]

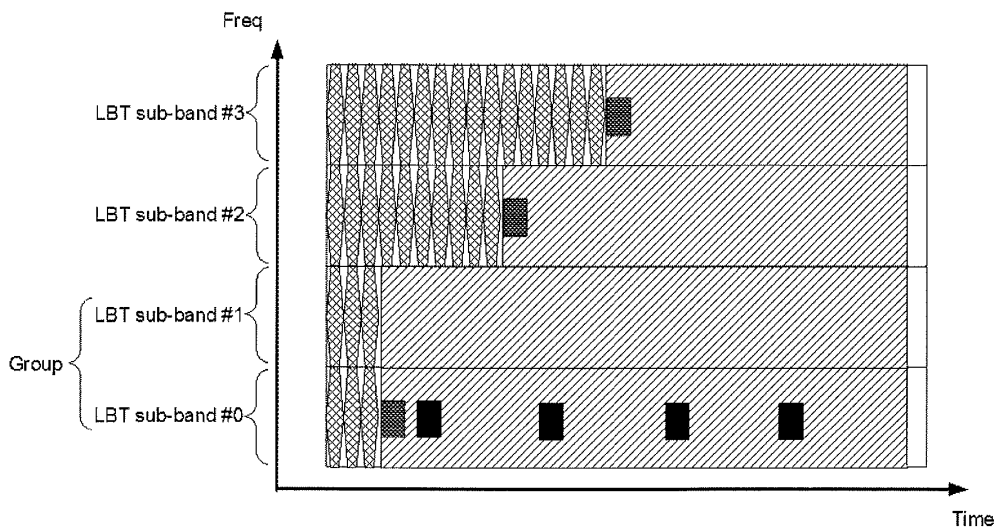


FIG. 34

[Fig.35]

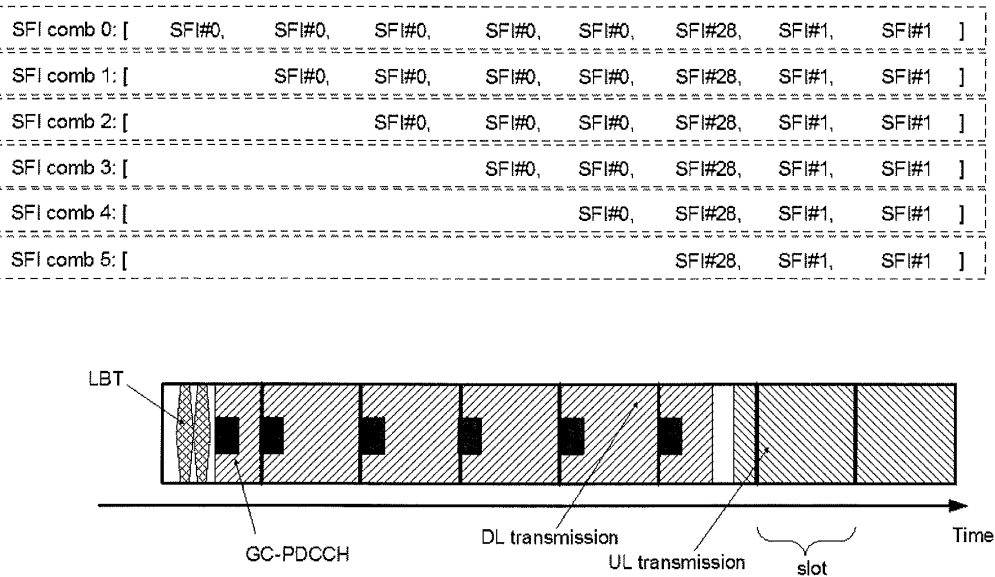


FIG. 35

[Fig.36]

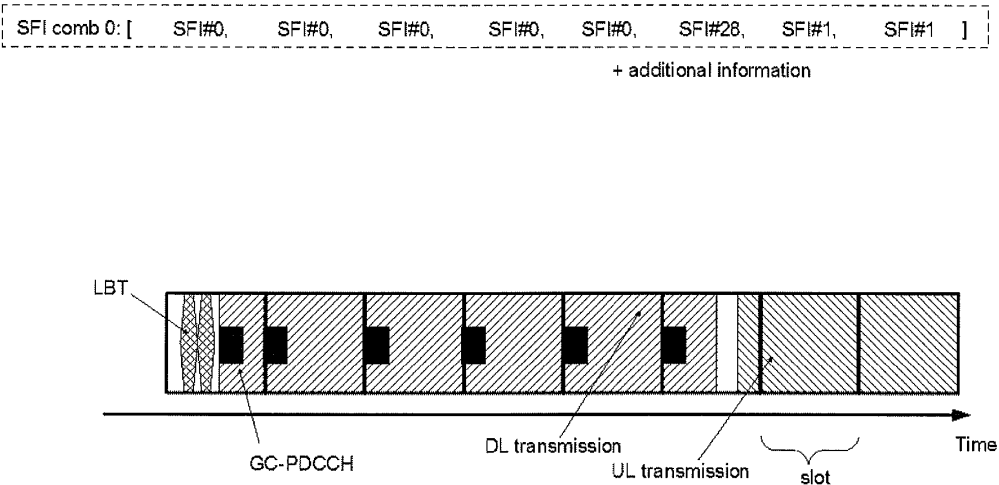


FIG. 36

[Fig.37]

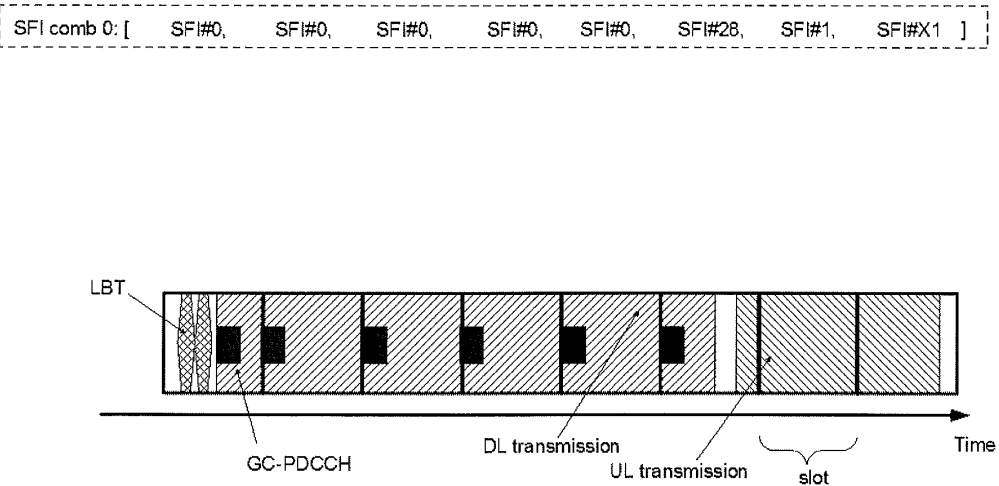
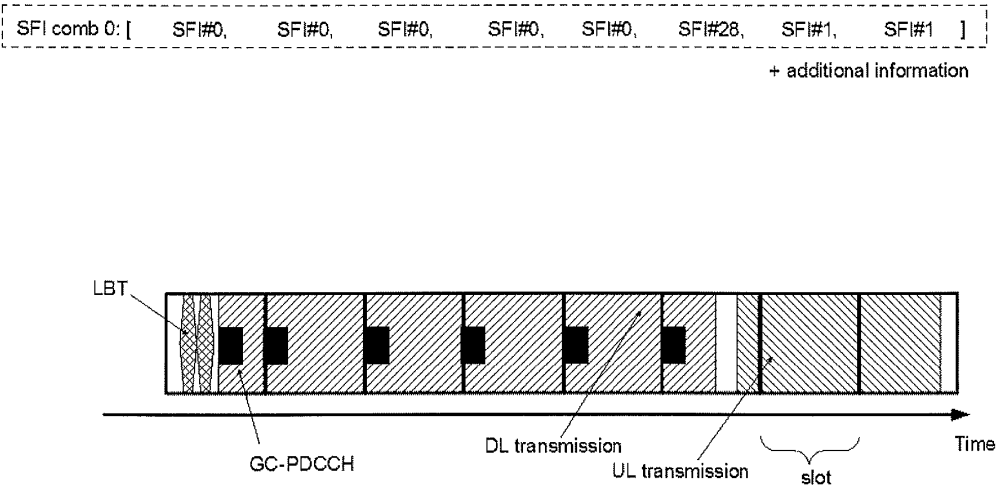
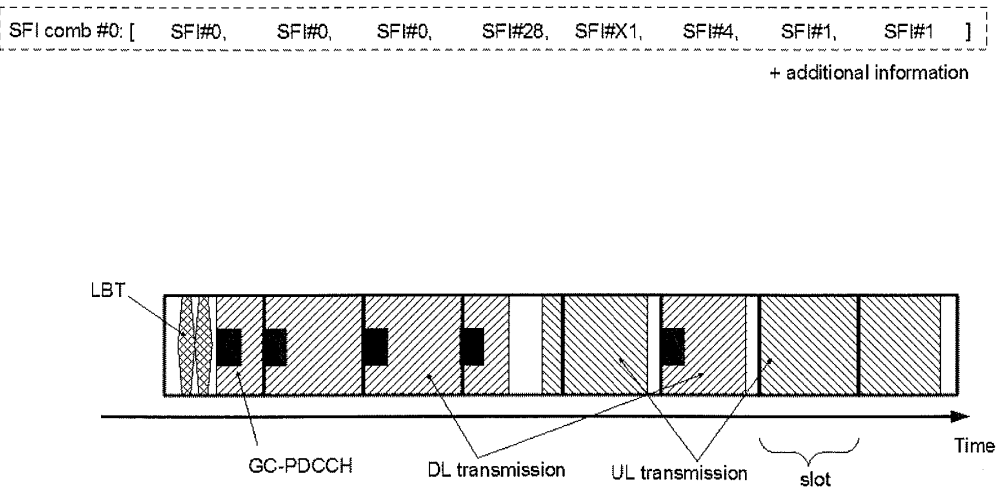


FIG. 37

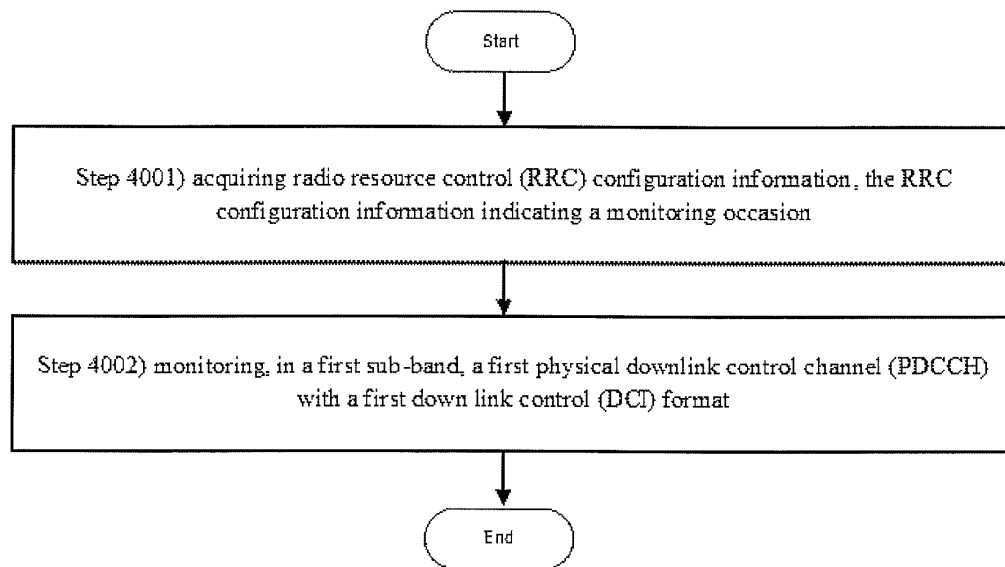
[Fig.38]



[Fig.39]



[Fig.40]

**FIG. 40**

[Fig.41]

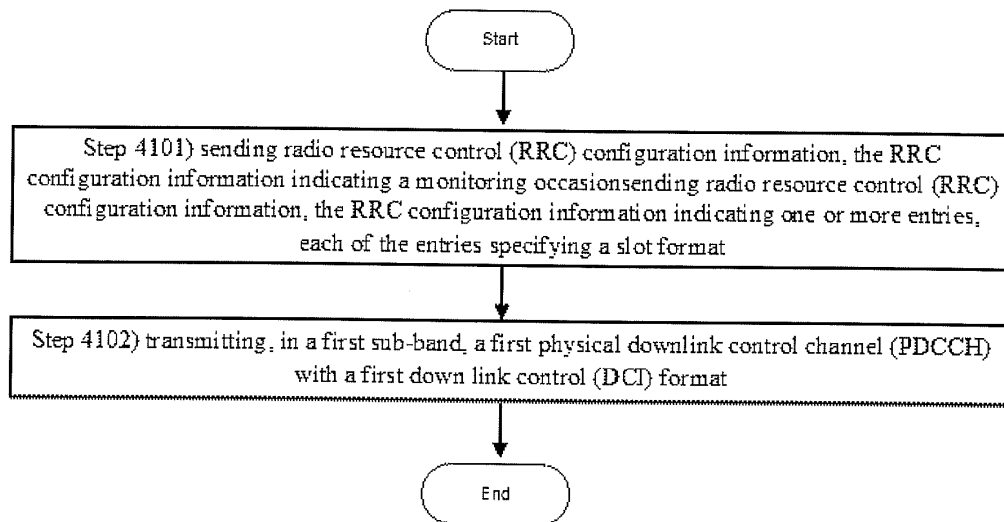


FIG. 41