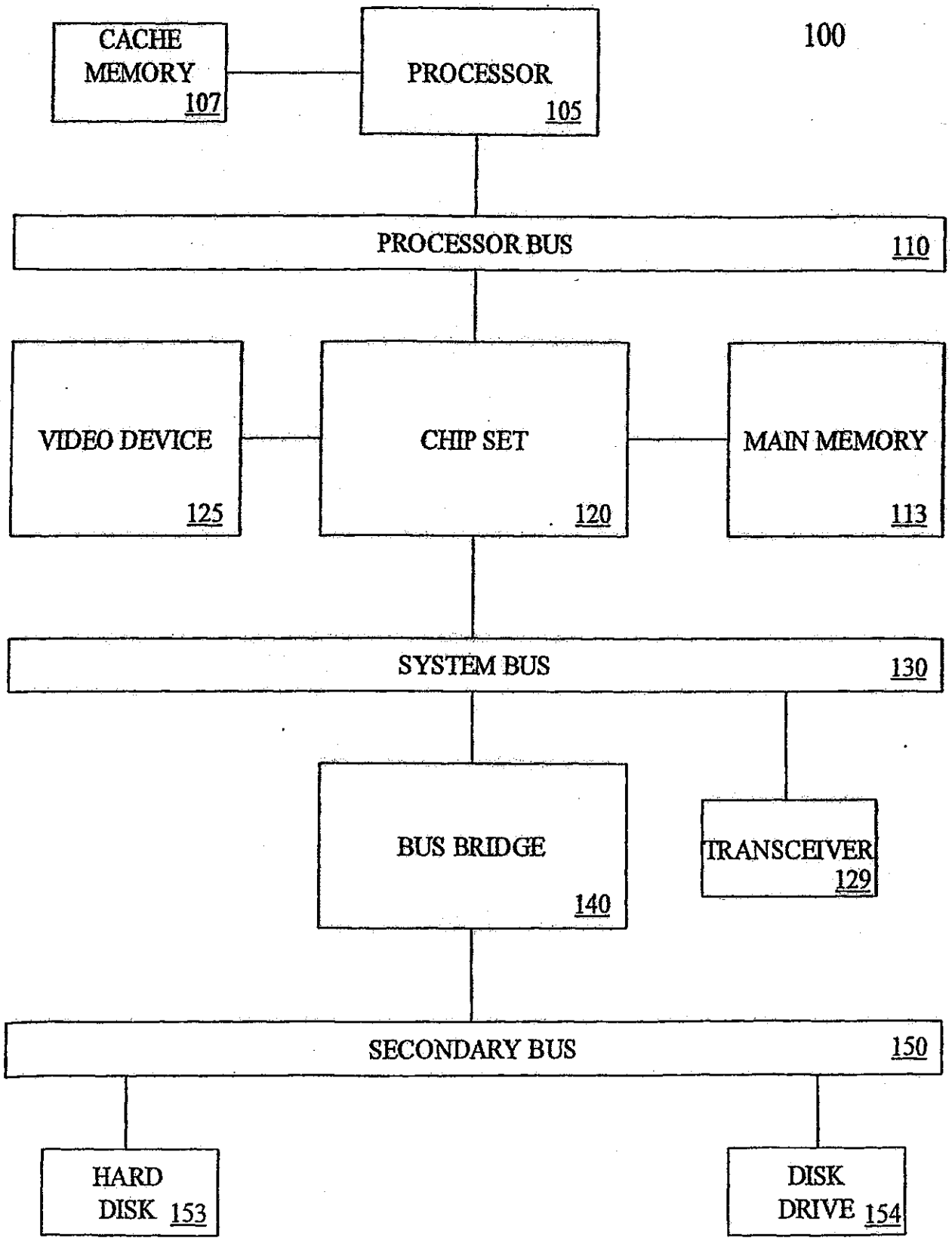

100
CACHE MEMORY 107
PROCESSOR 105
PROCESSOR BUS 110
VIDEO DEVICE 125
CHIP SET 120
MAIN MEMORY 113
SYSTEM BUS 130
BUS BRIDGE 140
TRANSCEIVER 129
SECONDARY BUS 150
HARD DISK 153
DISK DRIVE 154

FIGURE 1

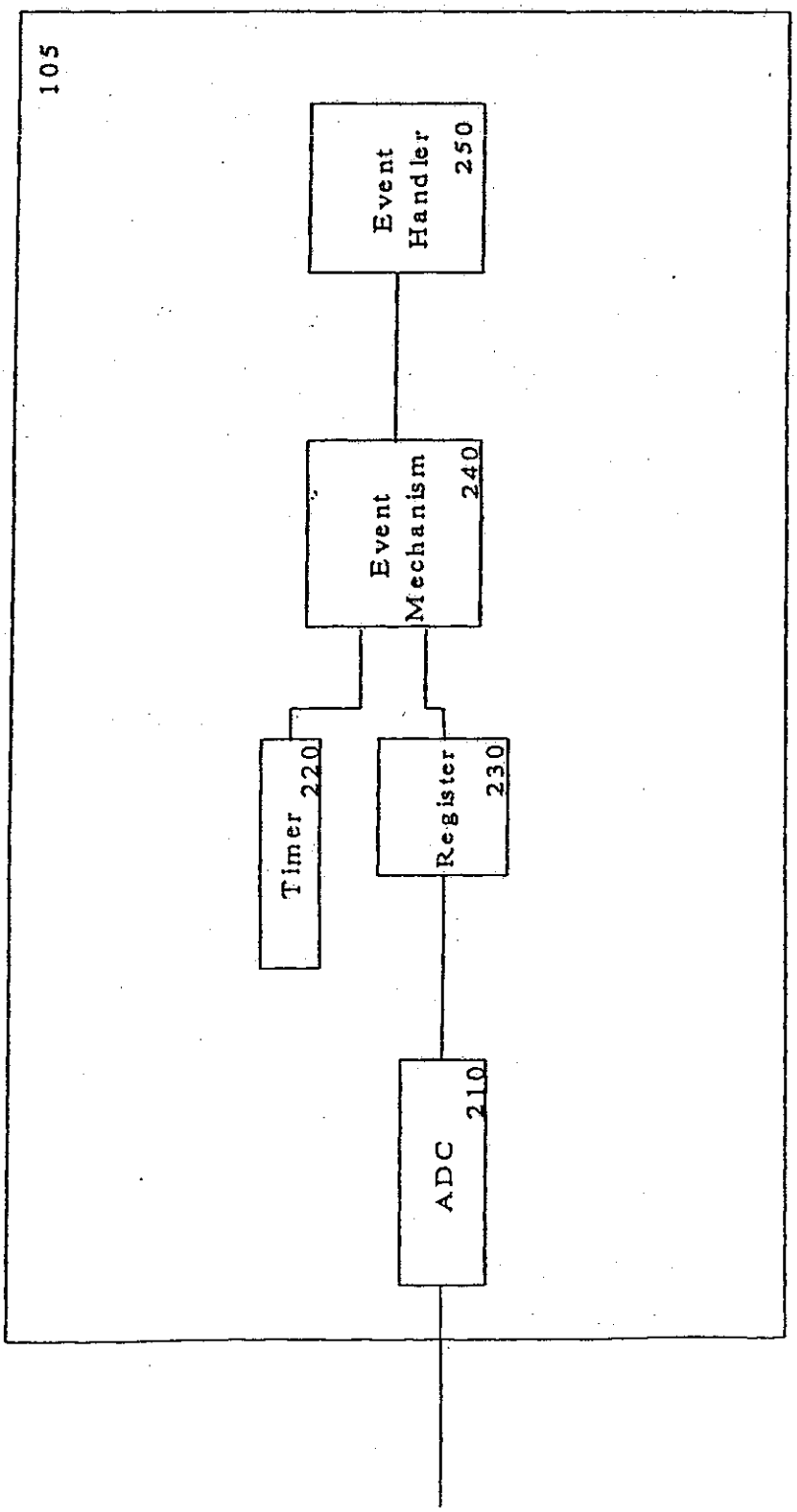

105
Event Handler 250
Event Mechanism 240
Timer 220
Register 230
ADC 210

FIGURE 2

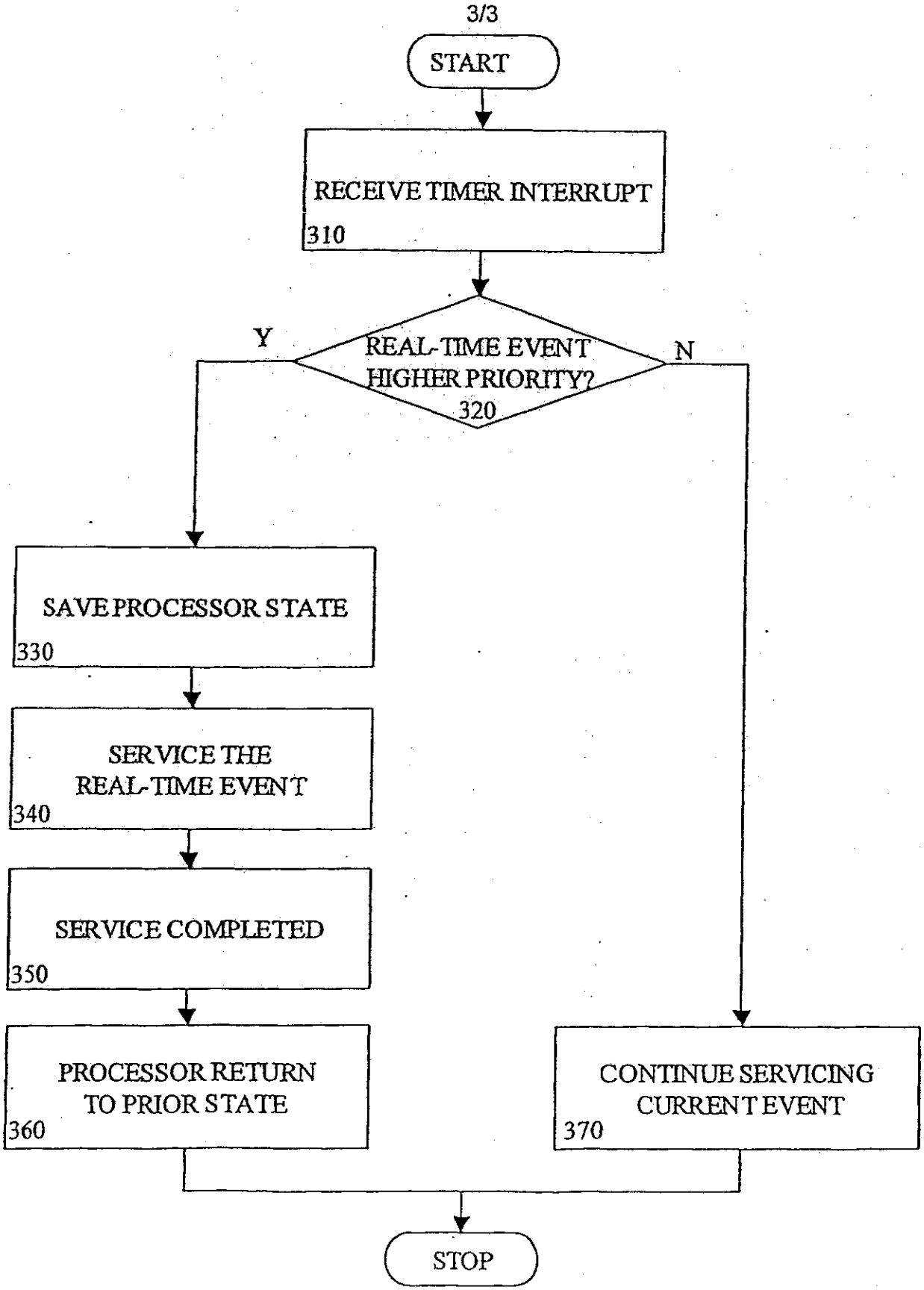

START
RECEIVE TIMER INTERRUPT
310
REAL-TIME EVENT HIGHER PRIORITY?
320
Y
N
SAVE PROCESSOR STATE
330
SERVICE THE REAL-TIME EVENT
340
SERVICE COMPLETED
350
PROCESSOR RETURN TO PRIOR STATE
360
CONTINUE SERVICING CURRENT EVENT
370
STOP

FIGURE 3