

ABSTRACT

**A SIMULATED CIRCUIT LAYOUT FOR LOW VOLTAGE, LOW POWER AND HIGH
PERFORMANCE TYPE II CURRENT CONVEYOR**

The present invention relates to a current conveyor circuit capable of operating at very low voltages, said circuit comprising: three LVCM's and four MOSFETS, wherein LVCM1 provides a constant bias current to flow through M3, if port X is kept open and the difference between the bias current and the injected current flows through M3 if a current is injected into port X, which gets reflected at port Z due to the action of LVCM1, M3 and M4, LVCM2 maintains the drain currents of M1 and M2 constant, and LVCM3 maintains a constant tail current in the circuit.

(FIGURE 1 IS SUGGESTED FOR PUBLICATION)