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- (71) Applicant: INTEL CORPORATION [US/US]; 2625 Walsh Avenue, Santa Clara, CA 95051 (US).
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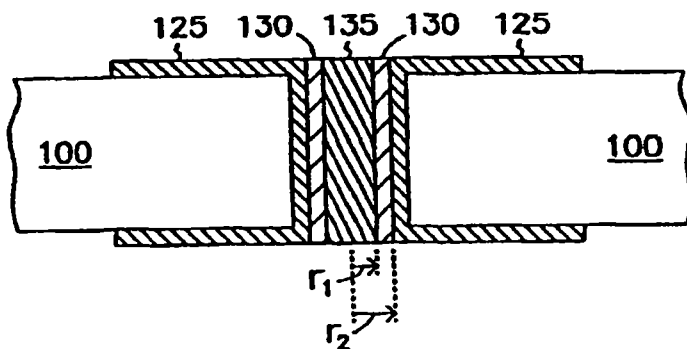


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ACTION

(54) Title: SELF-ALIGNED COAXIAL VIA CAPACITORS



(57) Abstract: The various embodiments of coaxial capacitors are self-aligned and formed in a via, including blind vias, buried vias and plated through holes. The coaxial capacitors are adapted to utilize the plating (125) of a plated via as a first electrode. The dielectric layer (130) is formed to overlie the first electrode (125) while leaving a portion of the via unfilled. A second electrode (135) is formed in the portion of the via left unfilled by the dielectric layer (130). Such coaxial capacitors are suited for use in decoupling and power dampening applications to reduce signal and power noise and/or reduce power overshoot and droop in electronic devices.

For such applications, it is generally expected that a plurality of coaxial capacitors, often numbering in the thousands, will be coupled in parallel in order to achieve the desired level of capacitance.

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