

FAST SWITCHING SMARTFET

ABSTRACT OF THE DISCLOSURE

In a power integrated device having temperature monitoring circuits powered from input signal pulses, an overtemperature signal is memorized in a small capacitor (30 picofarads) to guarantee that the overtemperature signal will not be lost between two input pulses at a frequency greater than about 16 kHz. A larger charge storage capacitor (100 picofarads) is also added to store the input voltage V_{CC} needed to power the overtemperature circuits.

This Sheet Has Been Replaced

