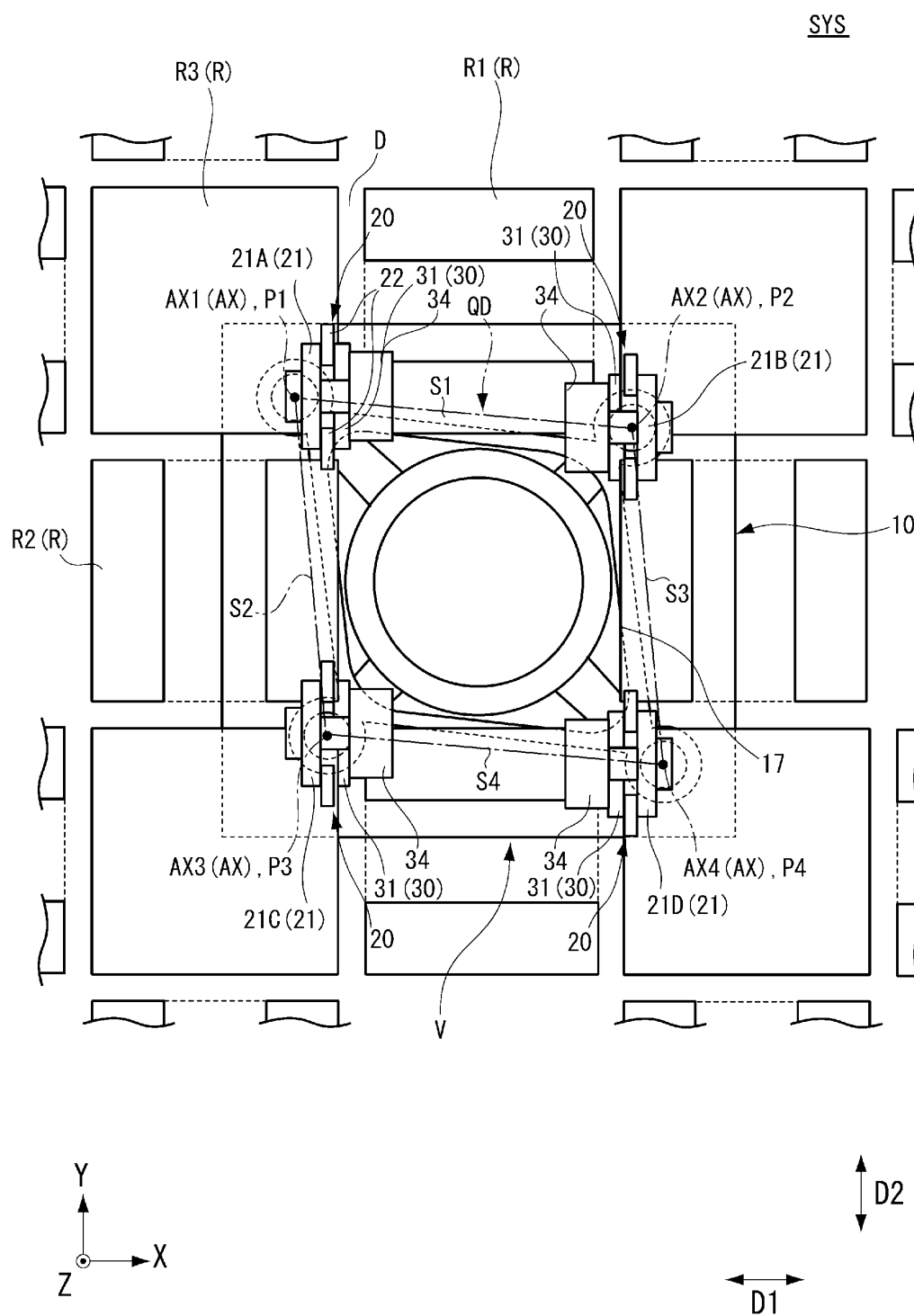
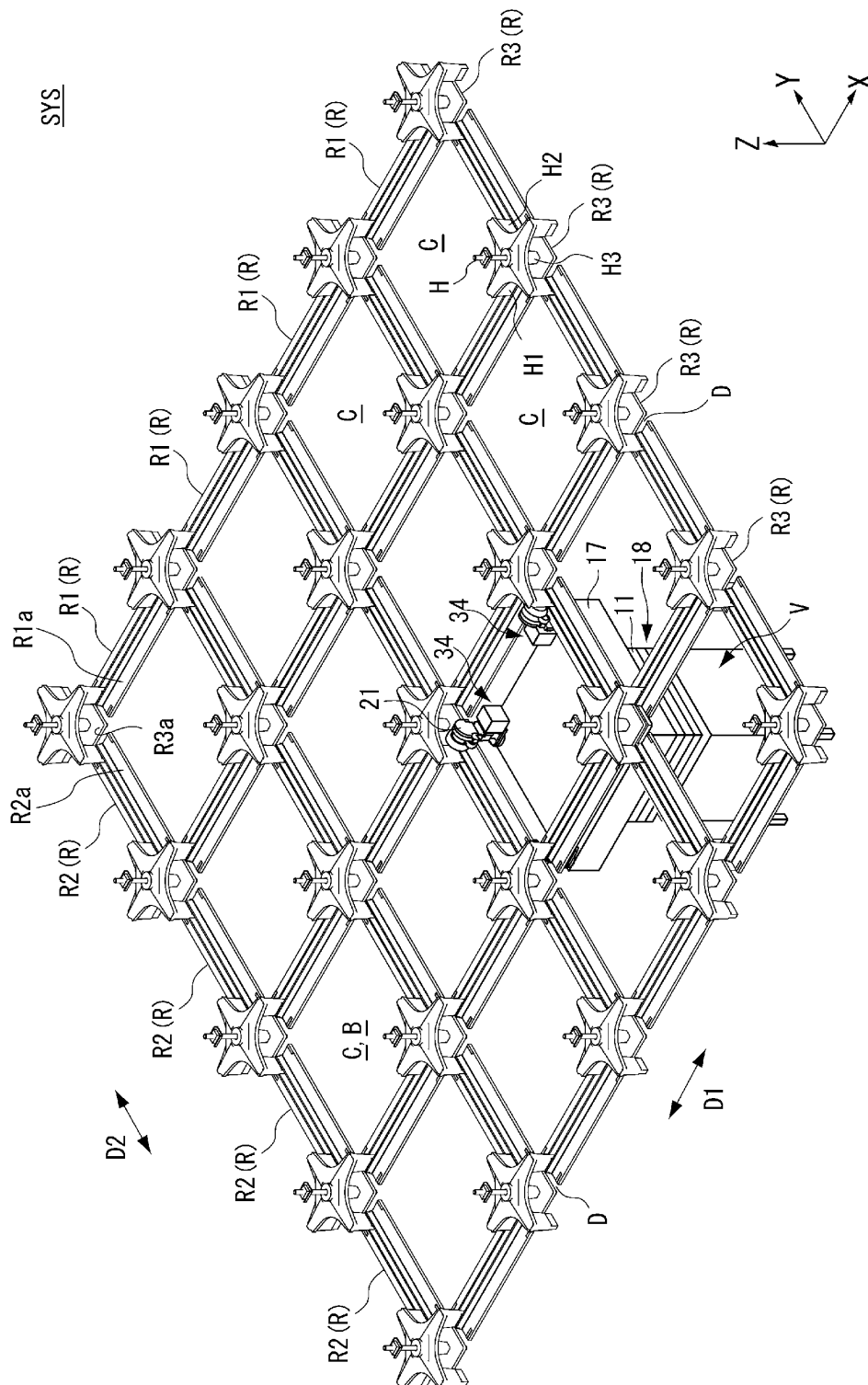


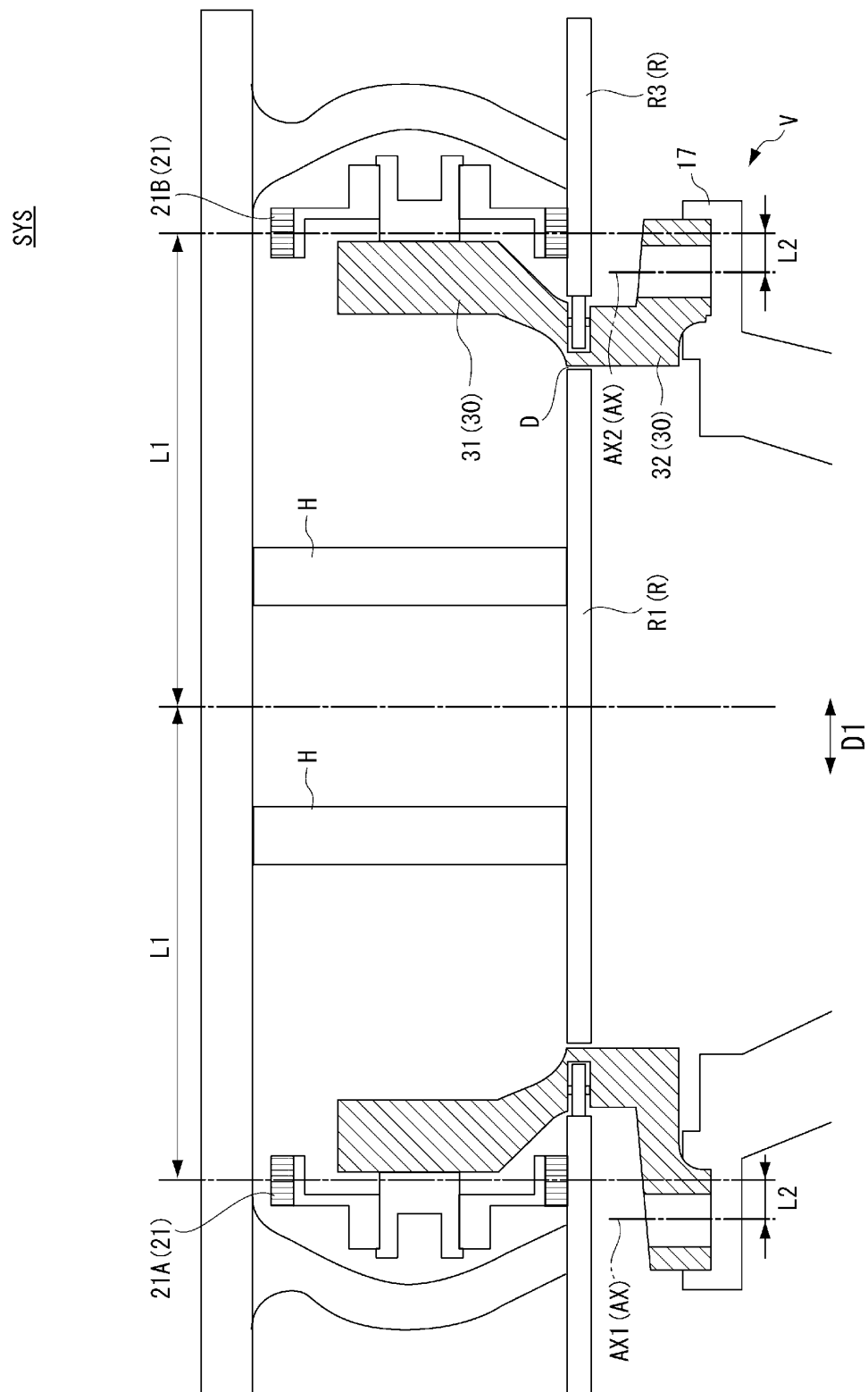
[図1]



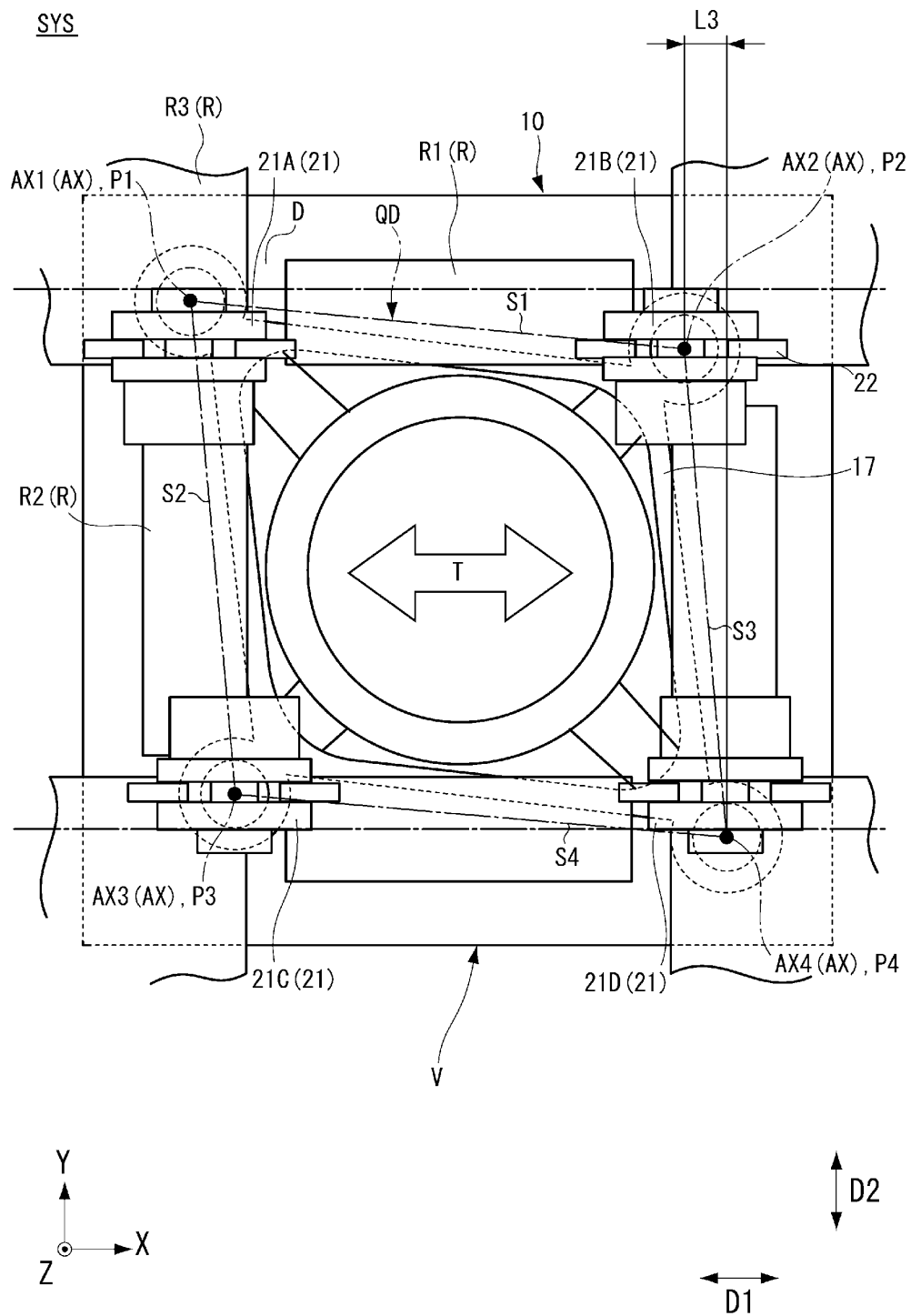
[図2]



[図3]

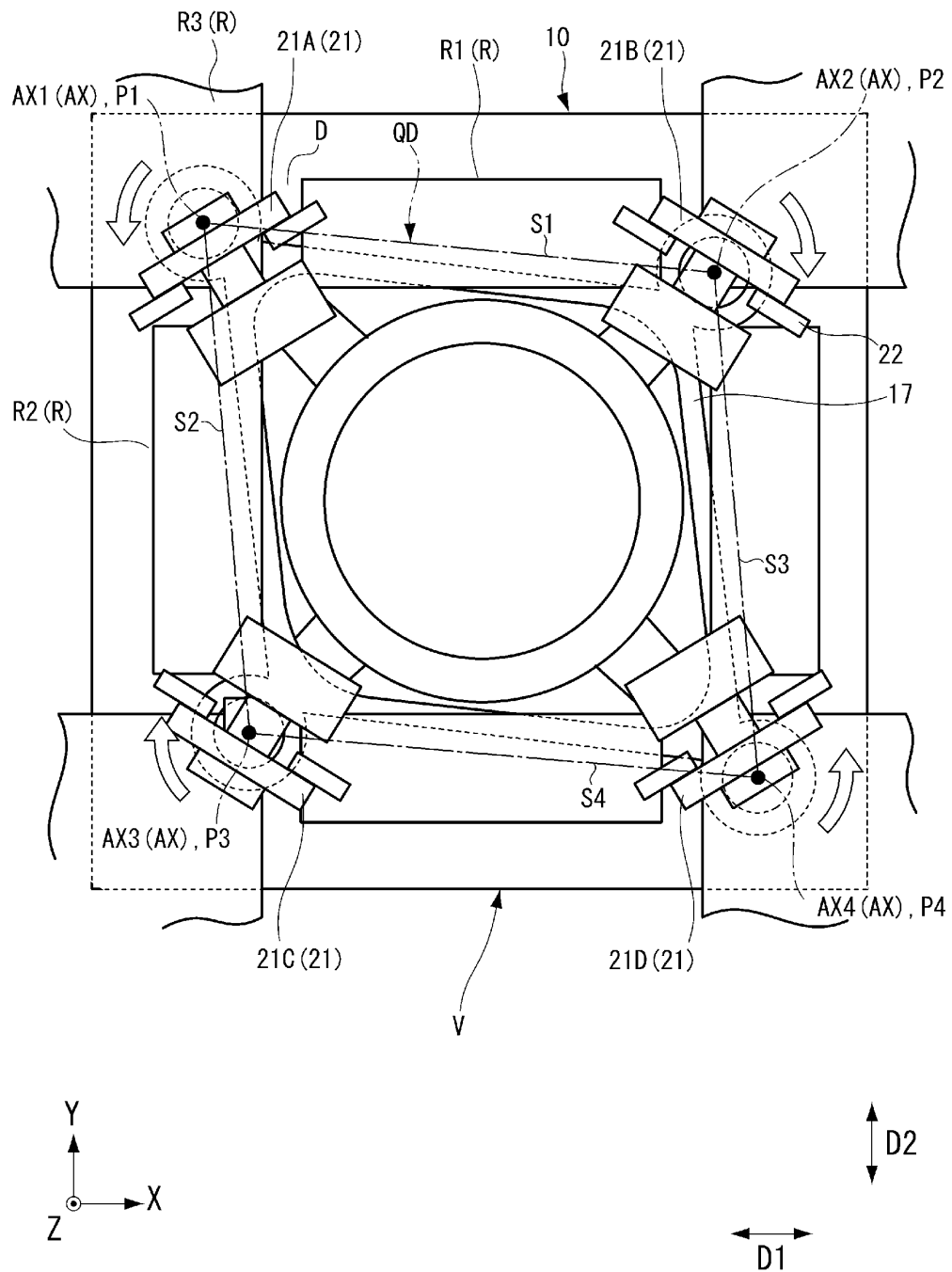


[図4]



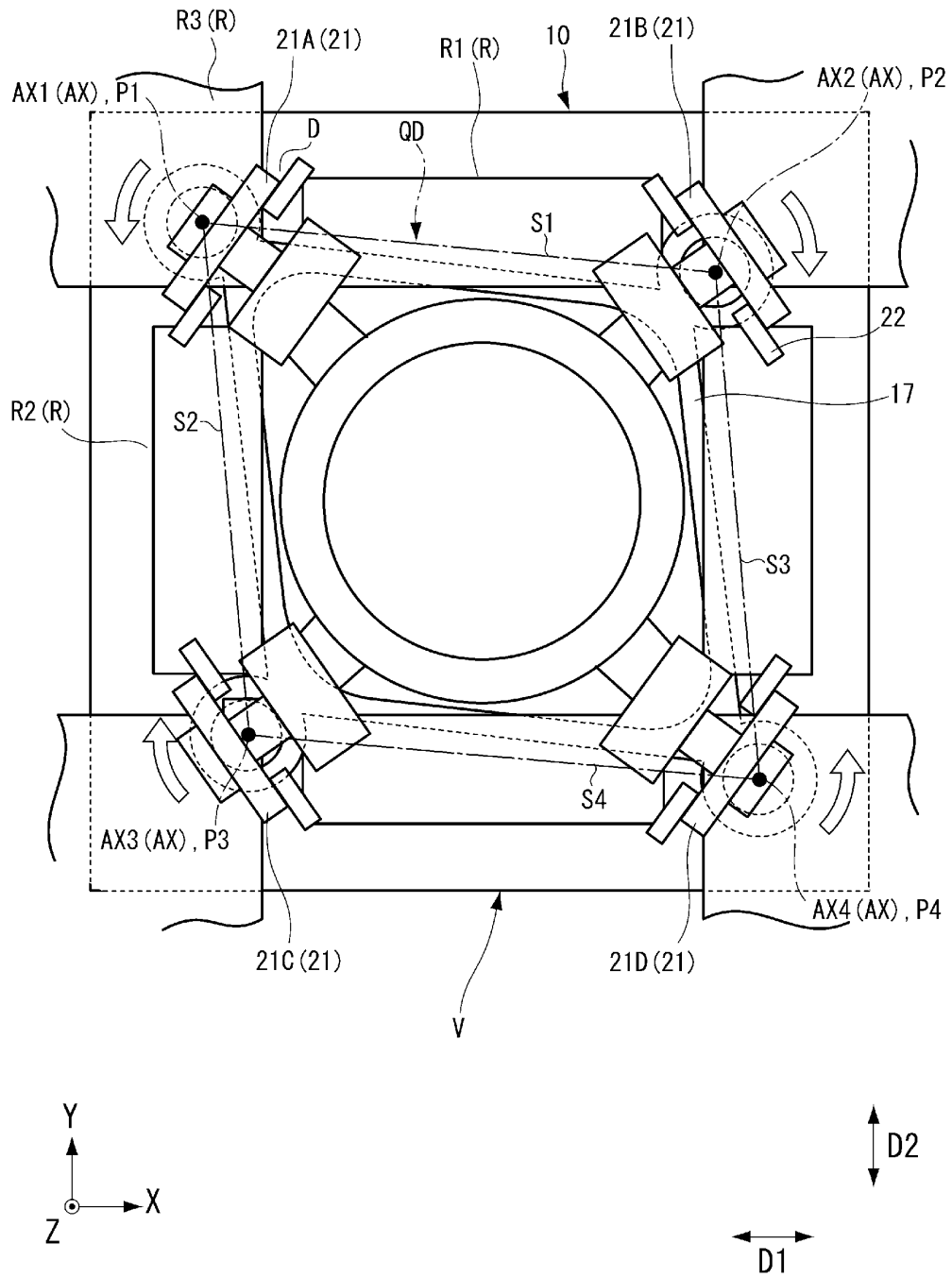
[図5]

SYS



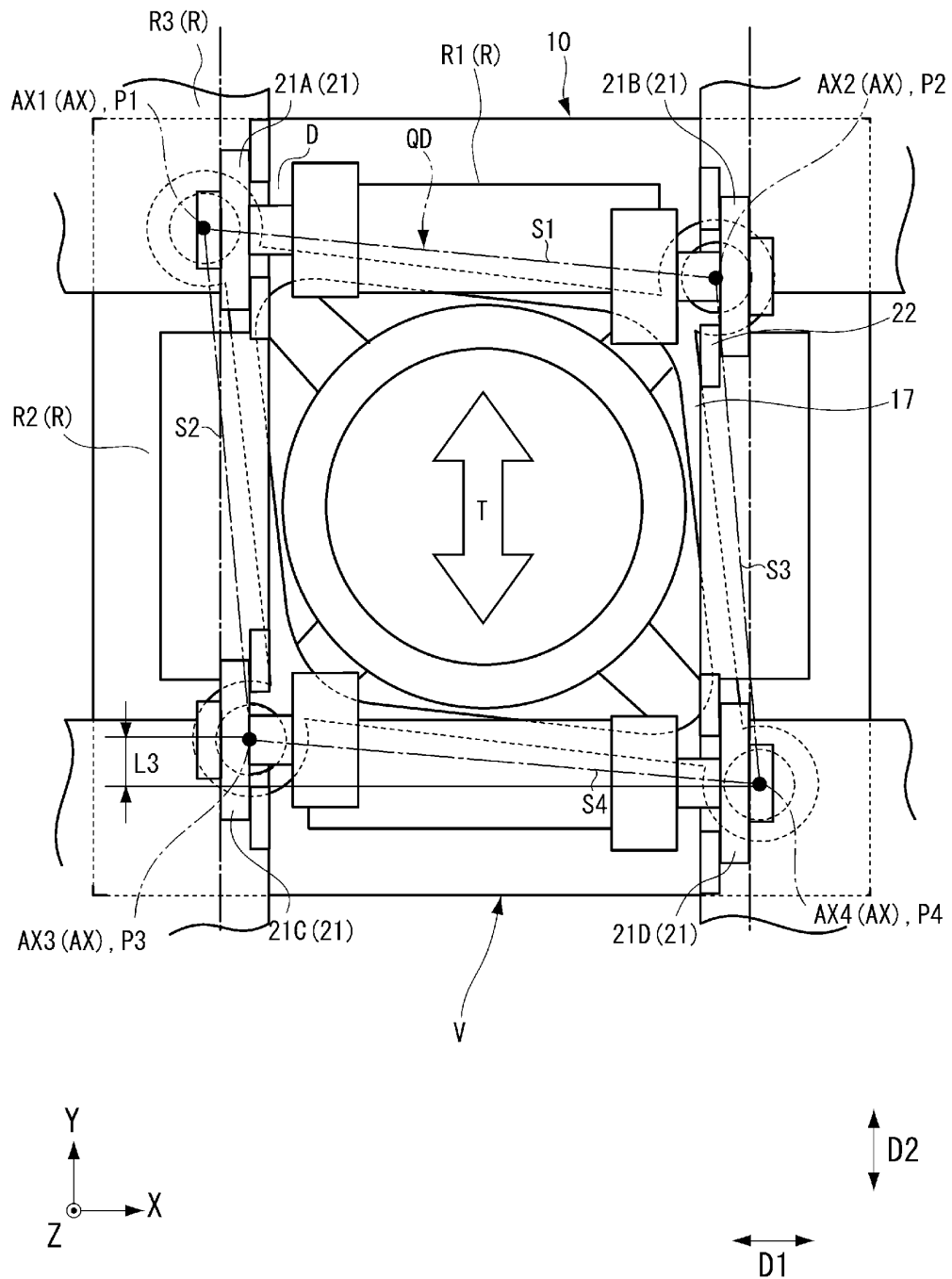
[図6]

SYS

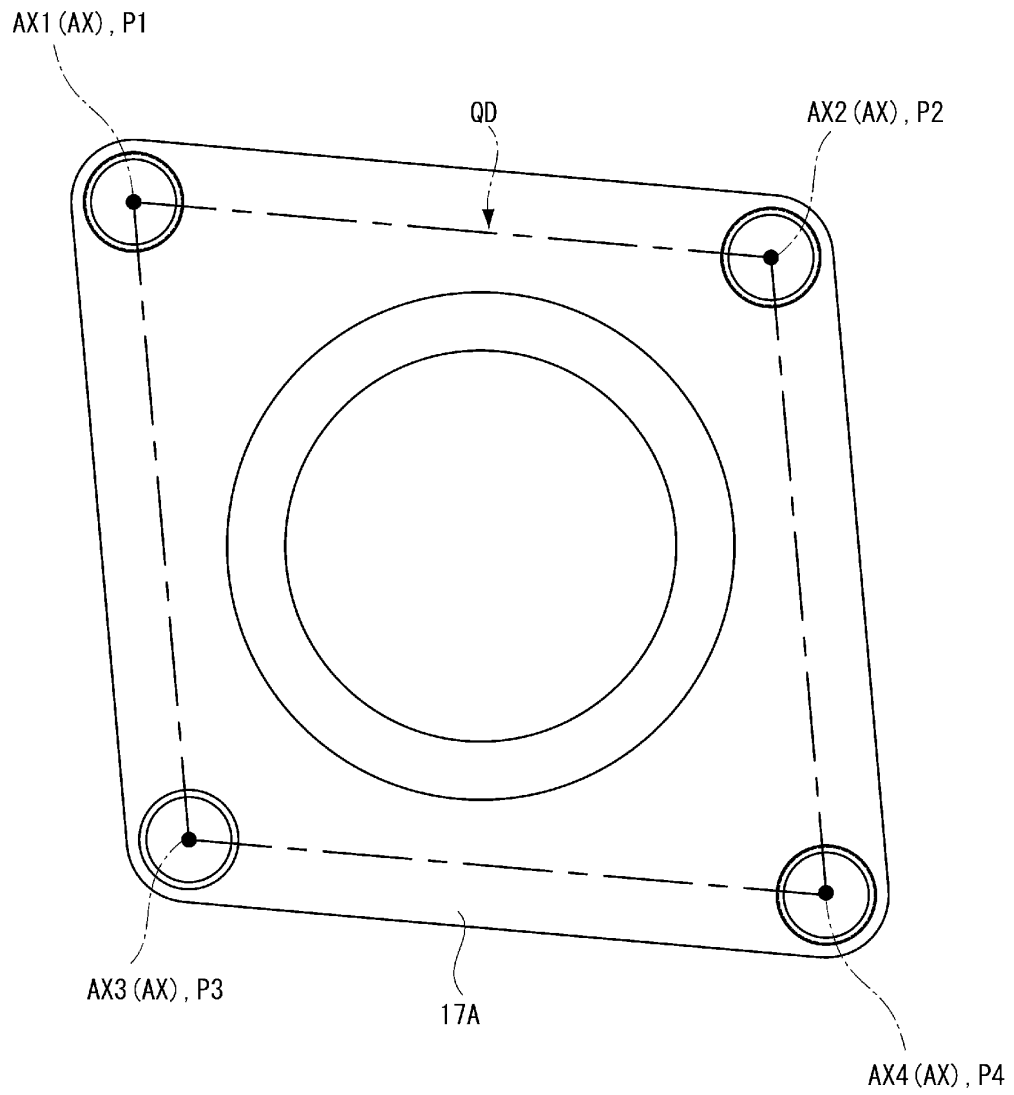


[図7]

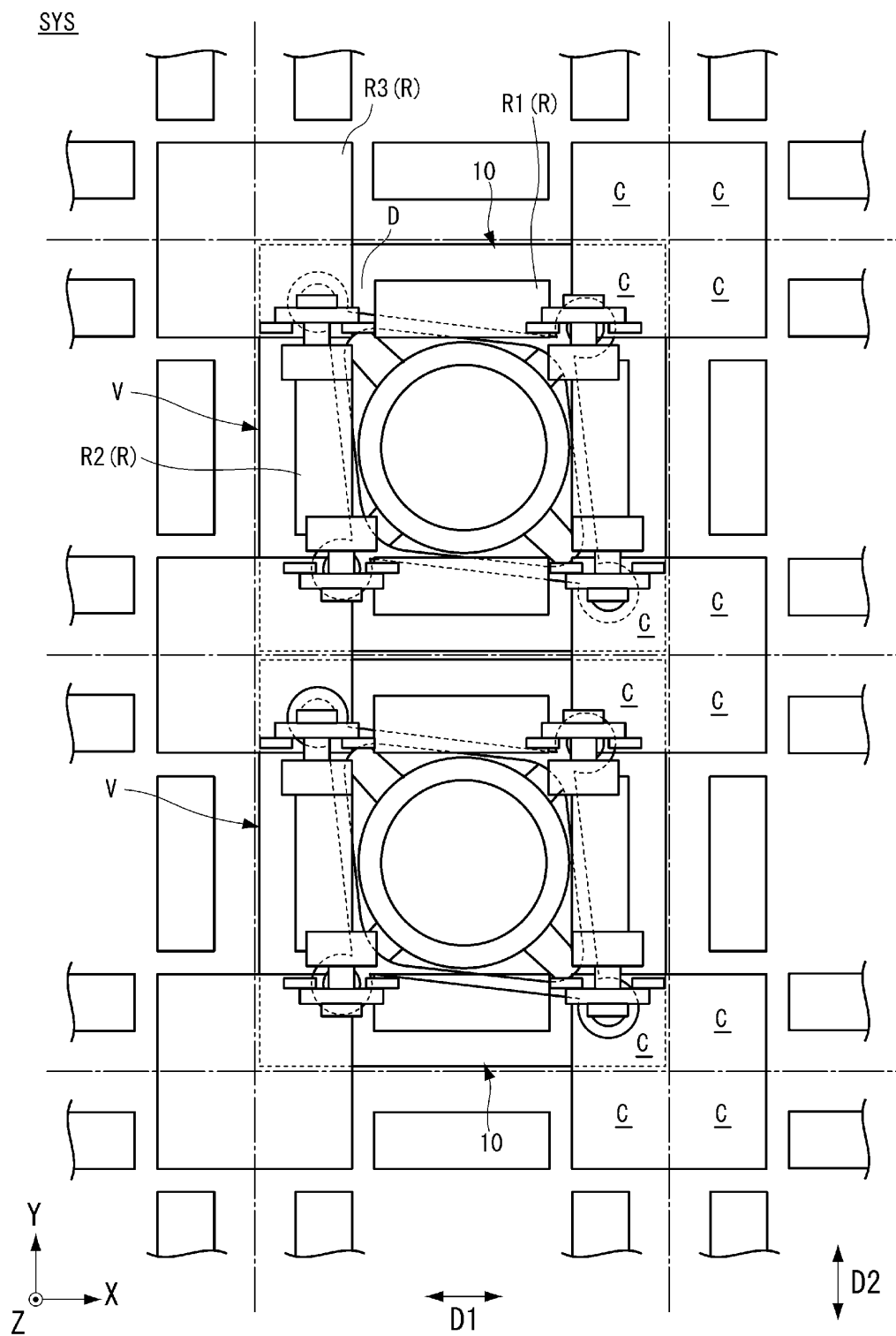
SYS



[図8]

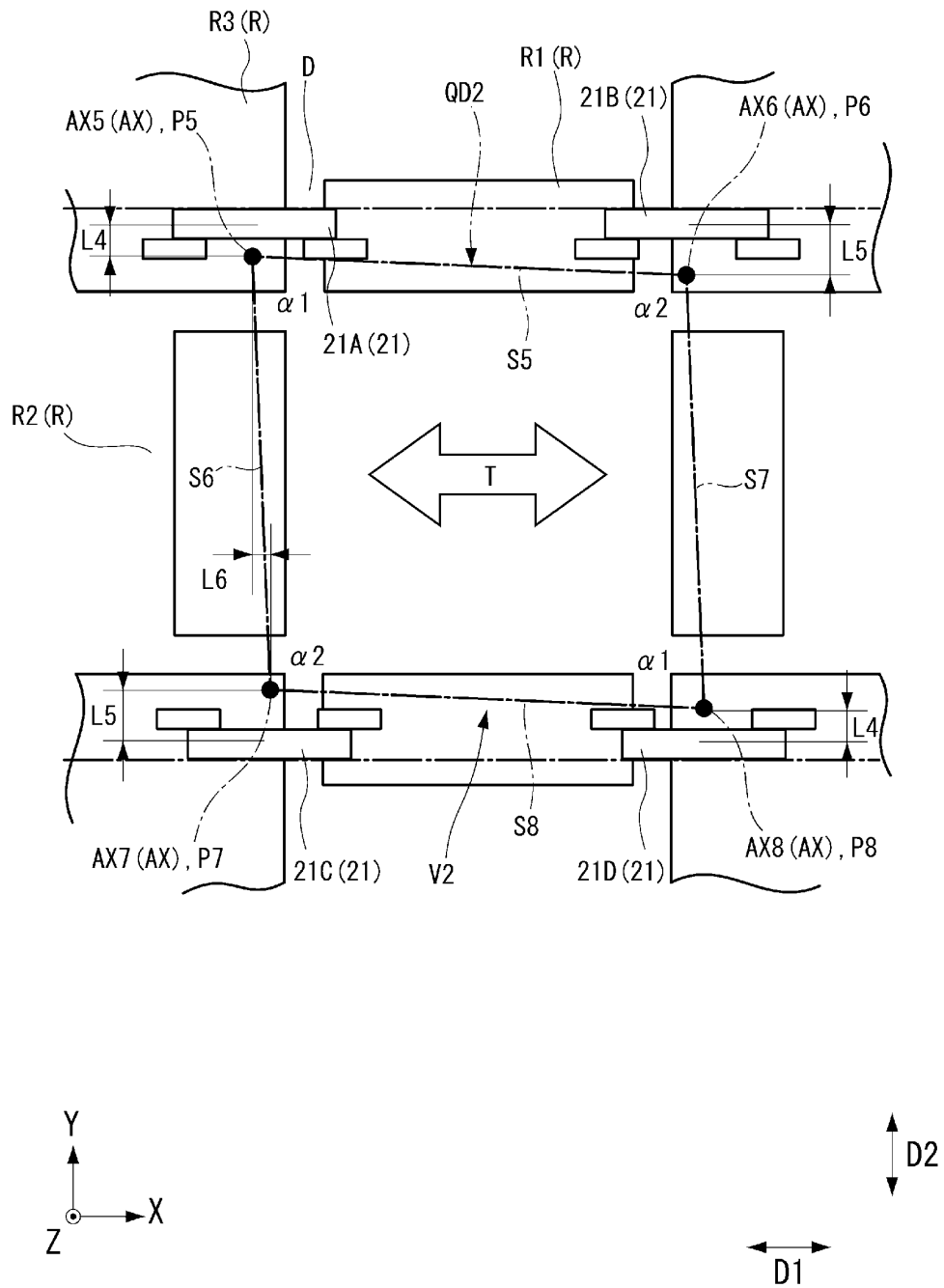


[図9]



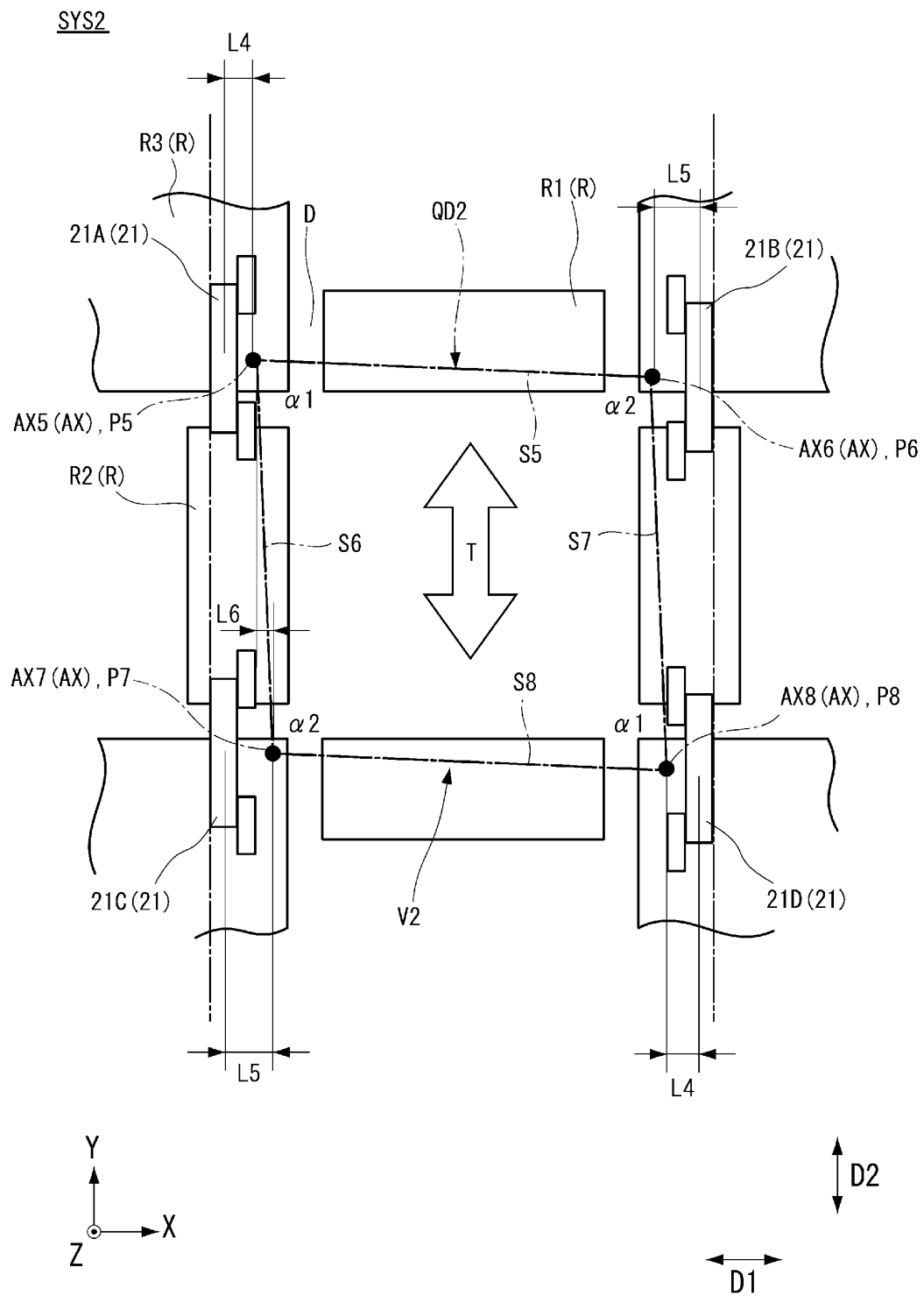
[図10]

SYS2



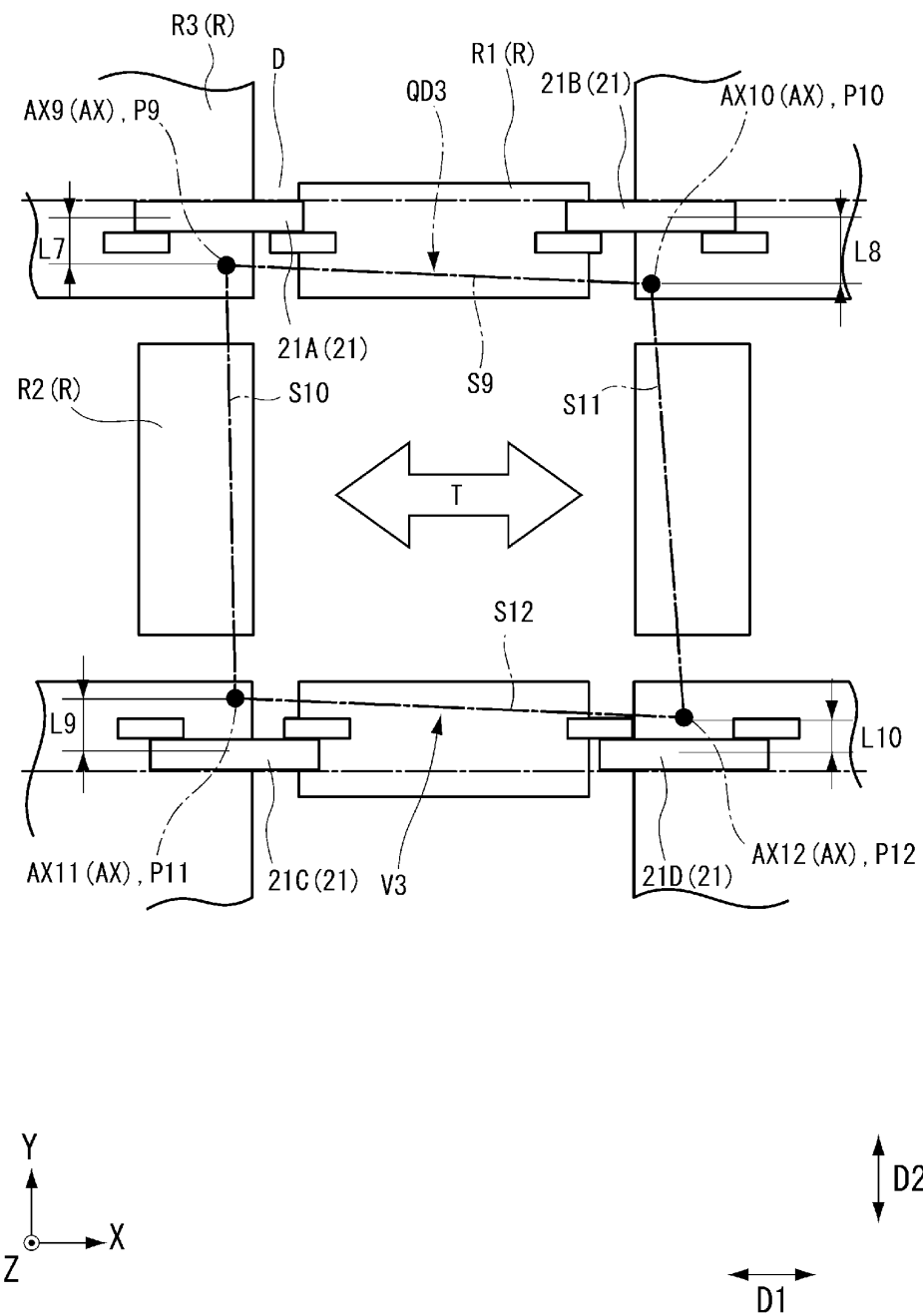


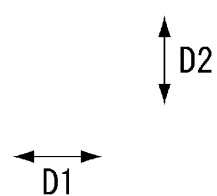
[図12]



[図13]

SYS3



SYS3

SYS3

