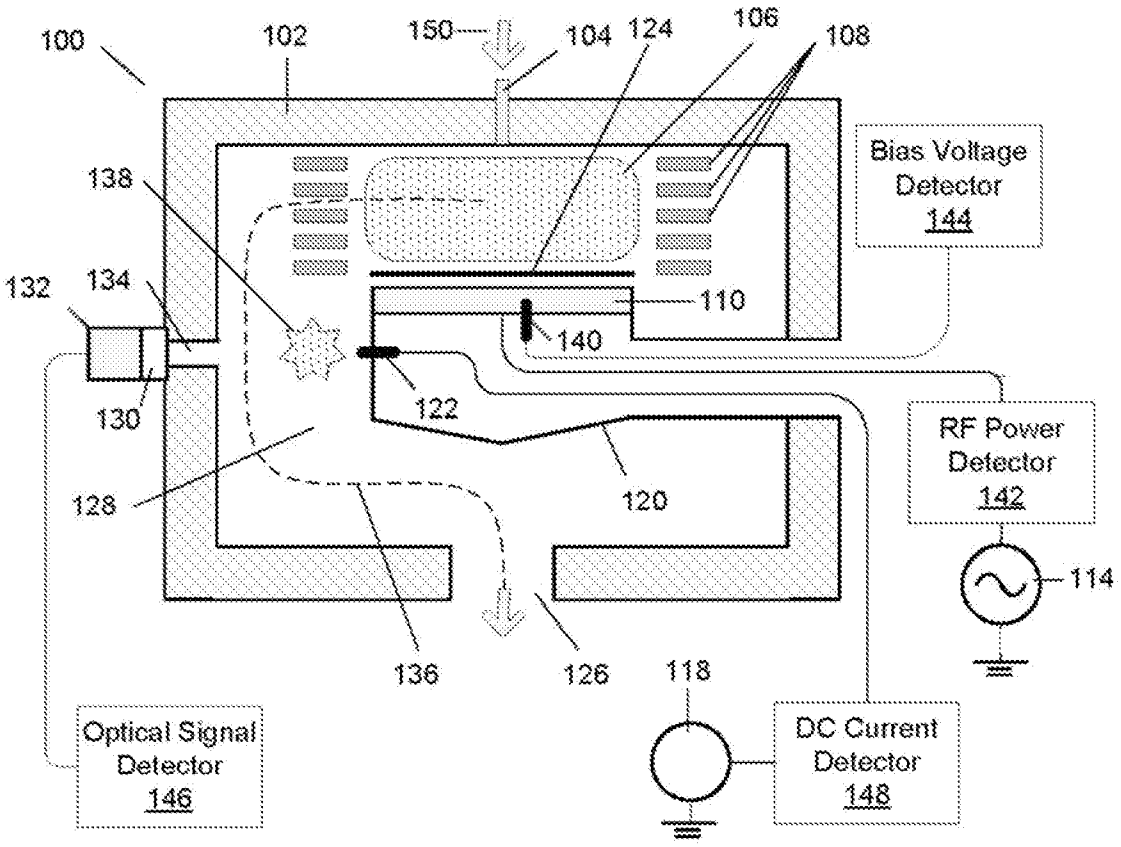


Fig. 1 (PRIOR ART)

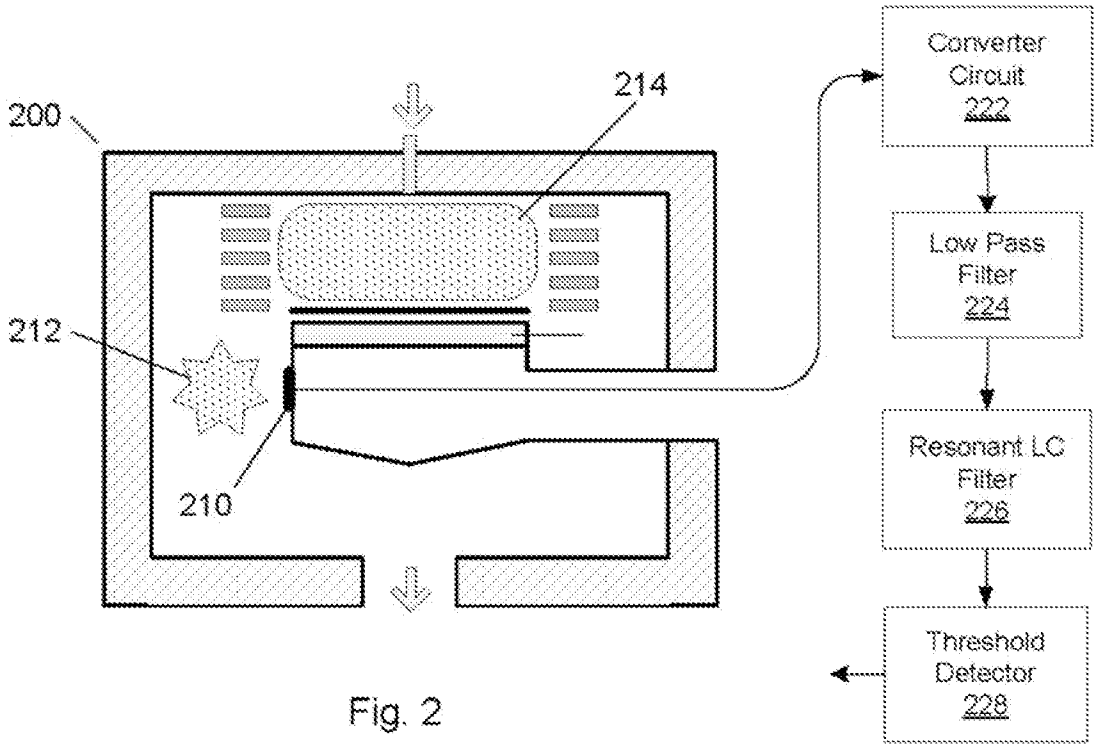

200
214
212
210
Converter Circuit 222
Low Pass Filter 224
Resonant LC Filter 226
Threshold Detector 228

Fig. 2

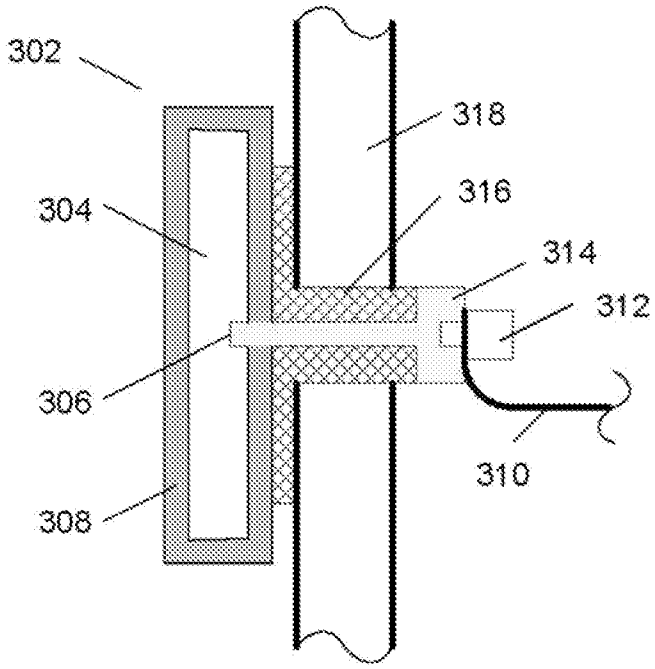


Fig. 3A

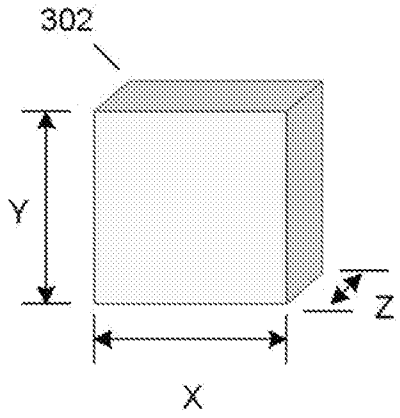


Fig. 3B

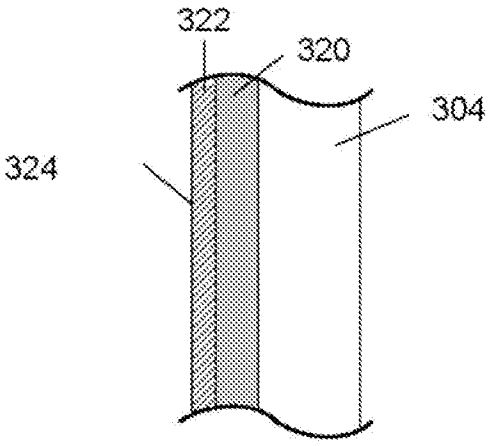


Fig. 3C

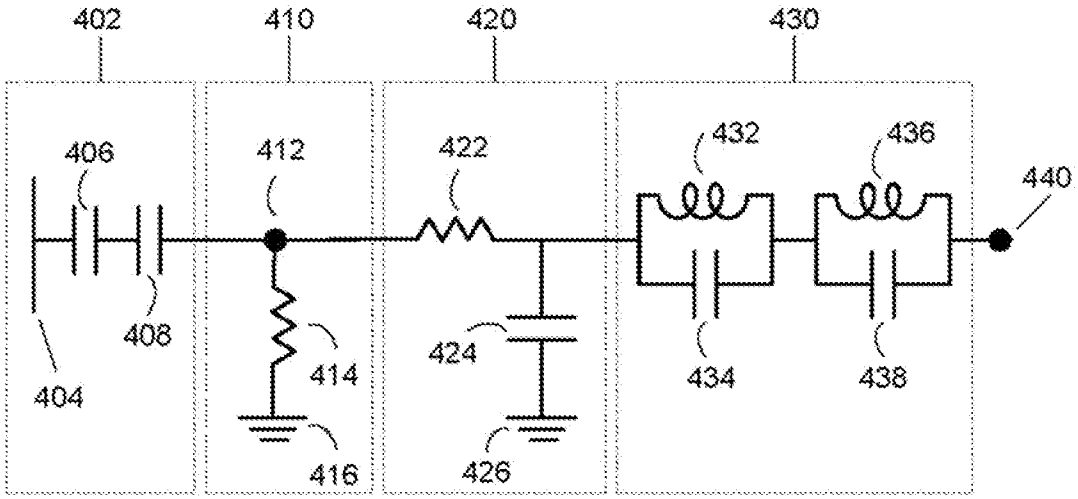

402
410
420
430
406
412
422
432
436
440
408
414
424
434
438
404
416
426

Fig. 4A

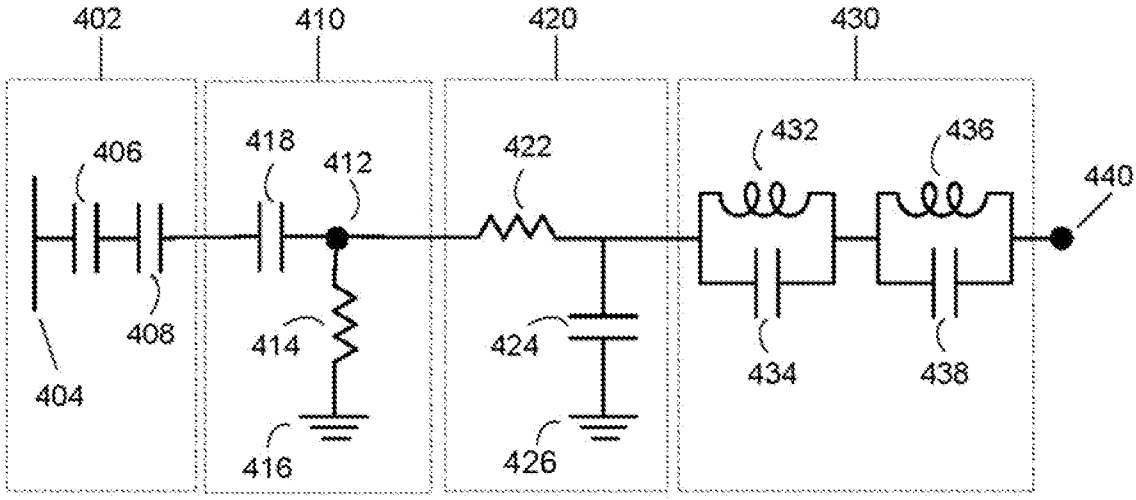

402
410
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418
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422
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436
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424
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404
416
426

Fig. 4B

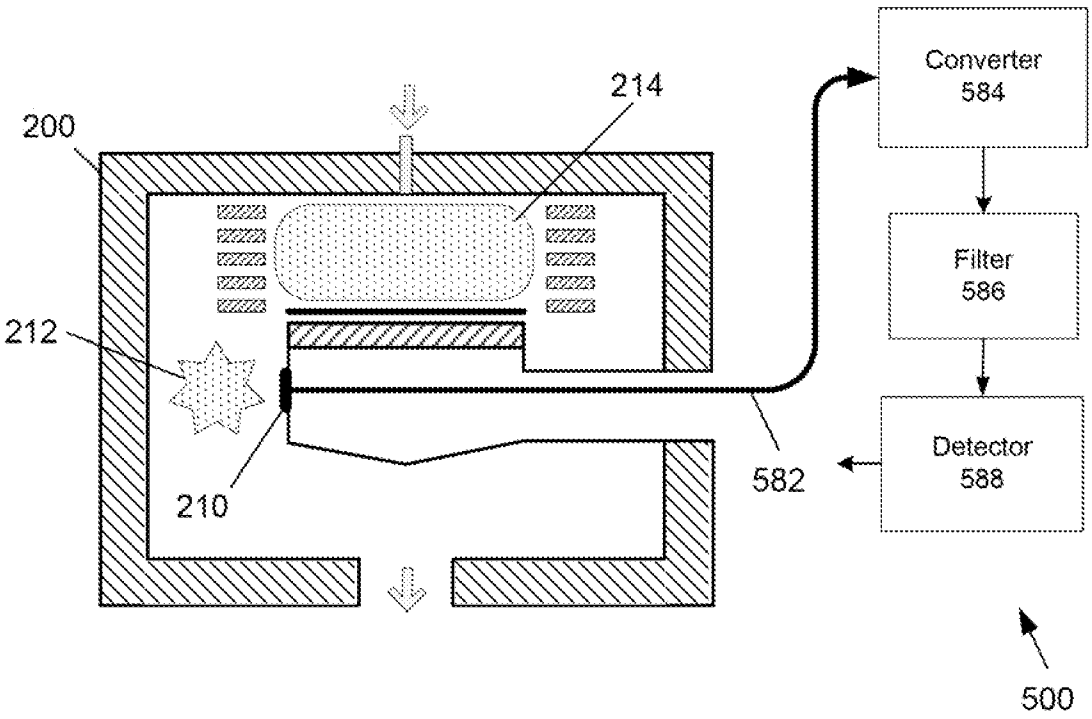

Converter
584
214
200
Filter
586
212
Detector
588
582
210
500

Fig. 5

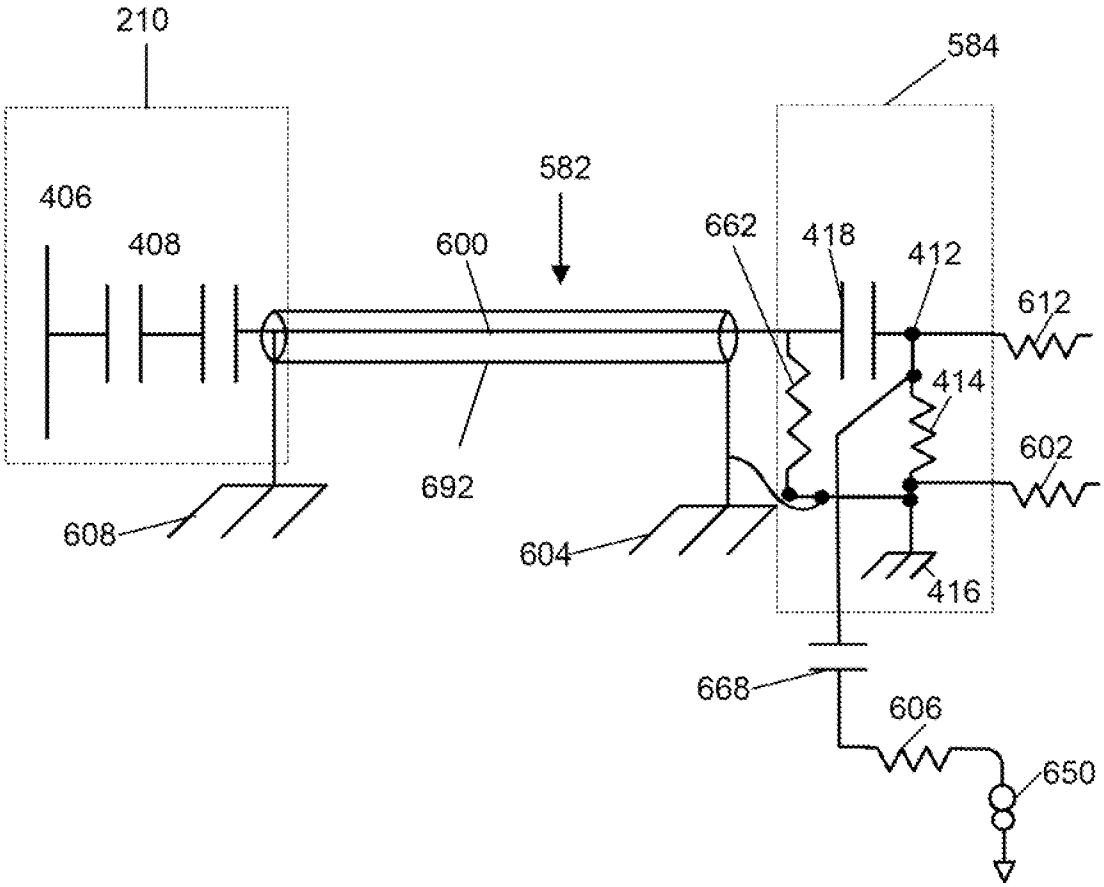

210
584
406
408
582
600
662
418
412
612
414
602
692
608
604
416
668
606
650

Fig. 6

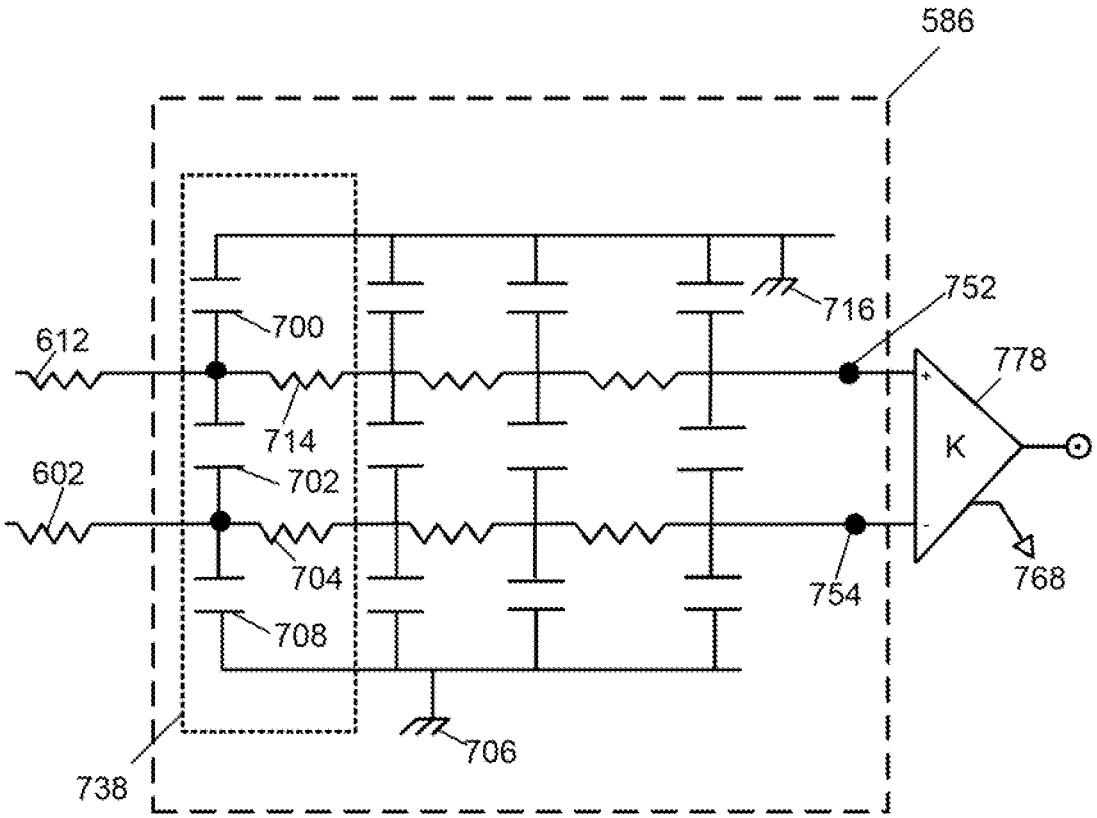


Fig. 7

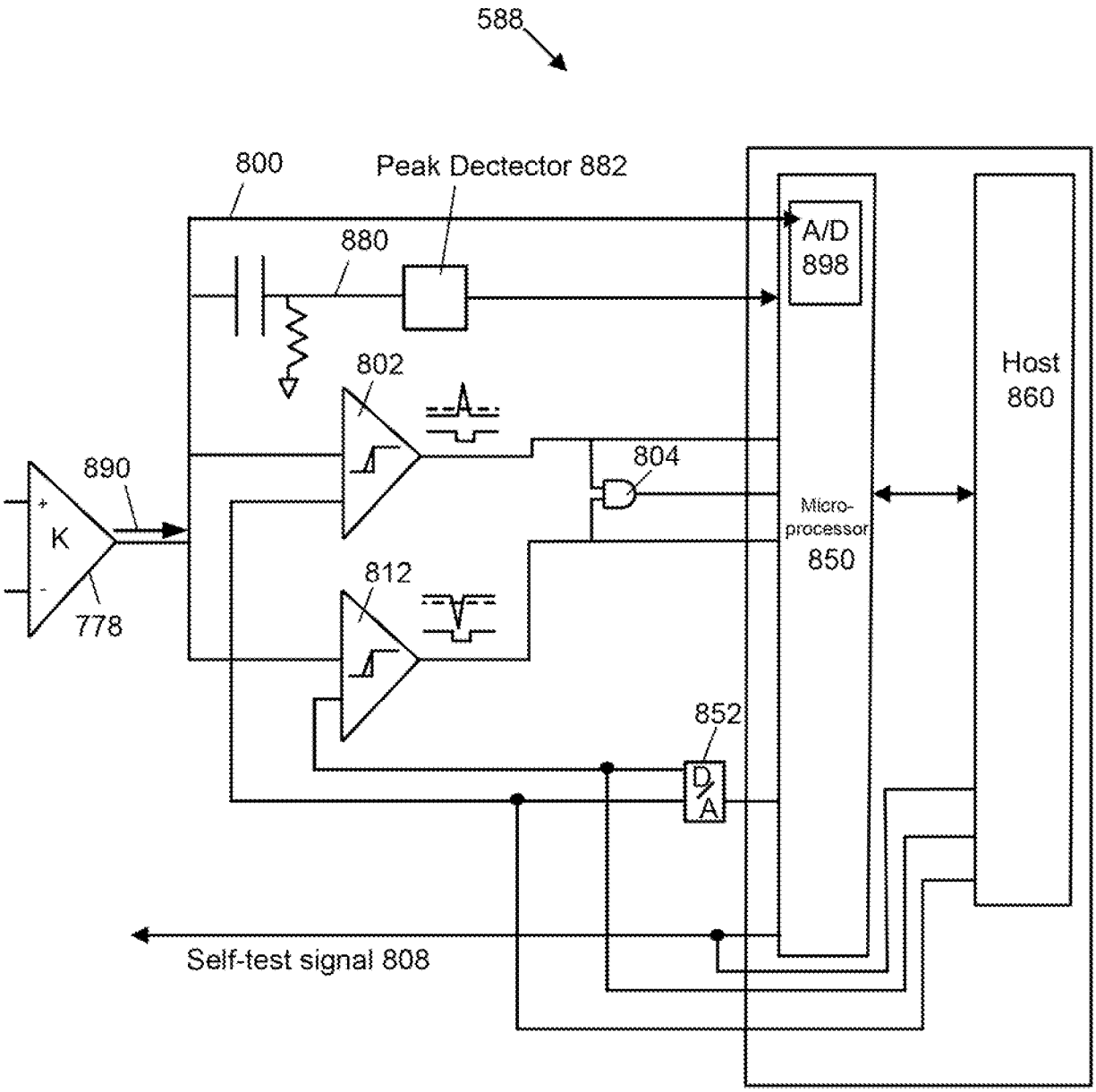

588
800
Peak Dectector 882
880
A/D
898
802
Host
860
890
804
K
Micro-
processor
850
812
778
852
D
A
Self-test signal 808

Fig. 8