

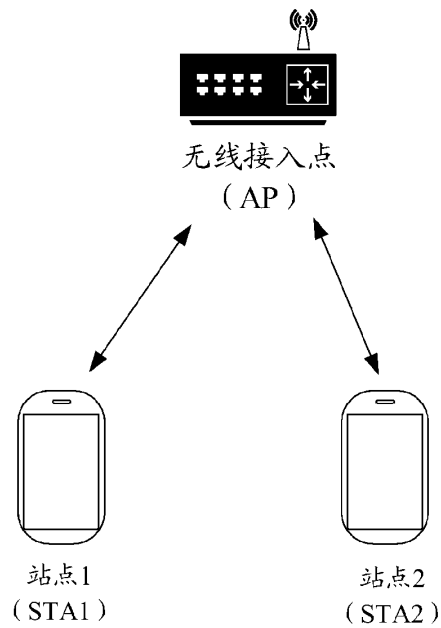


图 1

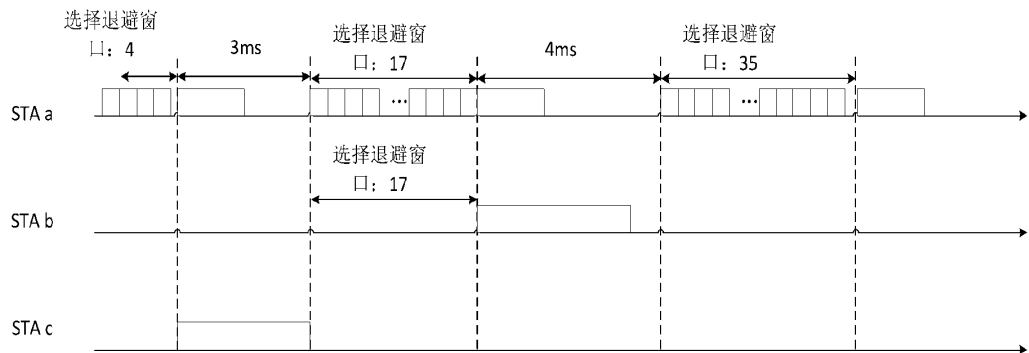


图 2A

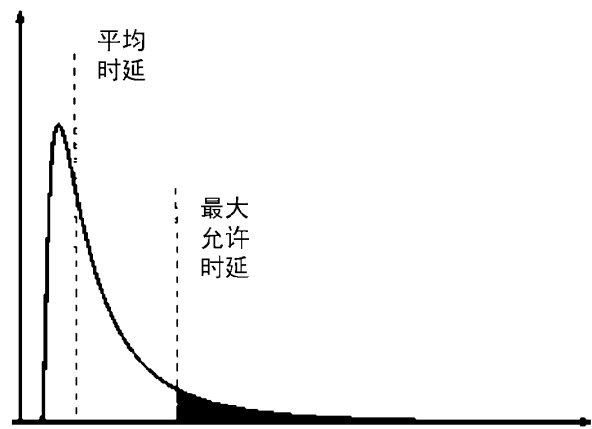


图 2B

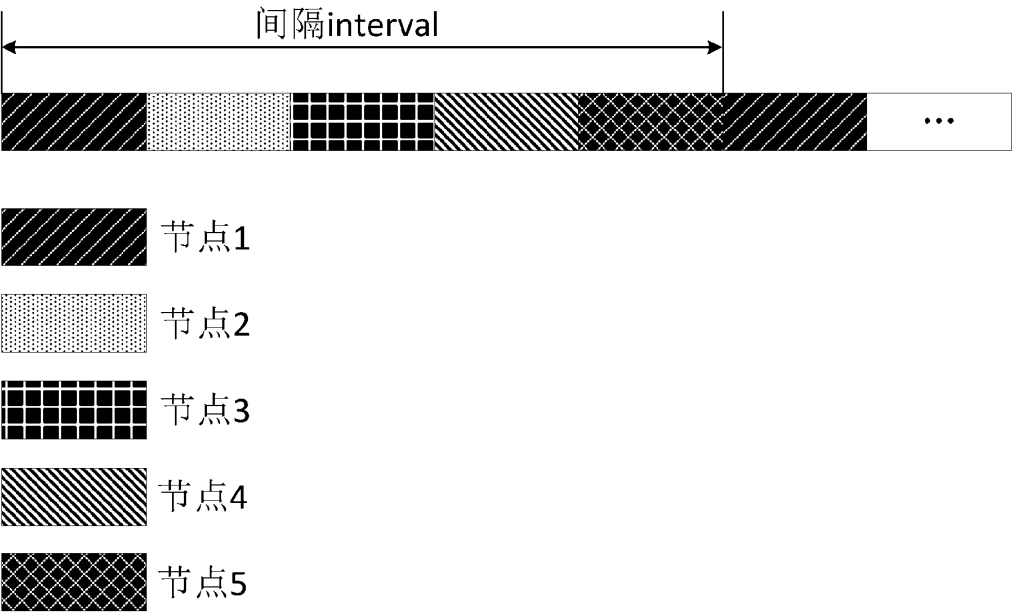


图 3

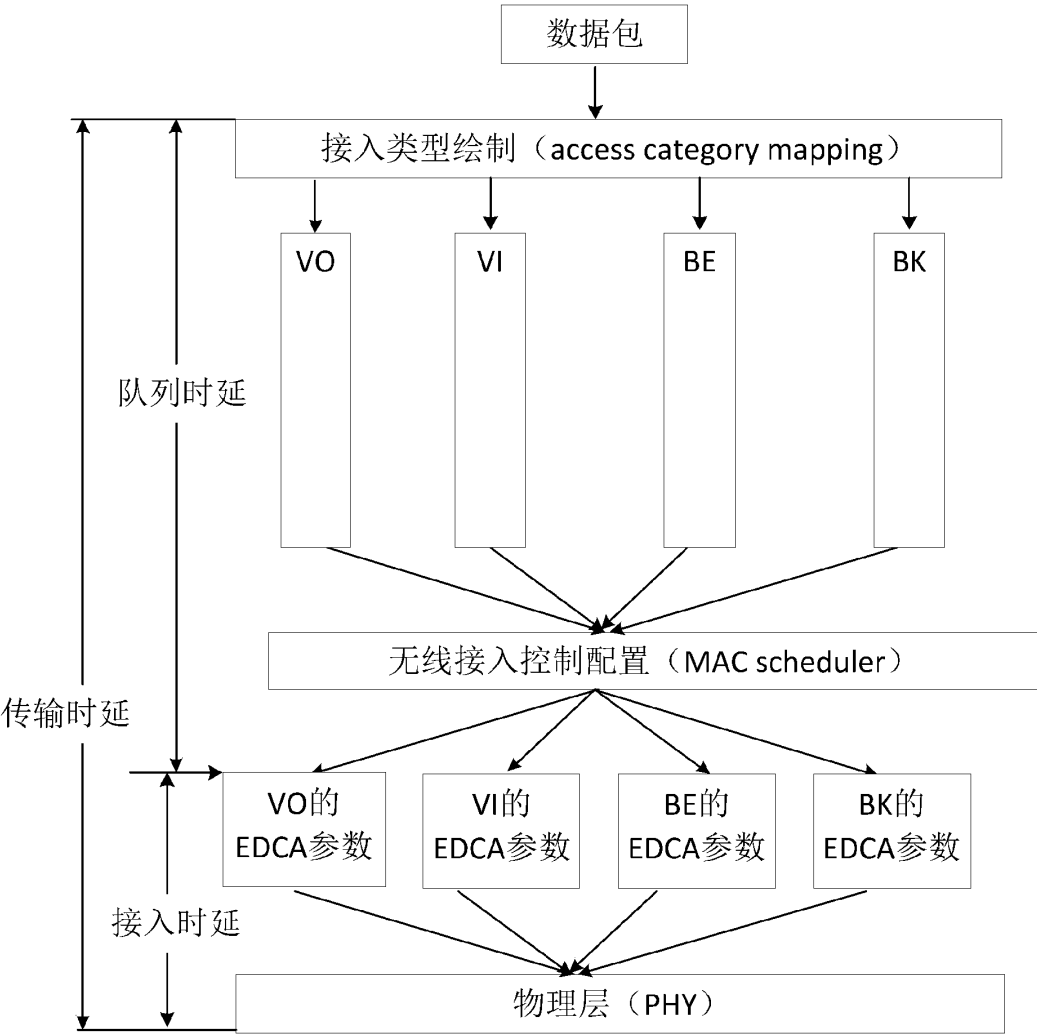


图 4A

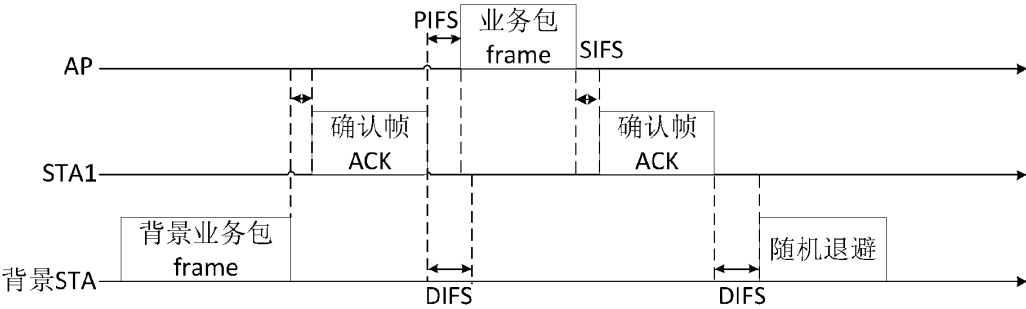


图 4B

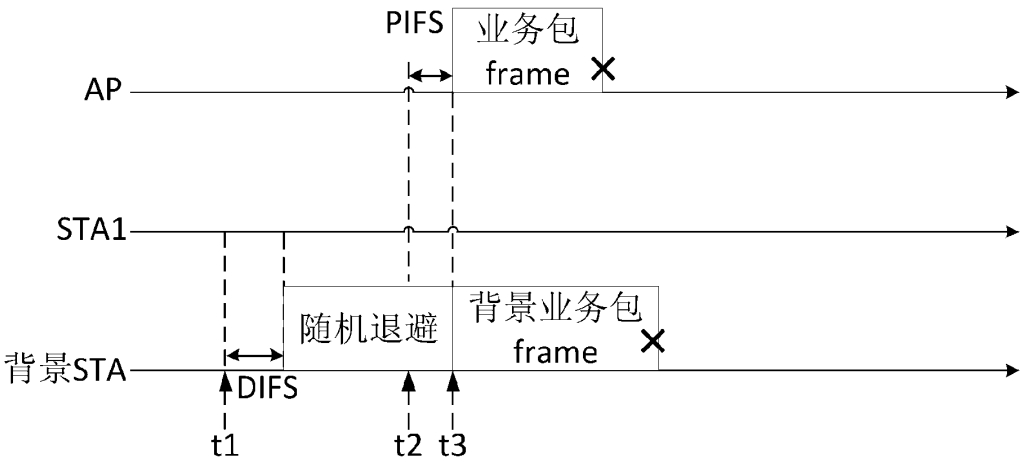


图 5

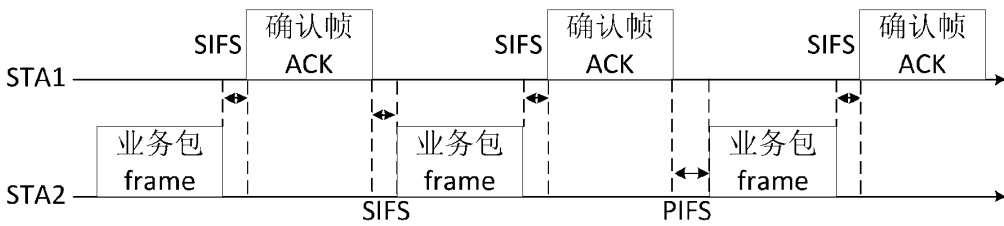


图 6A

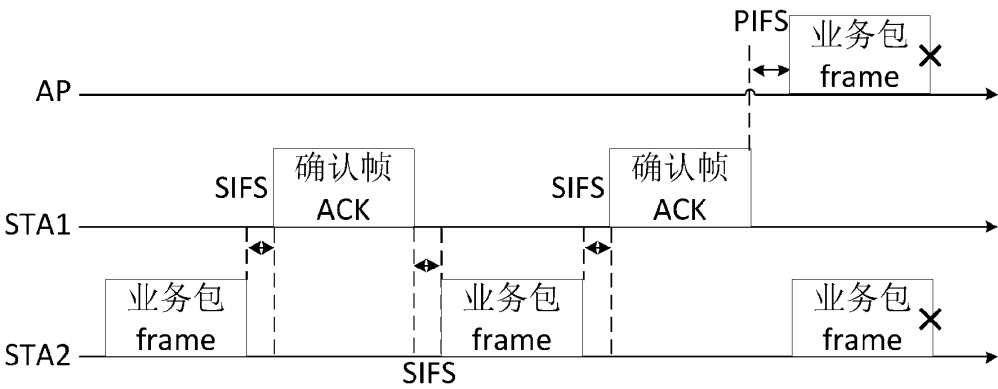


图 6B

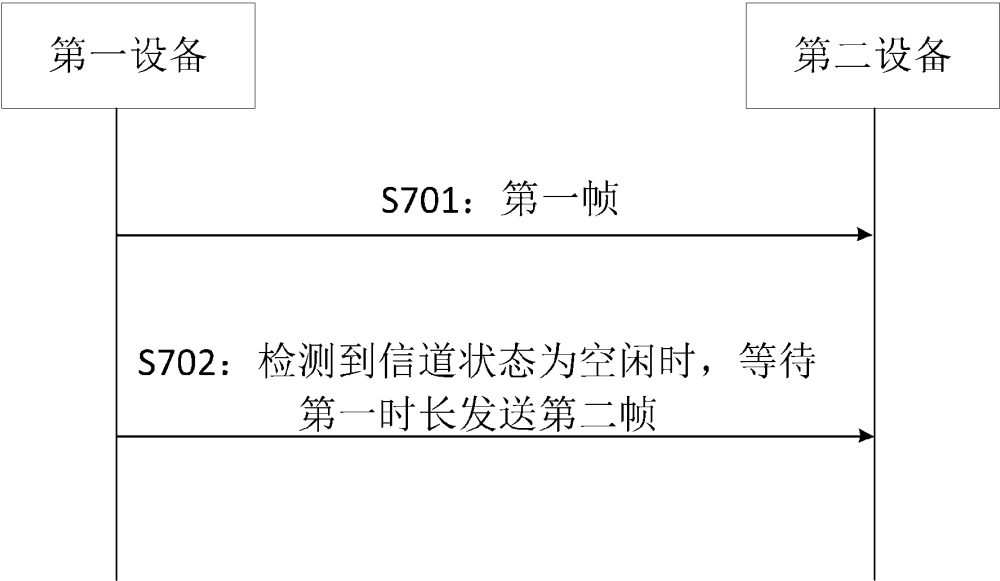


图 7

帧控制 frame control	时长 duration	目的地址 RA	FCS
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图 8

帧控制 frame control	时长 duration	地址1 address 1	地址2 address 2	地址3 address 3	序列控制 sequence control	地址4 address 4	服务质量控制 QoS control	高吞吐量控制 HT control	帧体 frame body	FCS
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图 9

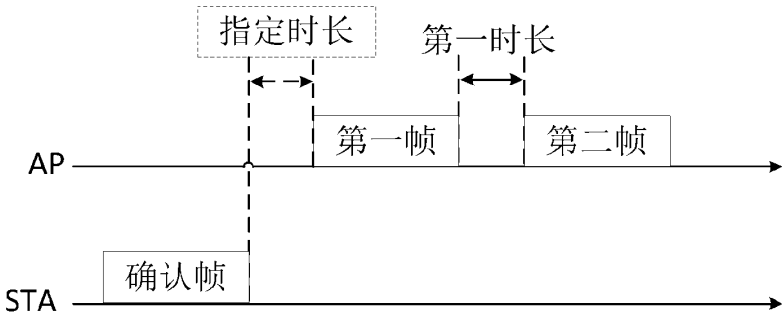


图 10A

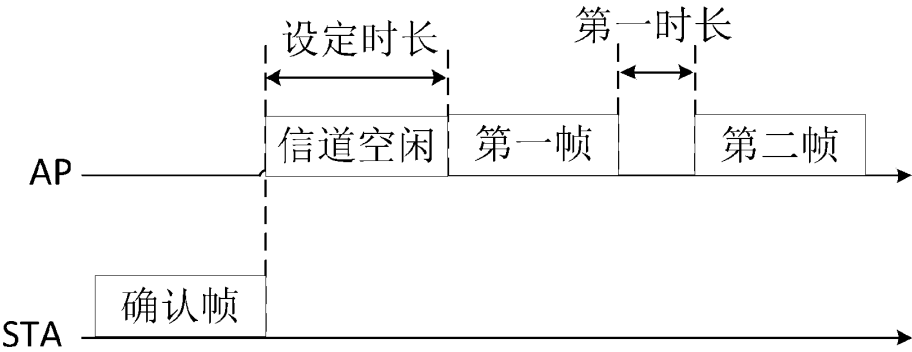


图 10B

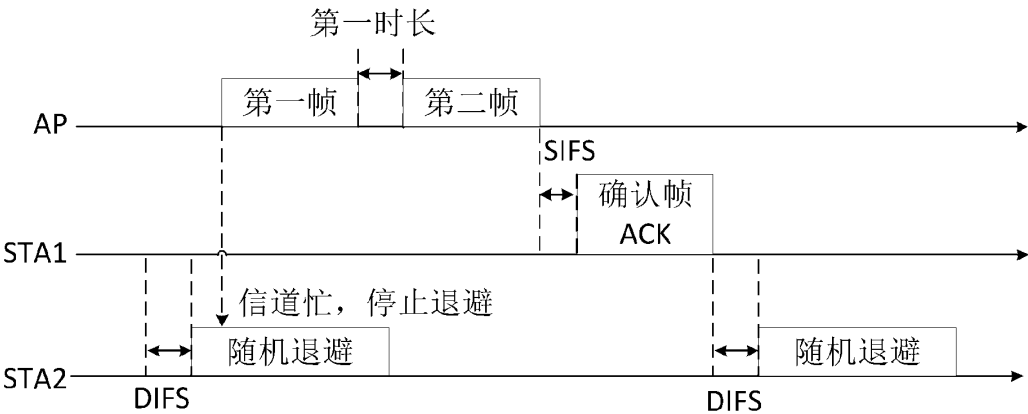


图 11A

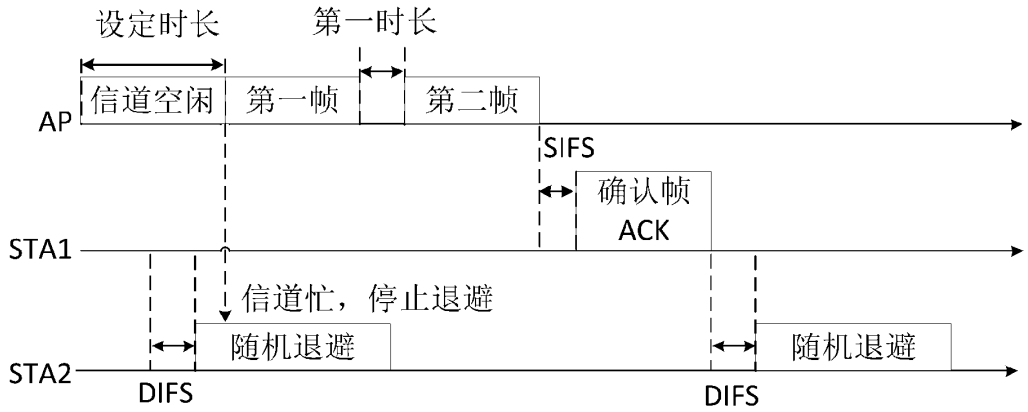


图 11B

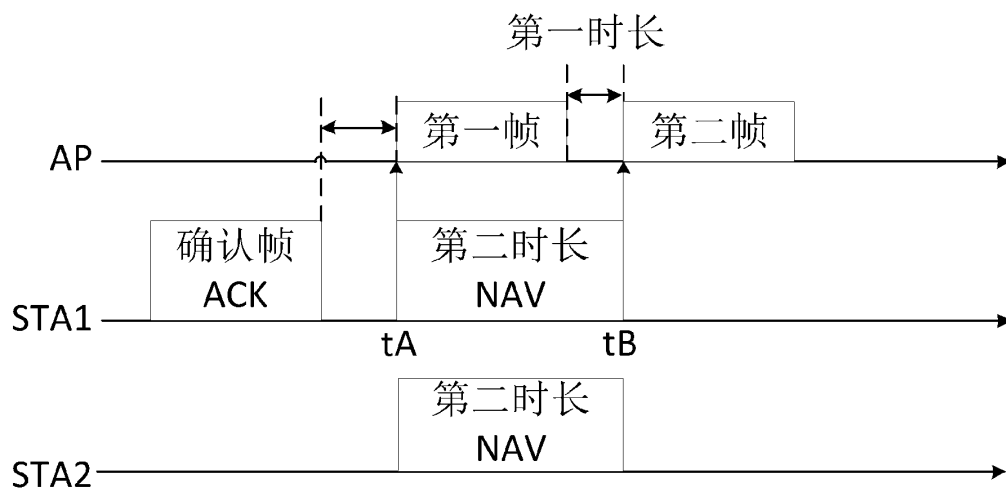


图 12

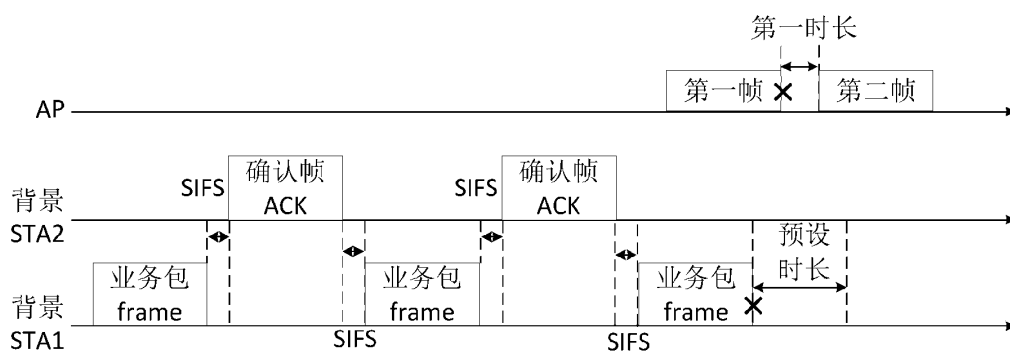


图 13

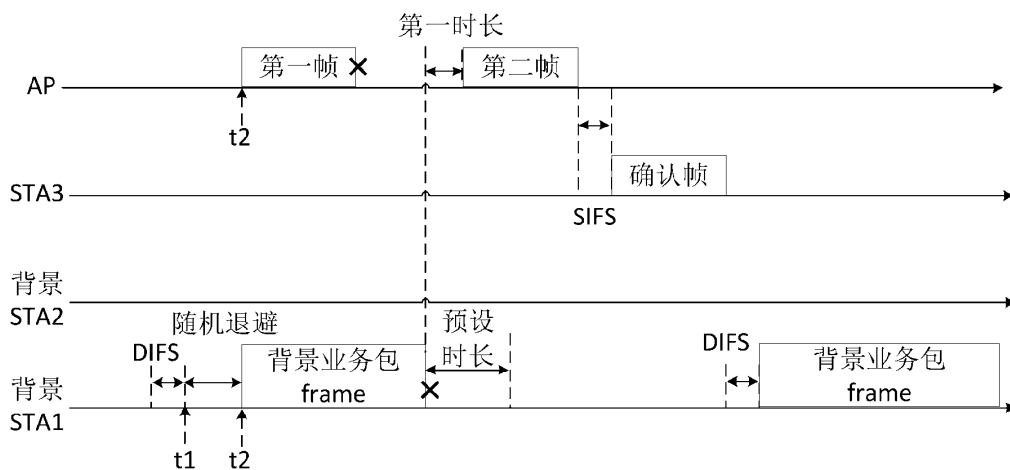


图 14

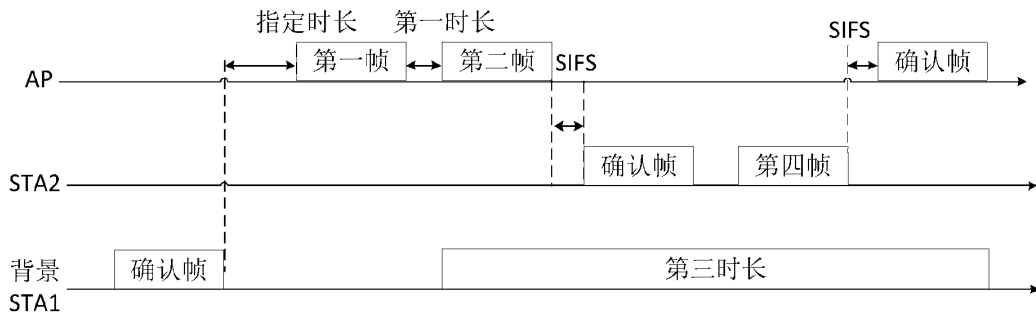


图 15A

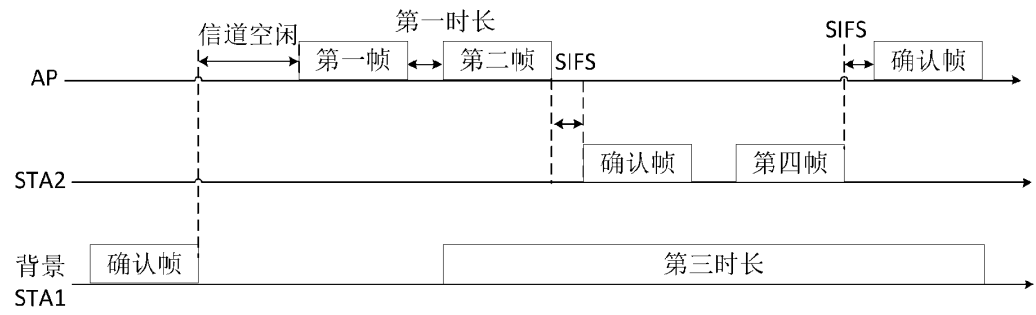


图 15B

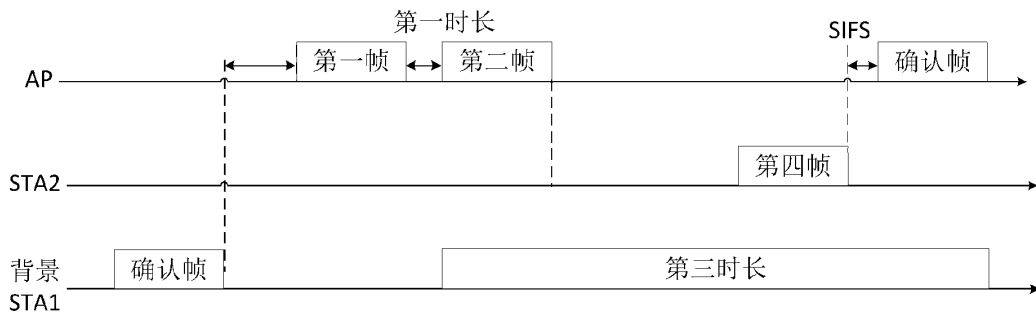


图 15C

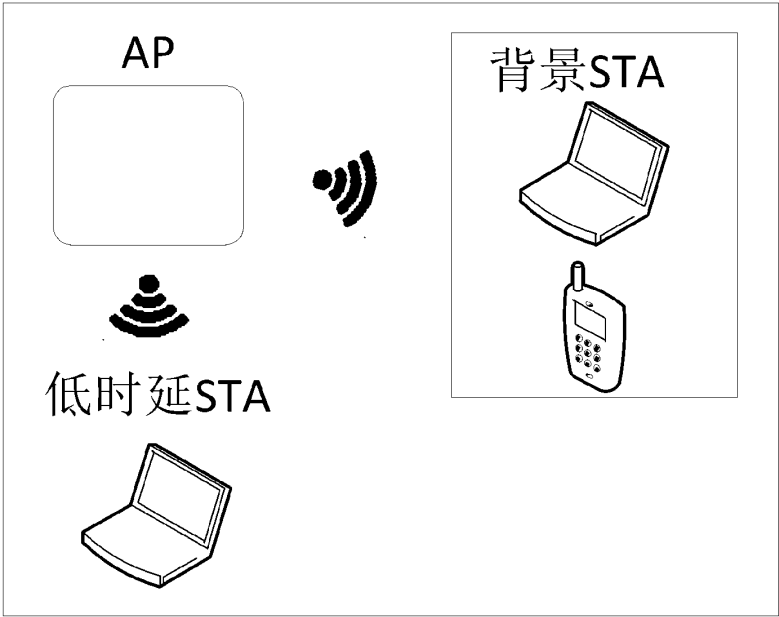


图 16

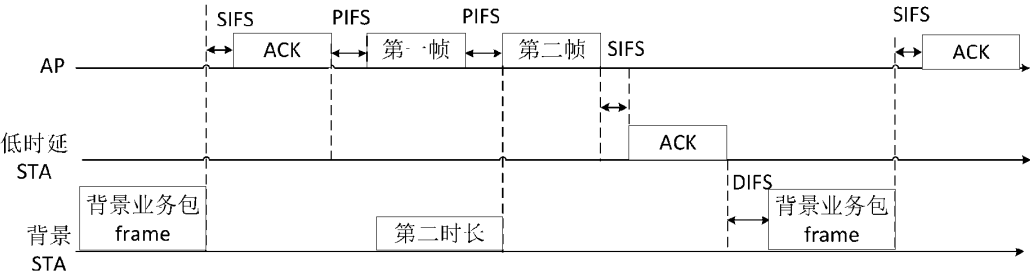


图 17

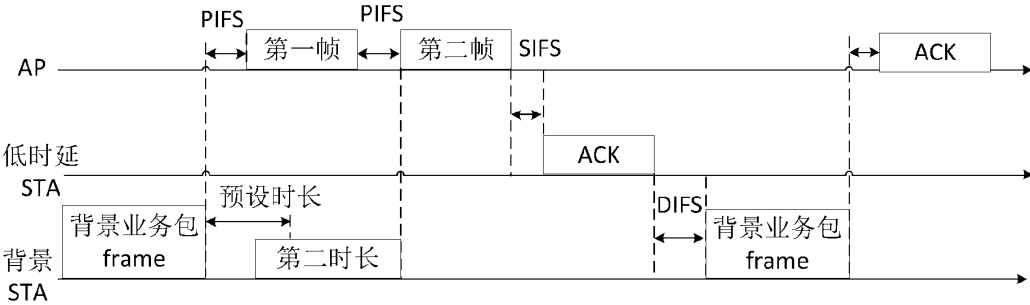


图 18

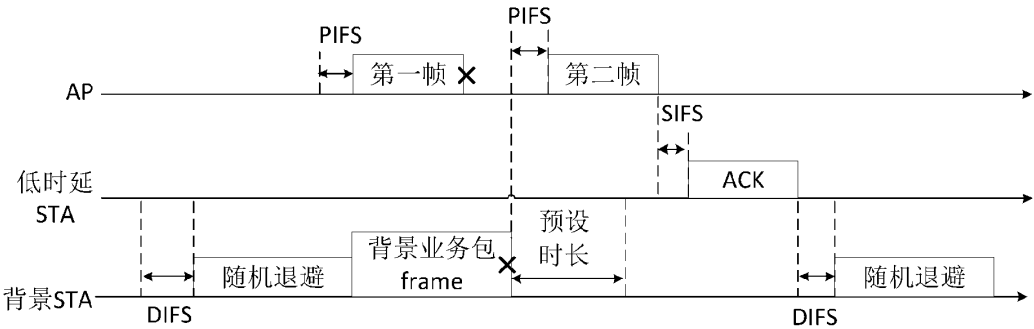


图 19

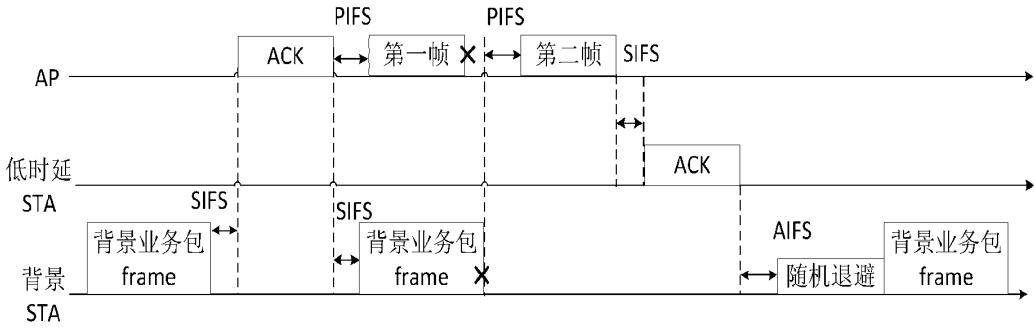


图 20

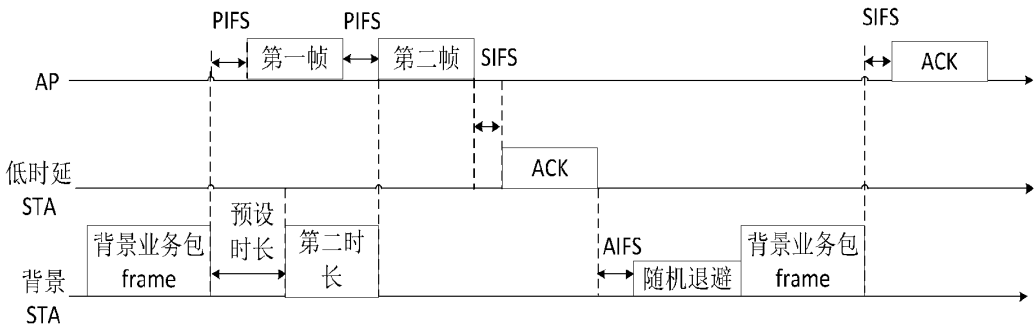


图 21

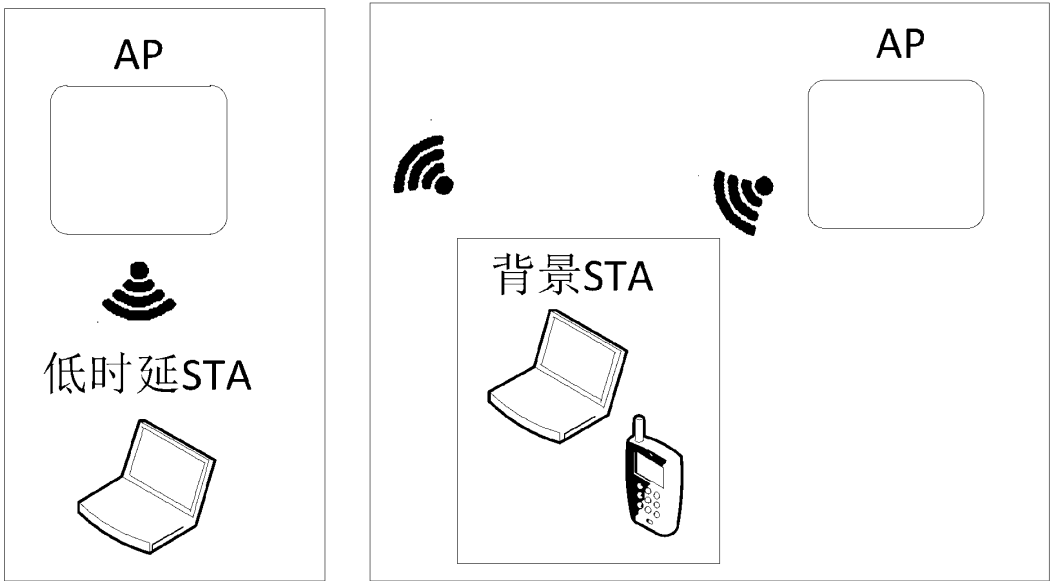


图 22

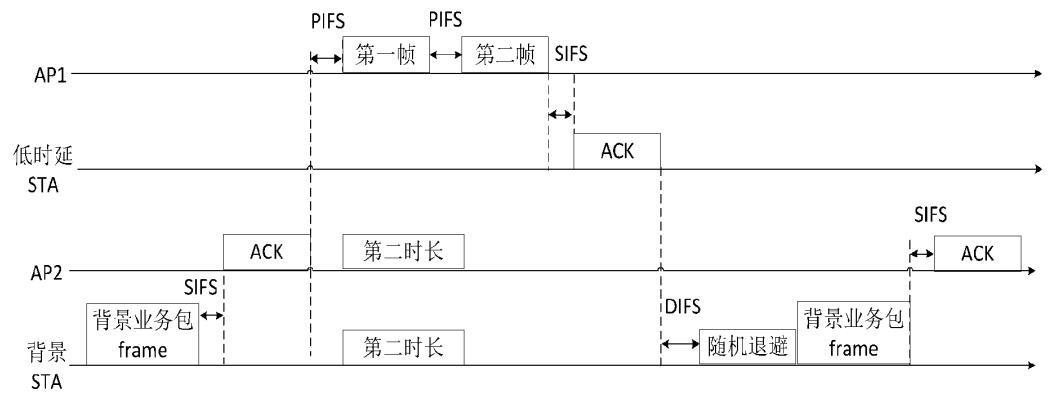


图 23

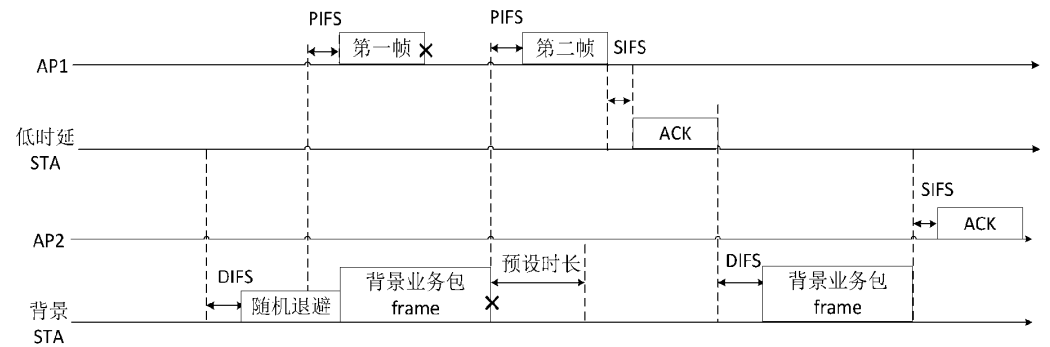
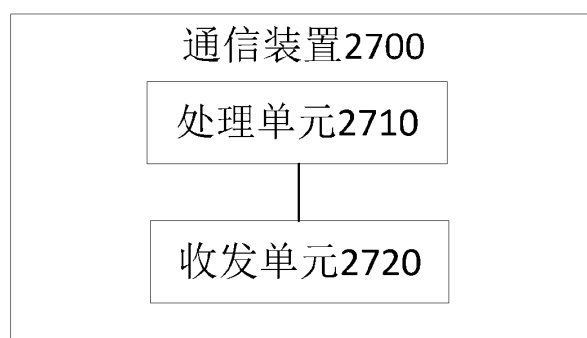
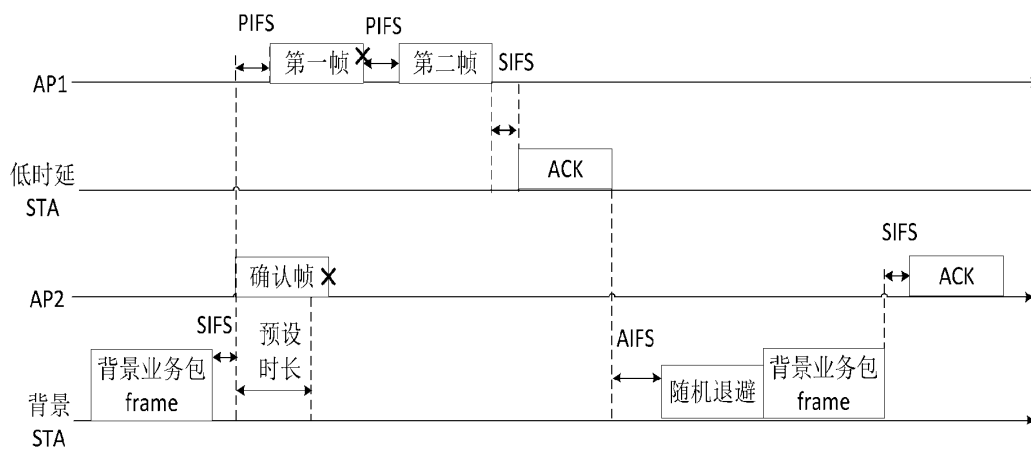
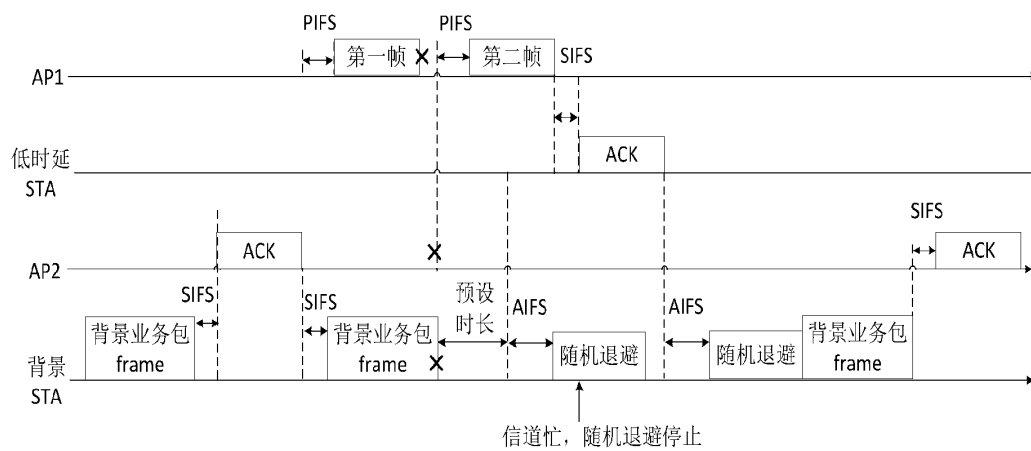


图 24



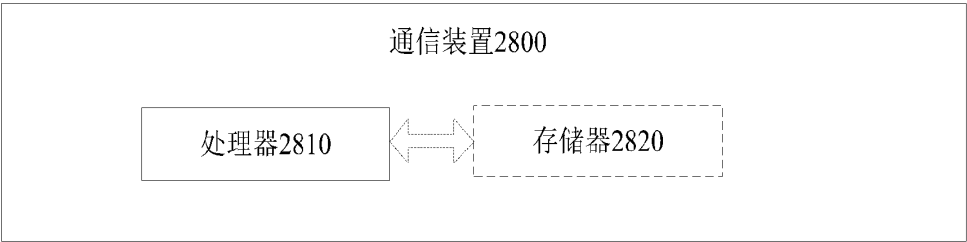


图 28

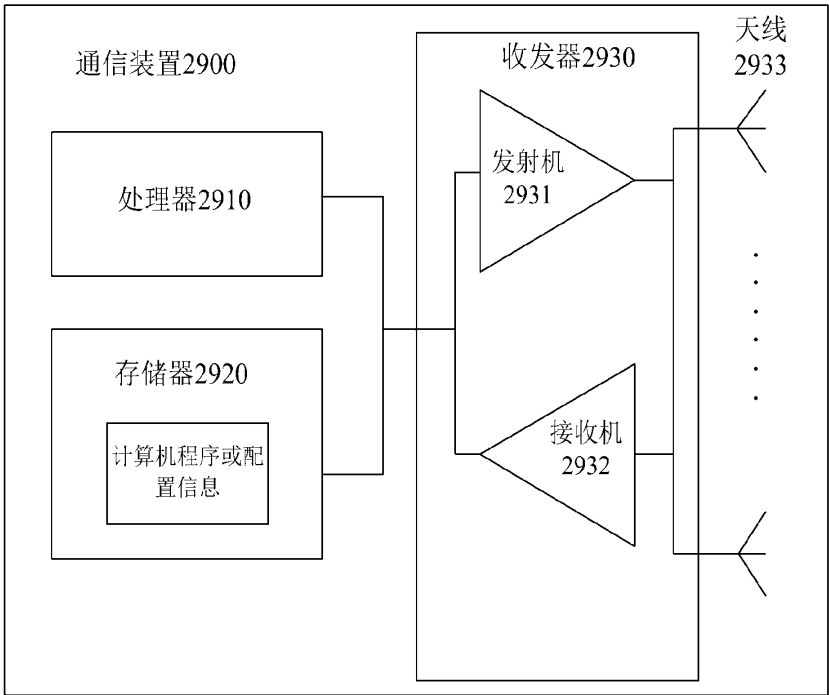


图 29

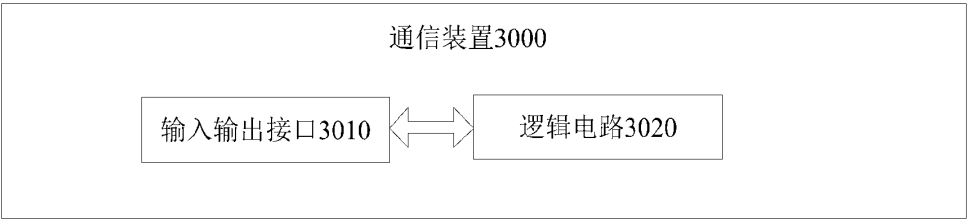


图 30