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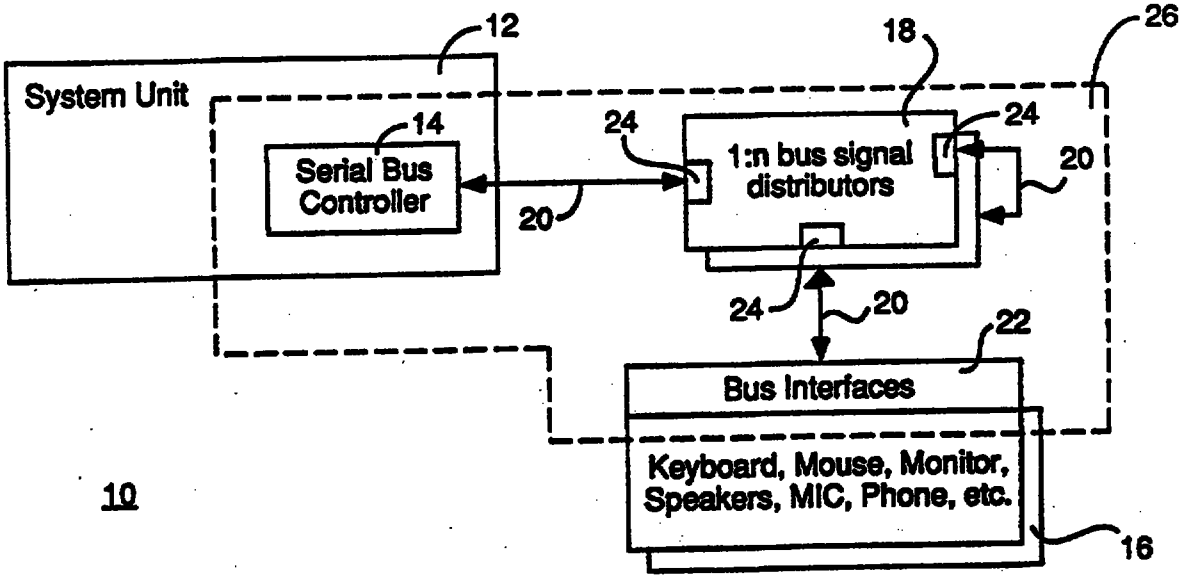
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(54) Title: HIERARCHICAL SERIAL BUS ASSEMBLY



(57) Abstract

Circuitry and complementary logic are provided to a bus controller (14), a number of 1:n bus signal distributors (18), and a number of bus interfaces (22) of a hierarchical bus assembly (26) for electrically representing data and control states to each other. The hierarchical serial bus assembly (26) is used to serially interface a number of isochronous and asynchronous peripherals (16) to the system unit (12) of a computer system (10). The serial bus elements are interconnected to each other using low cost two wire signal cable. Electrical signals are propagated between the serial bus elements in a differential manner. These circuitry and complementary logic of the serial bus elements jointly implement inference of data and control states from the states and/or durations of the propagating electrical signals.