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(74) Agent: **WILLIAMS, Julian**; IBM United Kingdom Limited, Intellectual Property Law, Hursley Park, Winchester Hampshire SO21 2JN (GB).

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(71) Applicant (for all designated States except US): **INTERNATIONAL BUSINESS MACHINES CORPORATION** [US/US]; New Orchard Road, Armonk, New York 10504 (US).

(71) Applicant (for MG only): **IBM UNITED KINGDOM LIMITED** [GB/GB]; PO Box 41, North Harbour, Portsmouth Hampshire PO6 3AU (GB).

(72) Inventors; and

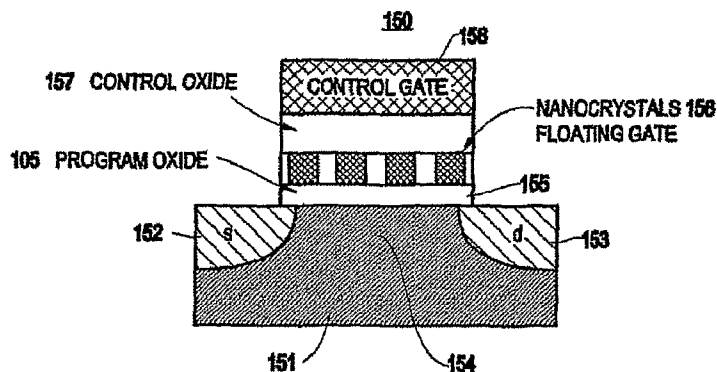
(75) Inventors/Applicants (for US only): **BLACK, Charles** [US/US]; 1161 York Avenue, 6A, New York, New York 10021 (US). **GUARINI, Kathryn** [US/US]; 290 Alden Road, Yorktown Heights, New York 10598D (US).

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(57) Abstract: A floating gate (156) for a field effect transistor (150) (and method for forming the same and method of forming a uniform nanoparticle array), includes a plurality of discrete nanoparticles (156) in which at least one of a size, spacing, and density of the nanoparticles is one of templated and defined by a self-assembled material.

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