## ABSTRACT OF THE DISCLOSURE

There is provided a liquid crystal display control device which can display pictures in a magnification mode by using only a memory having low-speed access and a low storage capacity. When a video signal has intermediate resolution or less, the enlargement processing is performed by a frame memory 110, a line memory 111 and an enlargement processing control circuit 118. If the input operation and the output operation to and from the frame memory 110 are synchronized with each other, it is sufficient for the frame memory 110 to have a storage capacity of two lines. When the video signal has the same high resolution as a liquid crystal display panel 124, the video signal is output through a gate circuit 109 to a display timing generating circuit 120, and it is displayed in a through mode. In this case, no processing is performed by the frame memory 110 or the like.

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