

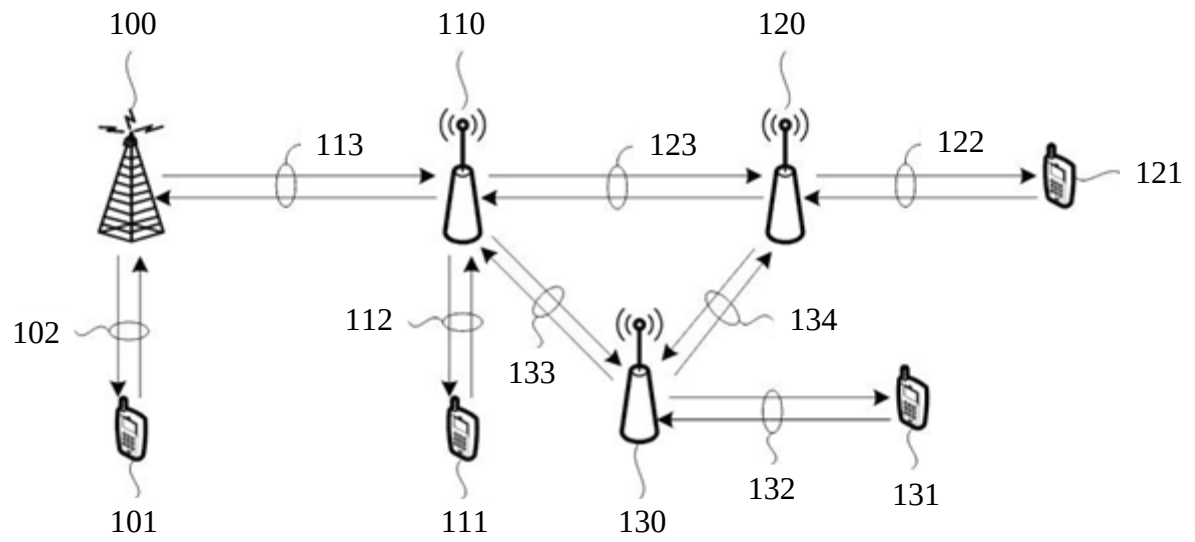


FIG. 1

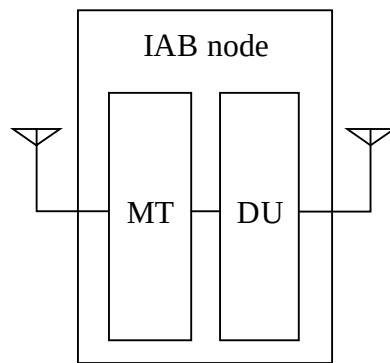


FIG. 2

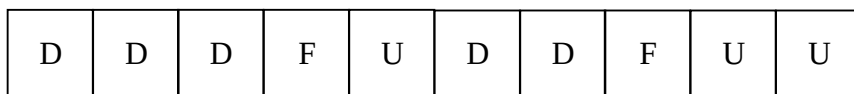


FIG. 3

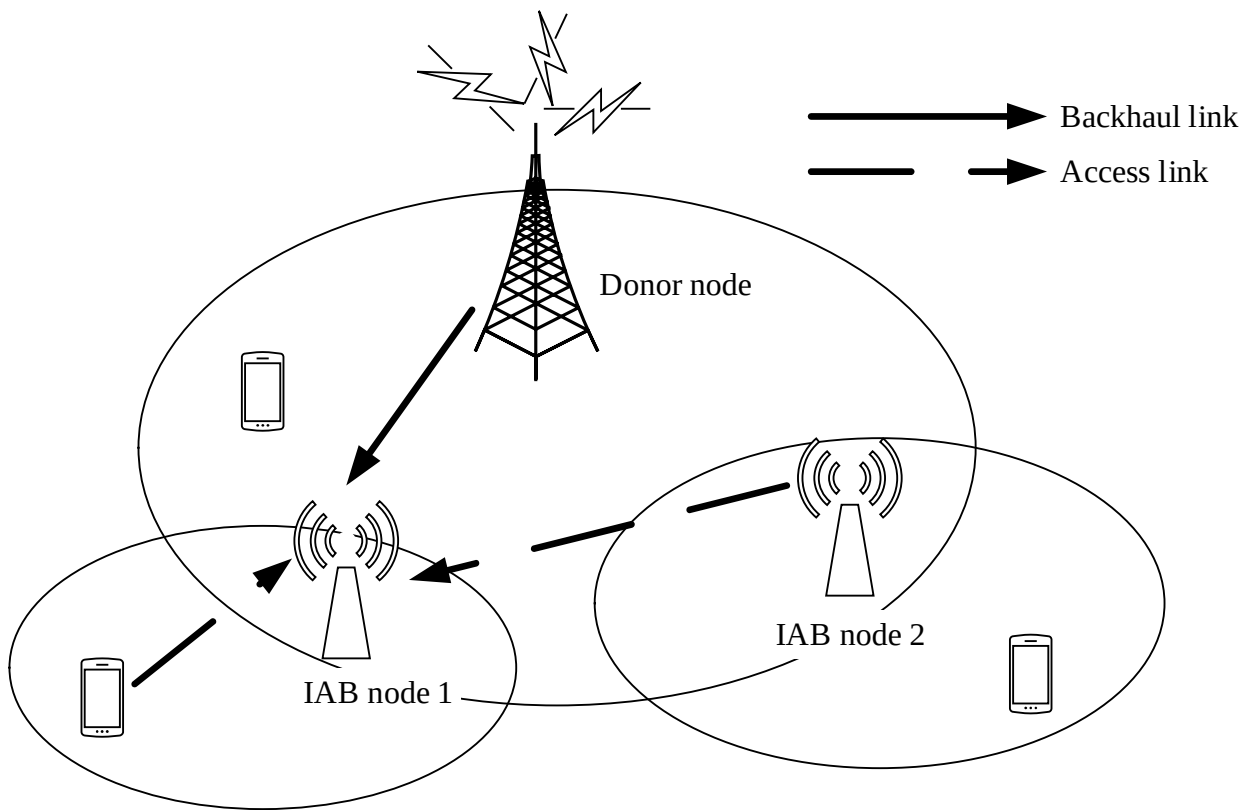


FIG. 4a

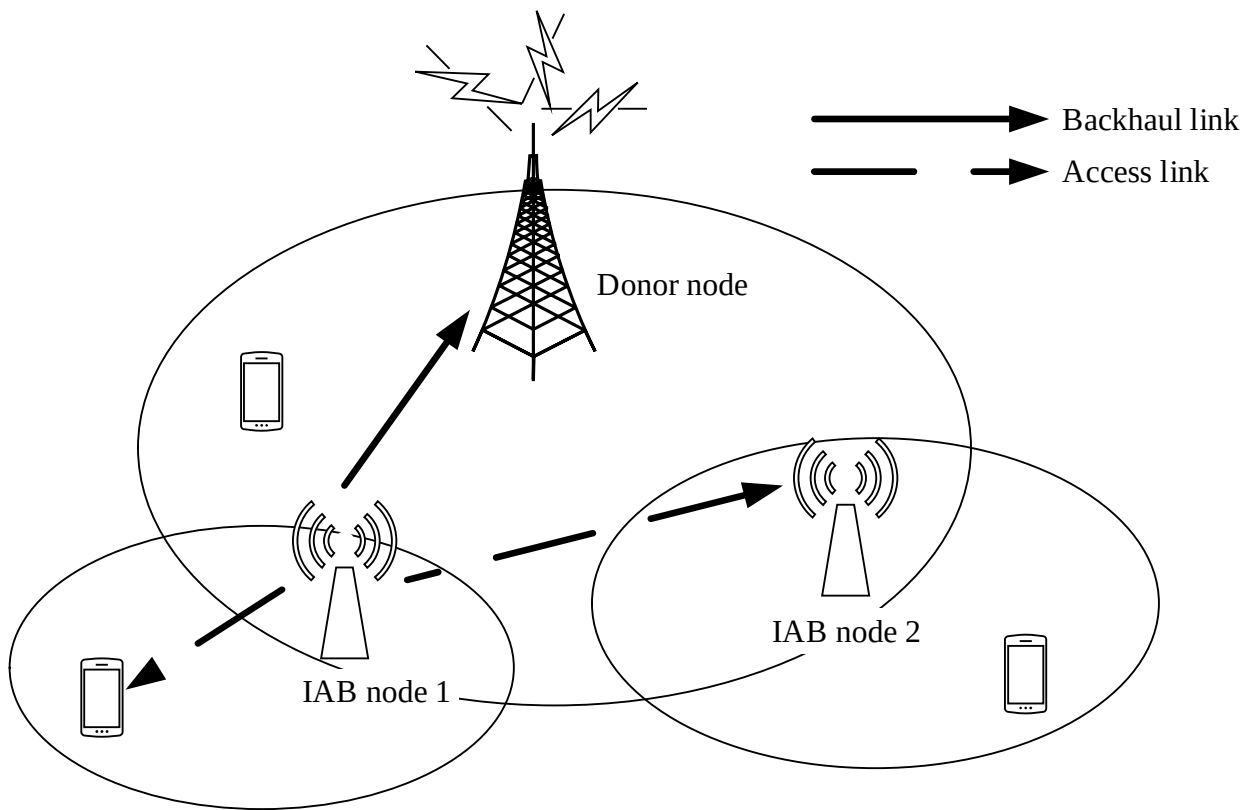


FIG. 4b

	0	1	2	3	4	5	6	7	8	9
	TDD period					TDD period				
First configuration	D	D	D	F	U	D	D	D	F	U
Second configuration	U	U	U	F	D	U	U	U	F	D

FIG. 5a

	0	1	2	3	4	5	6	7	8	9
	TDD period					TDD period				
First configuration	D	D	D	F	U	D	D	D	F	U
Second configuration		U	U		D		U	U		D

FIG. 5b

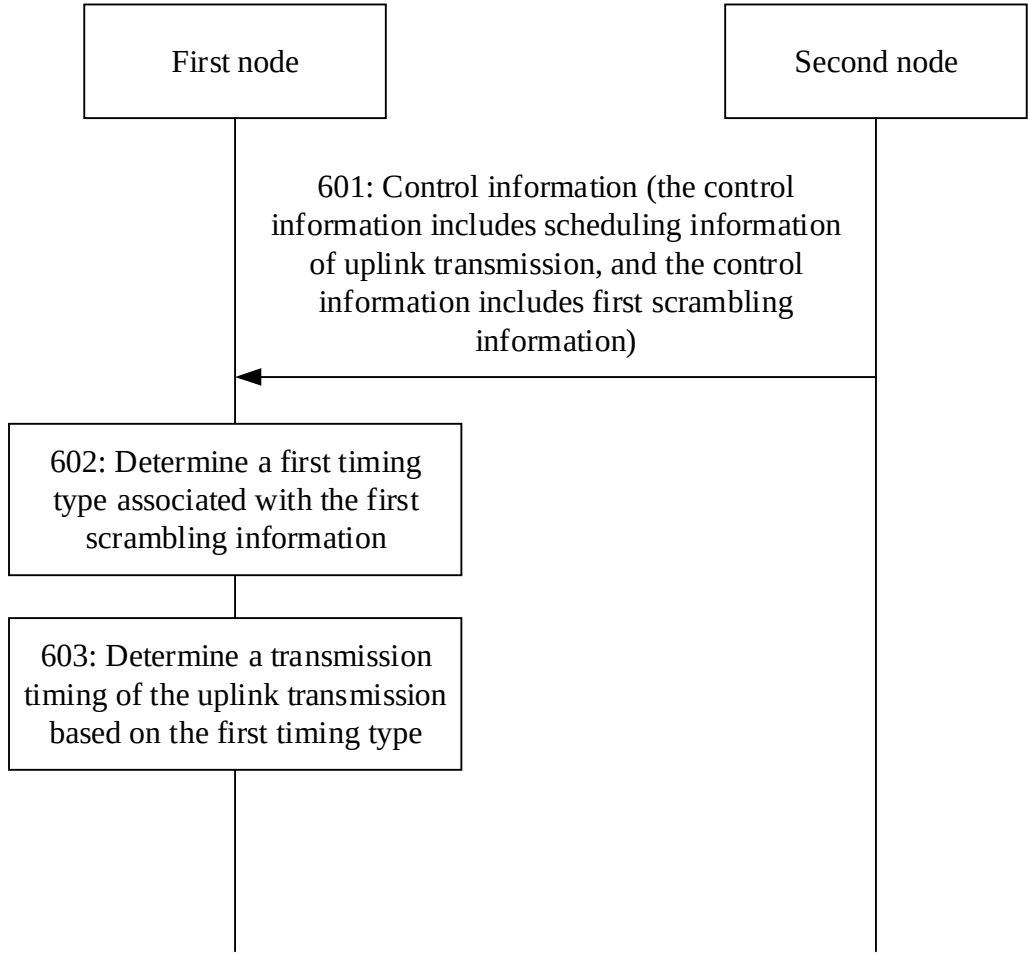


FIG. 6

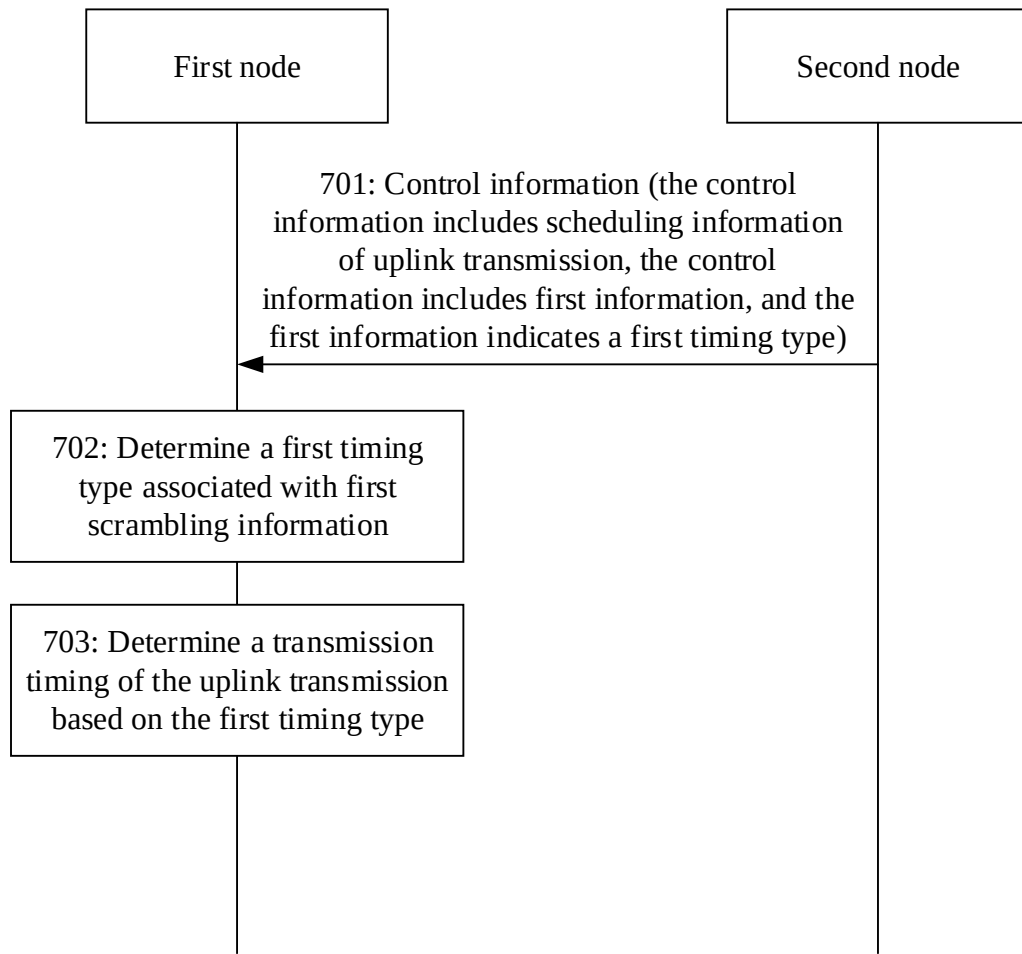


FIG. 7

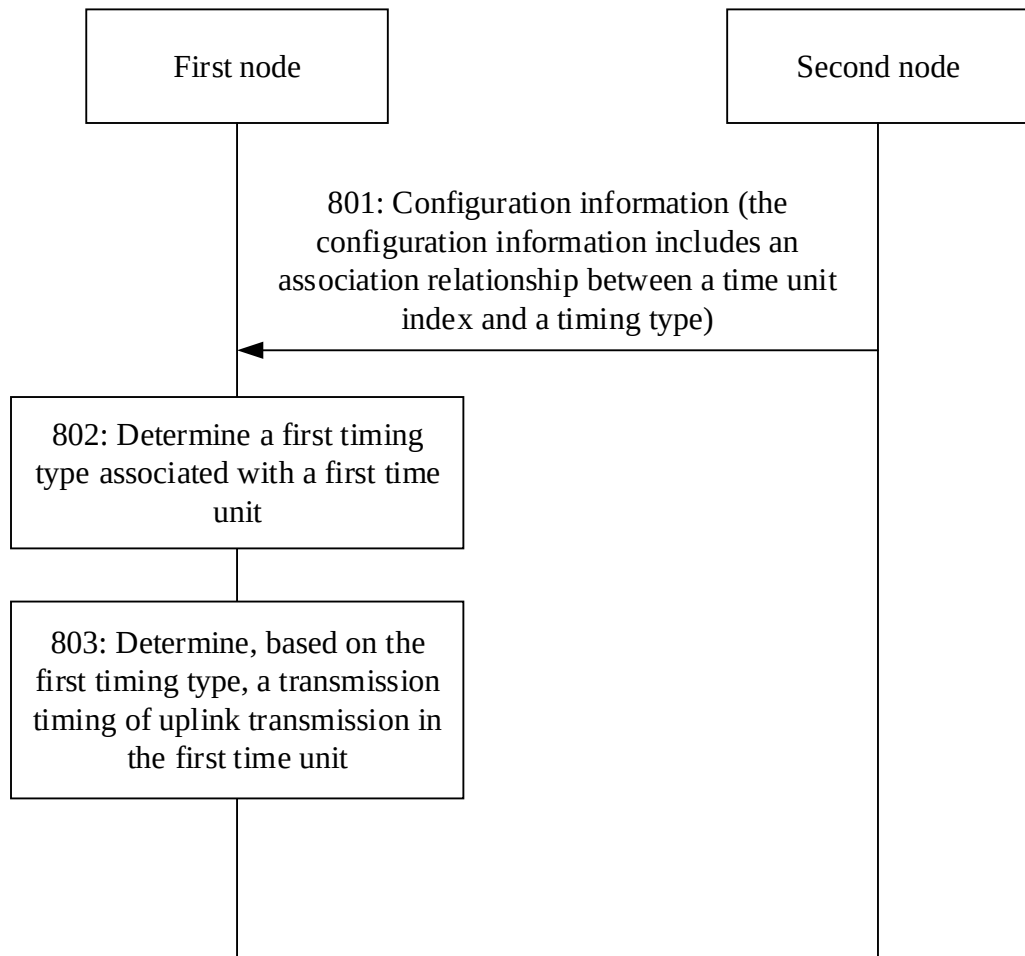


FIG. 8

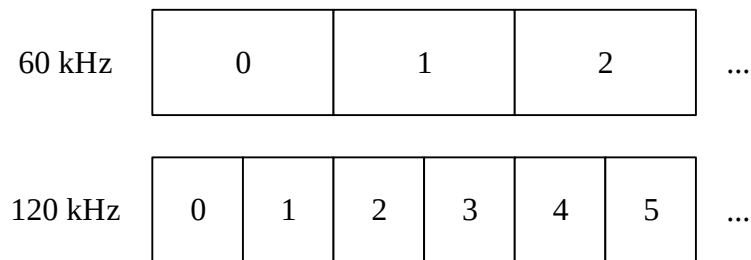


FIG. 9

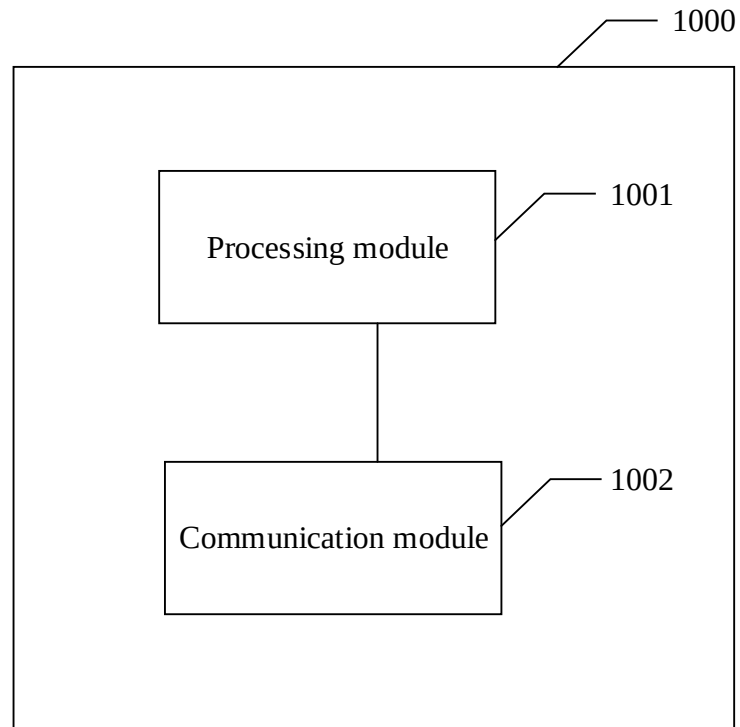


FIG. 10

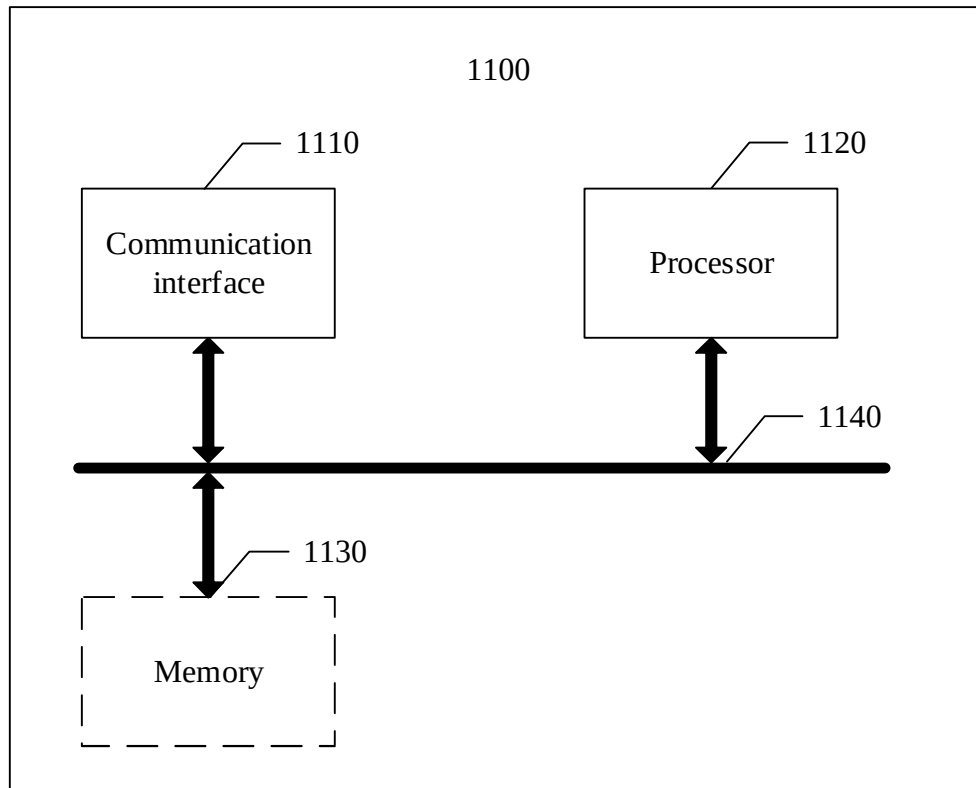


FIG. 11